# Memory FRAM

# 256 K (32 K × 8) Bit

# MB85R256F

#### **■ DESCRIPTIONS**

The MB85R256F is an FRAM (Ferroelectric Random Access Memory) chip in a configuration of 32,768 words  $\times$  8 bits, using the ferroelectric process and silicon gate CMOS process technologies for forming the nonvolatile memory cells.

The MB85R256F is able to retain data without using a back-up battery, as is needed for SRAM.

The memory cells used in the MB85R256F can be used for 10<sup>12</sup> read/write operations, which is a significant improvement over the number of read and write operations supported by Flash memory and E<sup>2</sup>PROM.

The MB85R256F uses a pseudo - SRAM interface compatible with conventional asynchronous SRAM.

#### **■ FEATURES**

Bit configuration : 32,768 words × 8 bits
 Read/write endurance : 10<sup>12</sup> times / byte

Data retention
 10 years (+85 °C), 95 years (+55 °C), over 200 years (+35 °C)

Operating power supply voltage: 2.7 V to 3.6 V

• Low power consumption : Operating power supply current 5 mA (Typ)

Standby current 5 µA (Typ)

Operation ambient temperature range: – 40 °C to + 85 °C

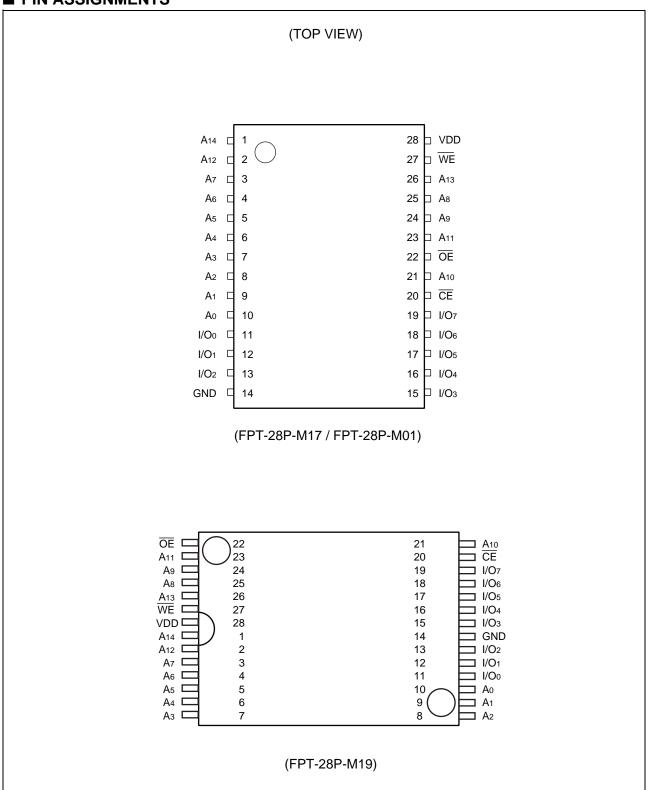
• Package : 28-pin plastic SOP (FPT-28P-M17)

28-pin plastic SOP (FPT-28P-M01) : 28-pin plastic TSOP(1) (FPT-28P-M19)

Both are RoHS compliant



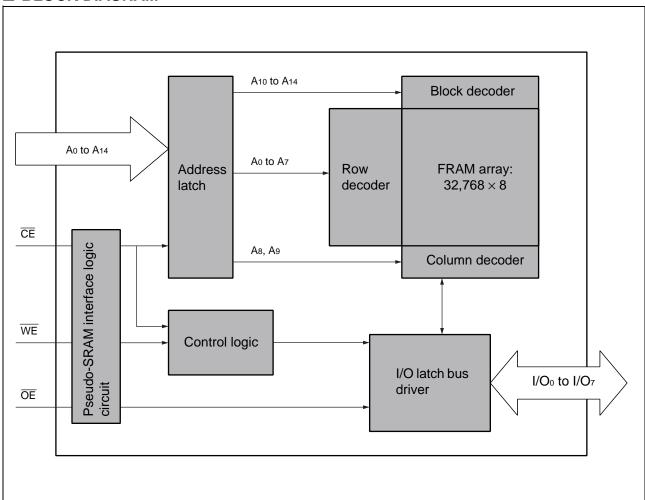
#### **■ PIN ASSIGNMENTS**



### **■ PIN FUNCTIONAL DESCRIPTIONS**

Pin no.	Pin name	Functional description
1 to 10, 21, 23 to 26	A <sub>0</sub> to A <sub>14</sub>	Address input pins
11 to 13, 15 to 19	I/O <sub>0</sub> to I/O <sub>7</sub>	Data input/output pins
20	CE	Chip enable input pin
27	WE	Write Enable input pin
22	ŌĒ	Output enable input pin
28	VDD	Supply Voltage pin
14	GND	Ground pin

#### **■ BLOCK DIAGRAM**



#### **■ FUNCTION LIST**

Operation mode	CE	WE	ŌĒ	I/O <sub>0</sub> to I/O <sub>7</sub>	Power supply current
	Н	×	×		0, "
Standby precharge	×	L	L	Hi-Z	Standby (Isa)
	×	Н	Н		(105)
	L	Y	Ł		
Latch address	Y	Н	L	_	_
	P	L	Н		
Write	L	L	Н	Data input	Operation (las)
Read	L	Н	L	Data output	Operation (IDD)

H: High level, L: Low level, ×: can be either H, L, → or , Hi-Z: High impedance, → : Latch address at falling edge

#### ■ ABSOLUTE MAXIMUM RANGES

Parameter	Symbol	Rat	Unit	
rarameter	Symbol	Min	Max	Onn
Power supply voltage*	V <sub>DD</sub>	- 0.5	+ 4.0	V
Input voltage*	Vin	- 0.5	V <sub>DD</sub> + 0.5	V
Output voltage*	Vоит	- 0.5	V <sub>DD</sub> + 0.5	V
Operation ambient temperature	TA	- 40	+ 85	°C
Storage temperature	Tstg	<b>– 55</b>	+ 125	°C

<sup>\*:</sup> These parameters are based on the condition that Vss is 0 V.

WARNING: Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

#### ■ RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol		Unit		
raiailletei	Symbol	Min	Тур	Max	Onit
Power supply voltage*	V <sub>DD</sub>	2.7	3.3	3.6	V
High level input voltage*	ViH	$V_{DD} \times 0.8$	_	V <sub>DD</sub> + 0.5	V
Low level input voltage*	VıL	- 0.5	_	+ 0.6	V
Operation ambient temperature	TA	- 40	_	+ 85	°C

 $<sup>\</sup>mbox{\ensuremath{^{*}}}$  : These parameters are based on the condition that  $\mbox{\ensuremath{Vss}}$  is 0 V.

WARNING: The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.

Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.

No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their representatives beforehand.

#### **■ ELECTRICAL CHARACTERISTICS**

#### 1. DC Characteristics

(within recommended operating conditions)

Parameter	Symbol	Conditions		Value		Unit
raiametei	Syllibol	Conditions	Min	Тур	Max	Onn
Input leakage current		$V_{IN} = 0 V to V_{DD}$	_	_	10	μΑ
Output leakage current	<b>I</b> LO	$V_{OUT} = 0 \text{ V to } V_{DD},$ $\overline{CE} = V_{IH} \text{ or } \overline{OE} = V_{IH}$	_	_	10	μΑ
Operating power supply current*1	loo	$\overline{\text{CE}} = 0.2 \text{ V},$ Other inputs = V <sub>DD</sub> - 0.2 V/0.2 V, t <sub>RC</sub> (Min), lout = 0 mA	_	5	10	mA
Standby current*2	IsB	$\overline{CE}, \overline{WE}, \overline{OE} \ge V_{DD}$	_	5	50	μΑ
High level output voltage	Vон	lон = − 2.0 mA	$V_{DD} \times 0.8$	_	_	V
Low level output voltage	Vol	IoL = 2.0 mA	_	_	0.4	V

<sup>\*1:</sup> During the measurement of IDD, the Address and Data In were taken to only change once per active cycle. Iout: output current

#### 2. AC Characteristics

• AC Characteristics Test Condition

Power supply voltage : 2.7 V to 3.6 V

Operation ambient temperature: - 40 °C to +85 °C

Input voltage amplitude : 0.3 V to 2.7 V

Input rising time : 10 ns
Input falling time : 10 ns
Input evaluation level : Vpd/2
Output evaluation level : Vpd/2
Output Load Capacitance: 100 pF

#### (1) Read cycle

Poromotor	Cumbal	Va	Value		
Parameter	Symbol	Min	Max	Unit	
Read cycle time	<b>t</b> RC	150			
CE active time	<b>t</b> ca	70	500		
Read pulse width	<b>t</b> RP	70	500		
Precharge time	<b>t</b> PC	80	_		
Address setup time	<b>t</b> as	0		ne	
Address hold time	<b>t</b> ah	25	_	ns	
CE access time	<b>t</b> ce	_	70		
OE access time	<b>t</b> oe	_	70		
CE output floating time	<b>t</b> HZ		25		
OE output floating time	tонz	_	25		

<sup>\*2:</sup> All pins other than setting pins shall be input at the CMOS level voltages such as  $H \ge V_{DD}$ ,  $L \le 0 \text{ V}$ .

(2) Write cycle

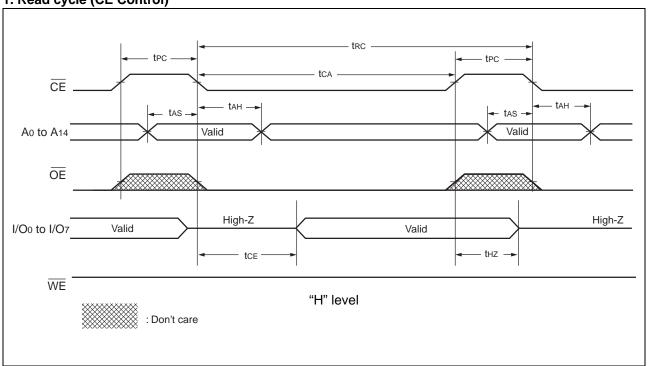
Parameter	Symbol	Va	Unit	
Farameter	Symbol	Min	Max	Offic
Write cycle time	twc	150	_	
CE active time	<b>t</b> ca	70	500	
Write pulse width	twp	70	500	
Precharge time	<b>t</b> PC	80	_	nc
Address setup time	<b>t</b> AS	0		ns
Address hold time	<b>t</b> AH	25		
Data setup time	tos	50	_	
Data hold time	tон	0	_	

3. Pin Capacitance

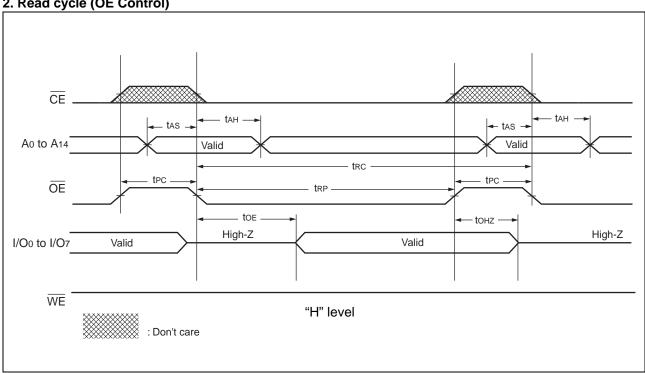
Parameter	Symbol	Conditions		Unit		
rarameter	Syllibol	Conditions	Min	Тур	Max	Oilit
Input capacitance	Cin	$V_{DD} = V_{IN} = V_{OUT} = 0 V,$	_	_	10	pF
Output capacitance	Соит	$f = 1 \text{ MHz}, T_A = +25 ^{\circ}\text{C}$	_	_	10	pF

#### **■ TIMING DIAGRAM**

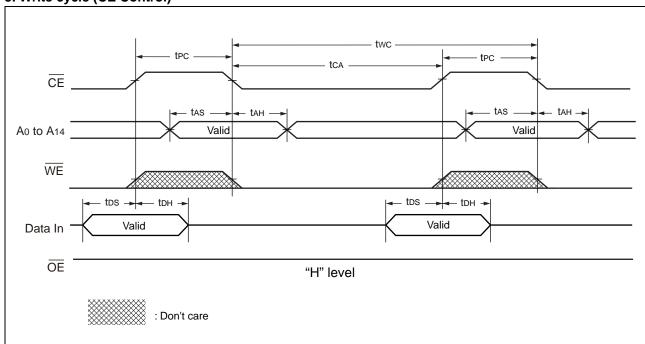
### 1. Read cycle (CE Control)



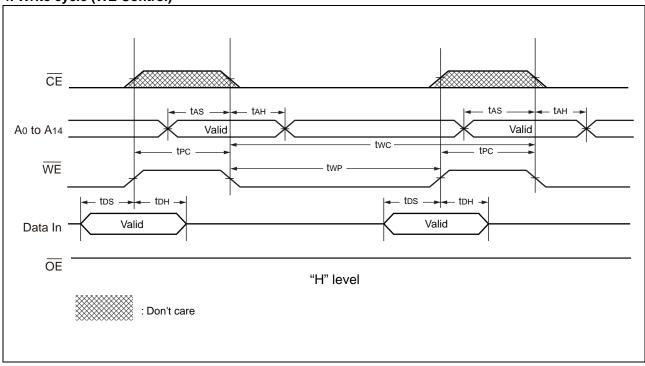
### 2. Read cycle (OE Control)



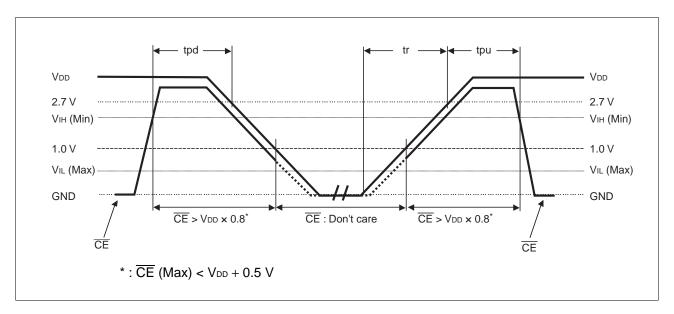
### 3. Write cycle (CE Control)



### 4. Write cycle (WE Control)



#### **■ POWER ON/OFF SEQUENCE**



Parameter	Symbol		Unit		
Farameter	Symbol	Min	Тур	Max	Oille
CE level hold time at power OFF	tpd	80	_	_	ns
CE level hold time at power ON	tpu	80	_	_	ns
Power supply rising time	tr	0.05		200	ms

If the device does not operate within the specified conditions of read cycle, write cycle or power on/off sequence, memory data can not be guaranteed.

#### **■ FRAM CHARACTERISTICS**

Item	Min	Max	Unit	Parameter
Read/Write Endurance*1	1012	_	Times/byte	Operation Ambient Temperature T <sub>A</sub> = +85 °C
	10	_		Operation Ambient Temperature T <sub>A</sub> = +85 °C
Data Retention*2	95	_	Years	Operation Ambient Temperature T <sub>A</sub> = +55 °C
	≥ 200	_		Operation Ambient Temperature T <sub>A</sub> = +35 °C

<sup>\*1:</sup> Total number of reading and writing defines the minimum value of endurance, as an FRAM memory operates with destructive readout mechanism.

#### **■ NOTES ON USE**

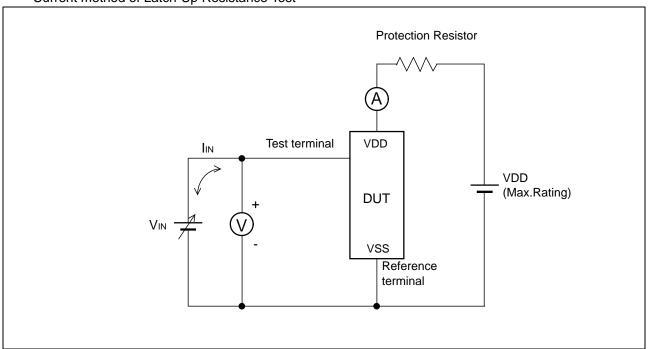
After the IR reflow completed, it is not guaranteed to save the data written prior to the IR reflow.

<sup>\*2 :</sup> Minimun values define retention time of the first reading/writing data right after shipment, and these values are calculated by qualification results.

#### **■ ESD AND LATCH-UP**

Test	DUT	Value
ESD HBM (Human Body Model) JESD22-A114 compliant		≥  2000 V
ESD MM (Machine Model) JESD22-A115 compliant		≥  200 V
ESD CDM (Charged Device Model) JESD22-C101 compliant		≥  1000 V
Latch-Up (I-test) JESD78 compliant	MB85R256FPF-G-BNDE1 MB85R256FPFCN-G-BNDE1	_
Latch-Up (V <sub>supply</sub> overvoltage test) JESD78 compliant		_
Latch-Up (Current Method) Proprietary method		≥  300 mA
Latch-Up (C-V Method) Proprietary method		_

#### • Current method of Latch-Up Resistance Test

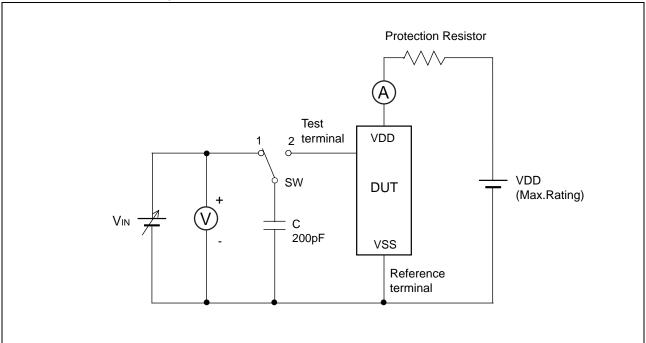


Note: The voltage V<sub>IN</sub> is increased gradually and the current I<sub>IN</sub> of 300 mA at maximum shall flow. Confirm the latch up does not occur under I<sub>IN</sub> = ± 300 mA.

In case the specific requirement is specified for I/O and  $I_{IN}$  cannot be 300 mA, the voltage shall be increased to the level that meets the specific requirement.

# MB85R256F

• C-V method of Latch-Up Resistance Test



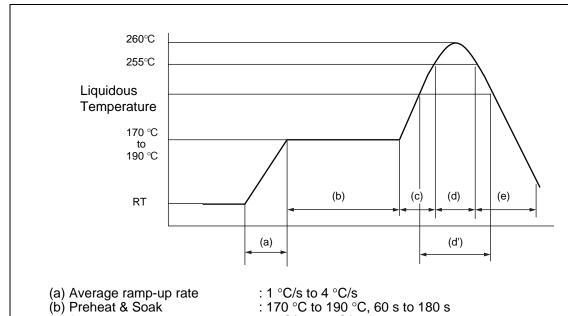
Note: Charge voltage alternately switching 1 and 2 approximately 2 sec interval. This switching process is considered as one cycle.

Repeat this process 5 times. However, if the latch-up condition occurs before completing 5 times, this test must be stopped immediately.

#### ■ REFLOW CONDITIONS AND FLOOR LIFE

Item	Condition				
Method	IR (infrared r	eflow) , Convection			
Times	2				
	Before unpacking	Please use within 2 years after production.			
	From unpacking to 2nd reflow	Within 8 days			
Floor life	In case over period of floor life	Baking with 125 °C+/-3 °C for 24hrs+2hrs/-0hrs is required. Then please use within 8 days. (Please remember baking is up to 2 times)			
Floor life condition	Between 5 °C and 30 °C and also below 70%RH required. (It is preferred lower humidity in the required temp range.)				

#### **Reflow Profile**



(a) Average ramp-up rate

(b) Preheat & Soak

(e) Cooling

(c) Average ramp-up rate

(d) Peak temperature

(d') Liquidous temperature

: 1 °C/s to 4 °C/s

: Temperature 260 °C Max; 255 °C within 10 s

: Up to 230 °C within 40 s or

Up to 225 °C within 60 s or Up to 220 °C within 80 s

: Natural cooling or forced cooling

Note: Temperature on the top of the package body is measured.

# MB85R256F

#### **■ RESTRICTED SUBSTANCES**

This product complies with the regulations below (Based on current knowledge as of November 2011).

- EU RoHS Directive (2002/95/EC)
- China RoHS (Administration on the Control of Pollution Caused by Electronic Information Products (电子信息产品污染控制管理办法))
- Vietnam RoHS (30/2011/TT-BCT)

Restricted substances in each regulation are as follows.

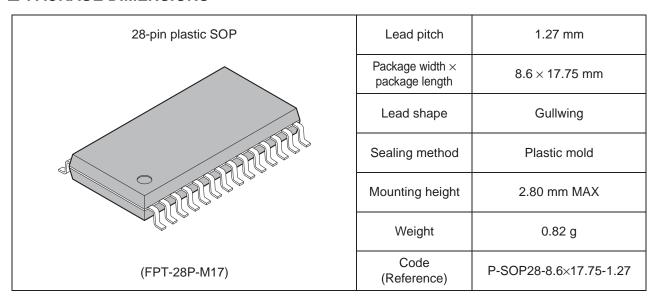
Substances	Threshold	Contain status*
Lead and its compounds	1,000 ppm	0
Mercury and its compounds	1,000 ppm	0
Cadmium and its compounds	100 ppm	О
Hexavalent chromium compound	1,000 ppm	О
Polybrominated biphenyls (PBB)	1,000 ppm	0
Polybrominated diphenyl ethers (PBDE)	1,000 ppm	О

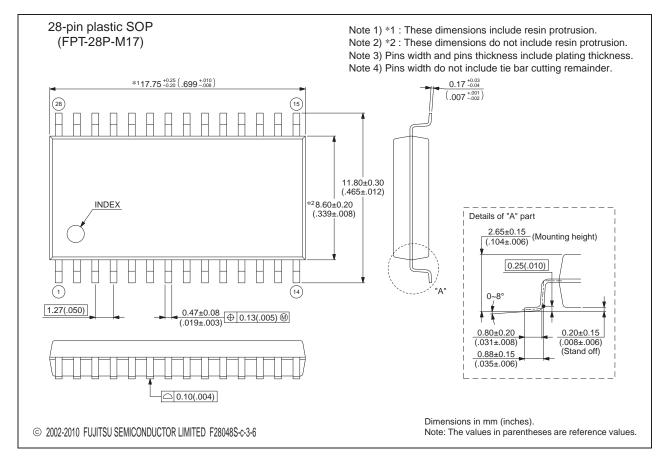
<sup>\*:</sup> The mark of "O" shows below a threshold value.

### **■ ORDERING INFORMATION**

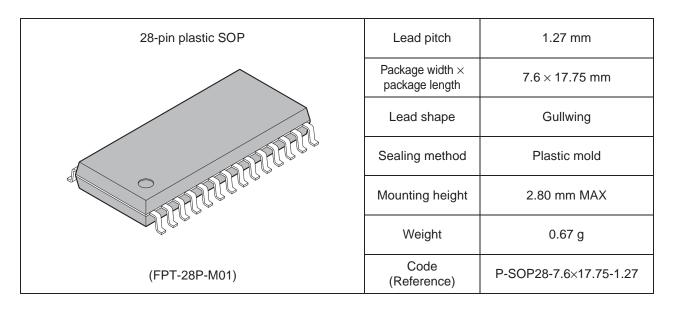
Part number	Package	Shipping form	Minimum shipping quantity
MB85R256FPF-G-BNDE1	28-pin plastic SOP (FPT-28P-M17)	Tube	1
MB85R256FPFCN-G-BNDE1	28-pin plastic TSOP(1) (FPT-28P-M19)	Tray	1
MB85R256FPF-G-BND-ERE1	28-pin plastic SOP (FPT-28P-M17)	Embossed carrier tape	1000
MB85R256FPNF-G-JNE2	28-pin plastic SOP (FPT-28P-M01)	Tube	1
MB85R256FPNF-G-JNERE2	28-pin plastic SOP (FPT-28P-M01)	Embossed carrier tape	1000

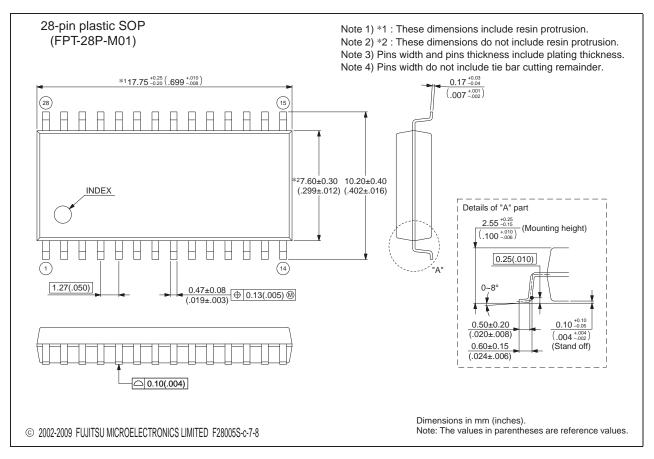
#### **■ PACKAGE DIMENSIONS**





(Continued)

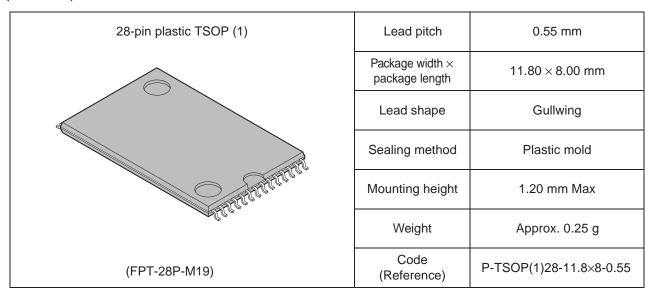


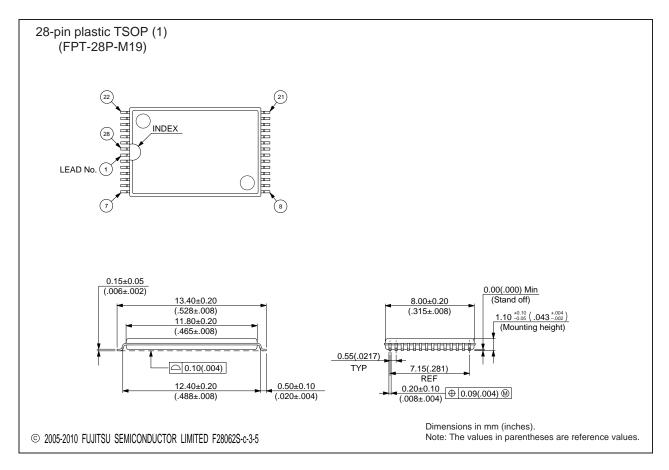


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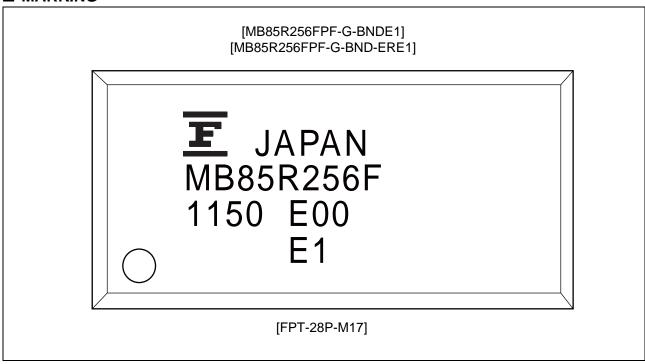
# MB85R256F

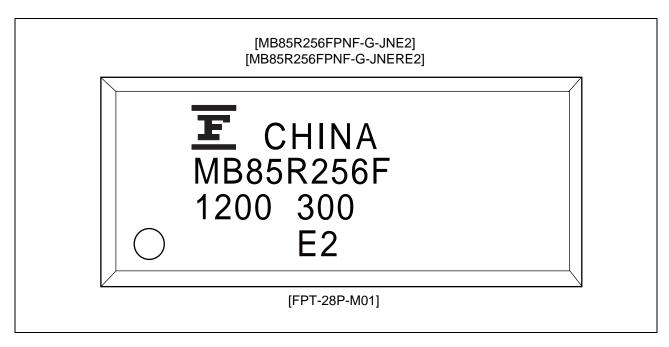
#### (Continued)

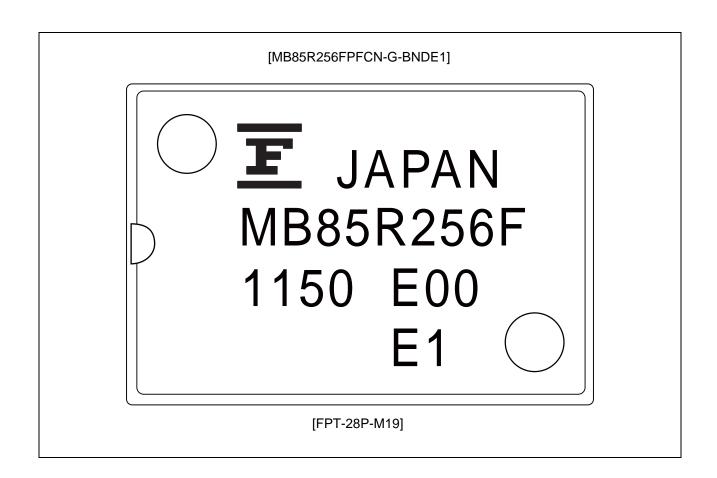




#### **■ MARKING**





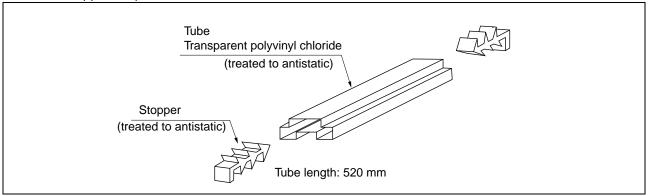


#### **■ PACKING INFORMATION**

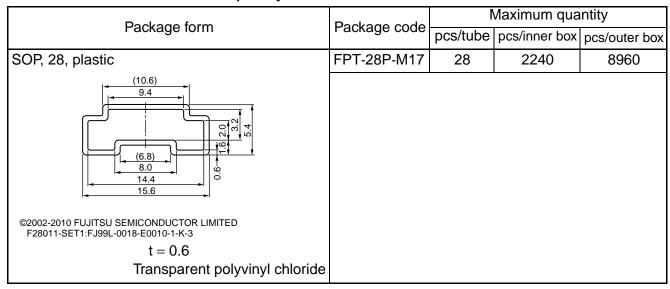
#### 1. Tube

#### 1.1 Tube Dimensions

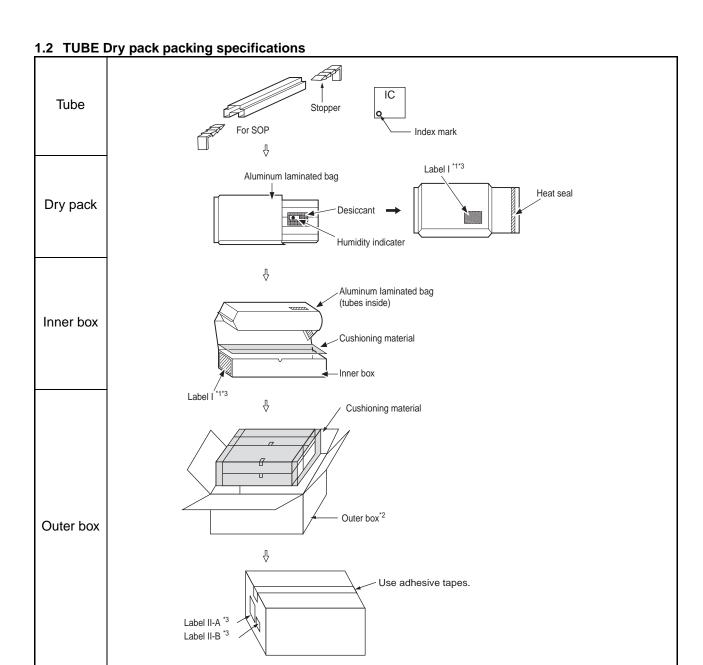
• Tube/stopper shape



#### **Tube cross-sections and Maximum quantity**



(Dimensions in mm)

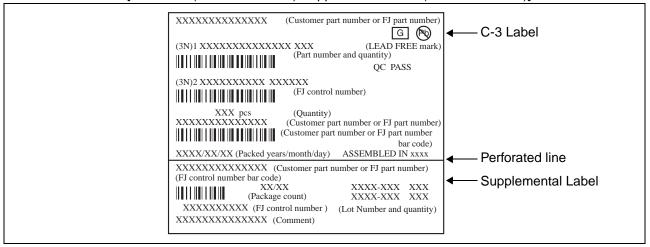


- \*1: For a product of witch part number is suffixed with "E1", a " G (N)" marks is display to the moisture barrier bag and the inner boxes.
- \*2: The space in the outer box will be filled with empty inner boxes, or cushions, etc.
- \*3: Please refer to an attached sheet about the indication label.

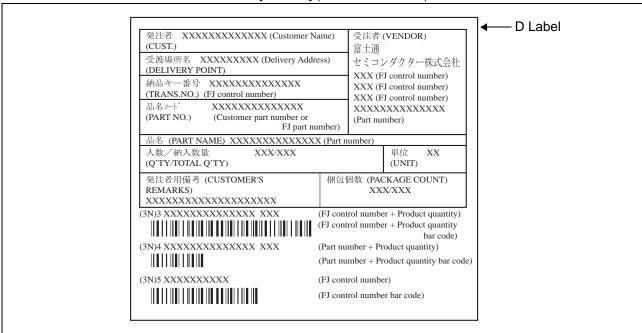
Note: The packing specifications may not be applied when the product is delivered via a distributer.

#### 1.3 Product label indicators

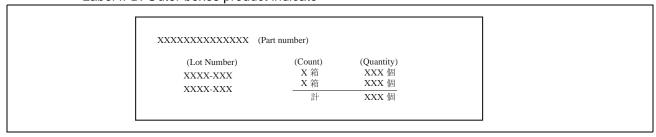
Label I: Label on Inner box/Moisture Barrier Bag/ (It sticks it on the reel for the emboss taping) [C-3 Label (50mm × 100mm) Supplemental Label (20mm × 100mm)]



Label II-A: Label on Outer box [D Label] (100mm × 100mm)



Label II-B: Outer boxes product indicate

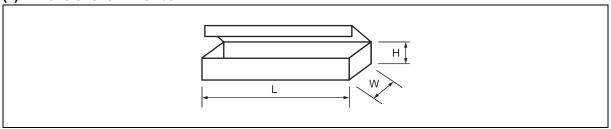


Note: Depending on shipment state, "Label II-A" and "Label II-B" on the external boxes might not be printed.

# MB85R256F

#### 1.4 Dimensions for Containers

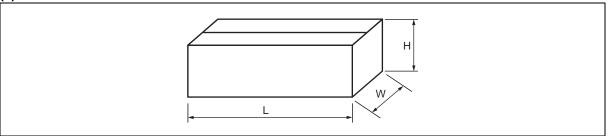
#### (1) Dimensions for inner box



L	W	Н
540	125	75

(Dimensions in mm)

#### (2) Dimensions for outer box

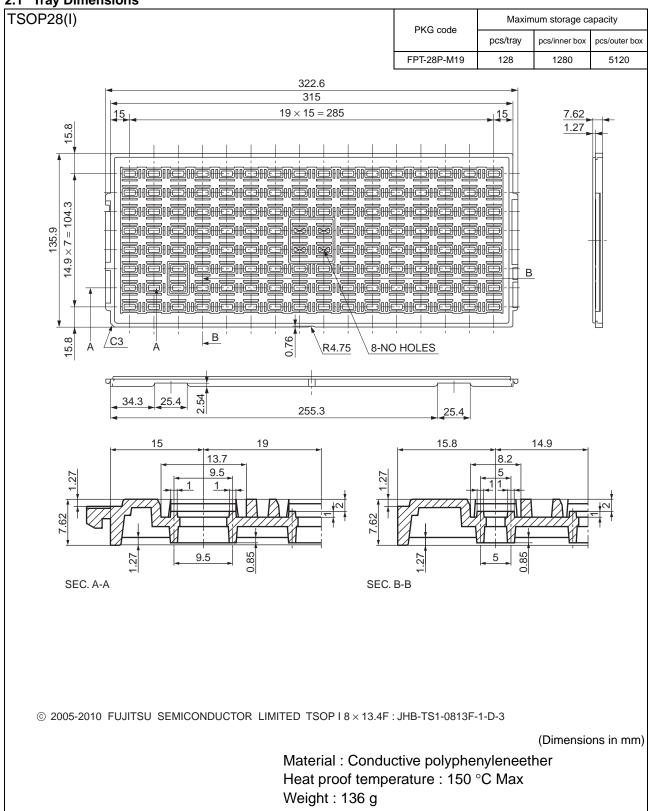


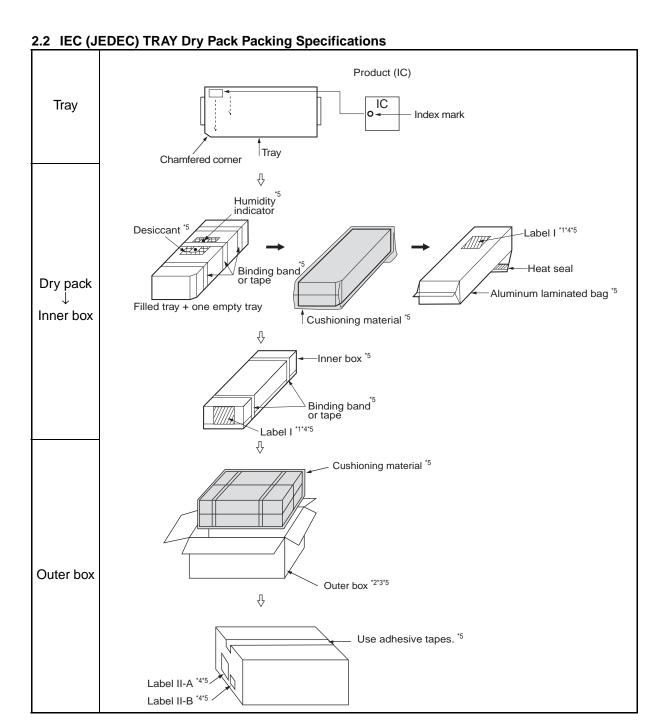
L	W	Н
565	270	180

(Dimensions in mm)

#### 2. Tray

#### 2.1 Tray Dimensions



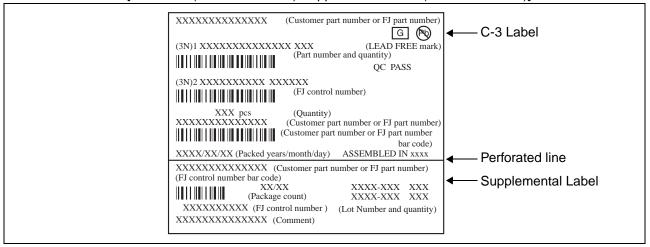


- \*2: The size of the outer box may be changed depending on the quantity of inner boxes.
- \*3: The space in the outer box will be filled with empty inner boxes, or cushions, etc.
- \*4: Please refer to an attached sheet about the indication label.
- \*5: The packing materials except tray may differ slightly from the color and dimensions depend on country of manufacture.

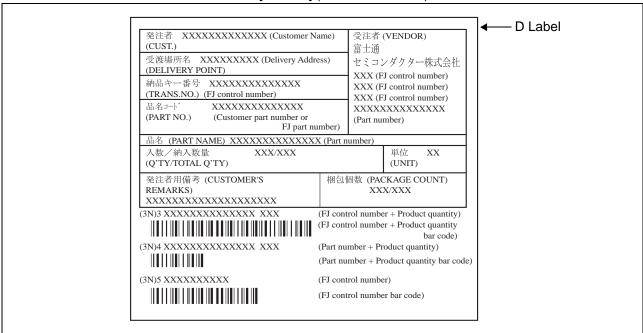
Note: The packing specifications may not be applied when the product is delivered via a distributer.

#### 2.3 Product label indicators

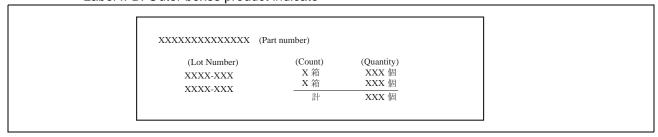
Label I: Label on Inner box/Moisture Barrier Bag/ (It sticks it on the reel for the emboss taping) [C-3 Label (50mm × 100mm) Supplemental Label (20mm × 100mm)]



Label II-A: Label on Outer box [D Label] (100mm × 100mm)



Label II-B: Outer boxes product indicate

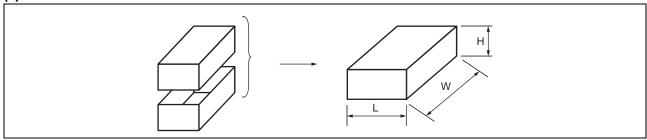


Note: Depending on shipment state, "Label II-A" and "Label II-B" on the external boxes might not be printed.

# MB85R256F

#### 2.4 Dimensions for Containers

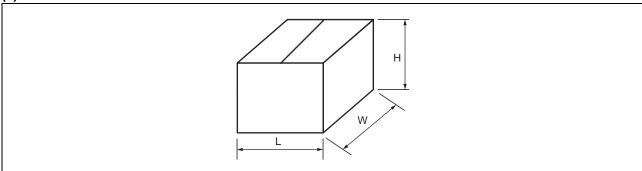
#### (1) Dimensions for inner box



L	W	Н
165	360	75

(Dimensions in mm)

#### (2) Dimensions for outer box



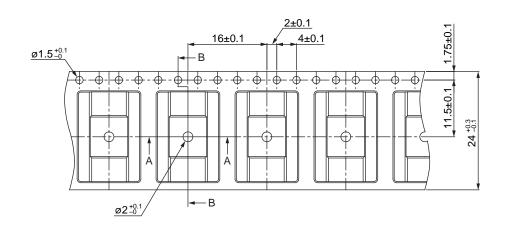
L	W	H
355	385	195

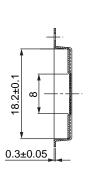
(Dimensions in mm)

#### 3. Tape

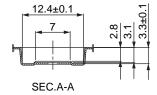
#### 3.1 Tape Dimensions

PKG code	Reel No	Maxim	um storage ca	apacity
PNG code	Reel No	pcs/reel	pcs/inner box	pcs/outer box
FPT-28P-M17	7	1000	1000	5000





SEC.B-B



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(Dimensions in mm)

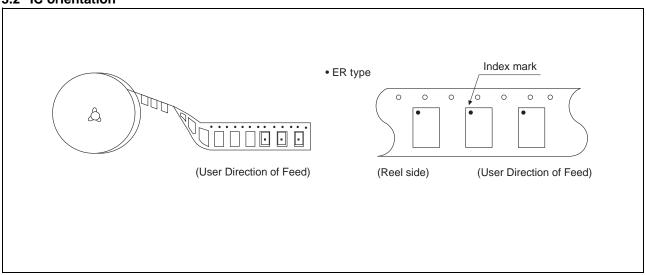
Material : Plant origin, conductive tape

Heat proof temperature : No heat resistance.

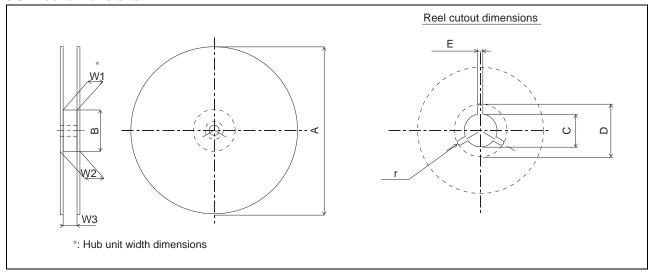
Package should not be baked

by using tape and reel.

#### 3.2 IC orientation

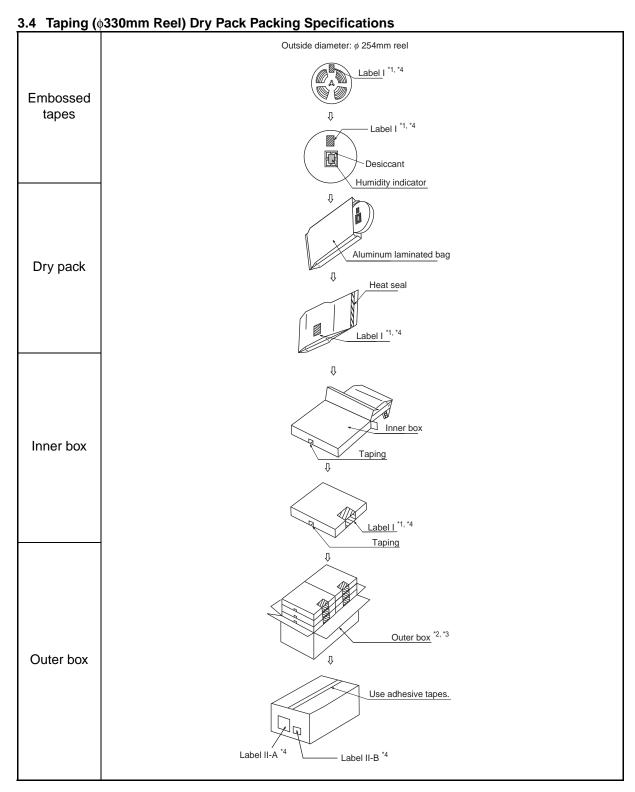


#### 3.3 Reel dimensions



#### Dimensions in mm

			_	_	-	_	_	_	_		_				
Reel No	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Tape width Symbol	8	12 16 24 32		16 24		4	4	56	12	16	24				
Α	254 ± 2	254 ± 2	$330 \pm 2$	254 ± 2	$330 \pm 2$	254 ± 2	$330 \pm 2$				330	) ± 2			
В				1	00 -0			100 -0	150 -2	100 -0	150 -2	100 -2		100 ± 2	
С		$13 \pm 0.2$ $13^{+0.5}_{-0.2}$													
D	21 ± 0.8 20.5 <sup>+1</sup> <sub>-0</sub>							20.5 +1 -0.2							
E		2 ± 0.5													
W1	8.4 +2	1:	2.4 +2	1	6.4 +2	24	4.4 +2	32	2.4 -2	4	4.4 +2	56.4 +2	12.4 +1	16.4 +1	24.4+0.1
W2	less than 14.4	less tha	an 18.4	less than 22.4 less than 30.4 less than 38.4 less than 50.4 less than 62.4					less than 18.4	less than 22.4	less than 30.4				
W3	7.9 ~ 10.9	11.9	~ 15.4	15.9	~ 19.4	.4 23.9 ~ 27.4			~ 35.4	43.9	- 47.4	55.9 ~ 59.4	12.4 ~ 14.4	16.4 ~ 18.4	24.4 ~ 26.4
r		1.0													



<sup>\*1:</sup> For a product of witch part number is suffixed with "E1", a " [G] (N)" marks is display to the moisture barrier bag and the inner boxes.

Note: The packing specifications may not be applied when the product is delivered via a distributer.

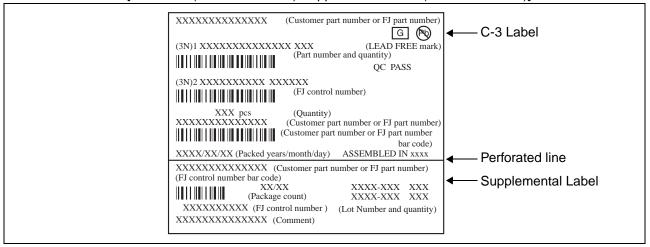
<sup>\*2:</sup> The size of the outer box may be changed depending on the quantity of inner boxes.

<sup>\*3:</sup> The space in the outer box will be filled with empty inner boxes, or cushions, etc.

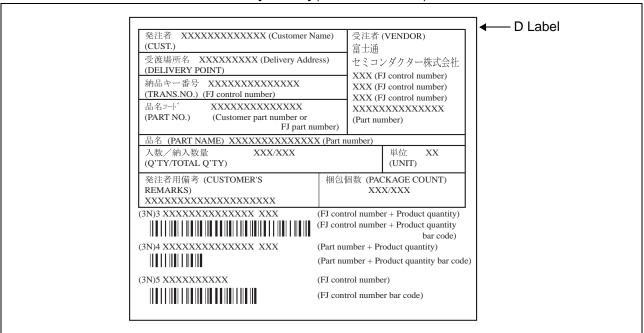
<sup>\*4:</sup> Please refer to an attached sheet about the indication label.

#### 3.5 Product label indicators

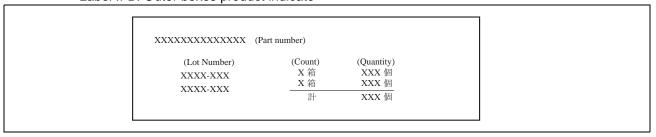
Label I: Label on Inner box/Moisture Barrier Bag/ (It sticks it on the reel for the emboss taping) [C-3 Label (50mm × 100mm) Supplemental Label (20mm × 100mm)]



Label II-A: Label on Outer box [D Label] (100mm × 100mm)



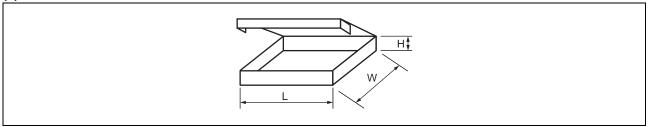
Label II-B: Outer boxes product indicate



Note: Depending on shipment state, "Label II-A" and "Label II-B" on the external boxes might not be printed.

#### 3.6 Dimensions for Containers

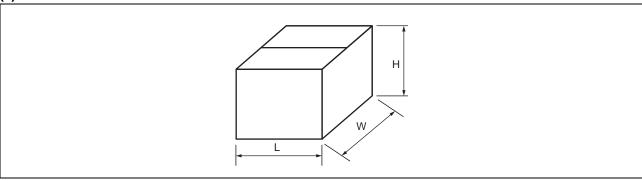
#### (1) Dimensions for inner box



Tape width	L	W	Н
12, 16			40
24, 32	365	345	50
44	303	343	65
56			75

(Dimensions in mm)

#### (2) Dimensions for outer box



L	W	Н
415	400	315

(Dimensions in mm)

# MB85R256F

### ■ MAJOR CHANGES IN THIS EDITION

A change on a page is indicated by a vertical line drawn on the left side of that page.

Page	Section	Change Results
1	■ FEATURES	Revised the Data retention 10 years (+85 °C)  →10 years (+85 °C), 95 years (+55 °C), over 200 years (+35 °C)
5	■ ABSOLUTE MAXIMUM RANGES	Revised the Storage Temperature $-40 ^{\circ}\text{C} \rightarrow -55 ^{\circ}\text{C}$
10	■ POWER ON/OFF SEQUENCE	Deleted the following description: "Because turning the power-on from an intermediate level cause malfunction, when the power is turned on, VDD is required to be started from 0V (see the figure below)."  Moved the following description under the table: "If the device does not operate within the specified conditions of read cycle, write cycle or power on/off sequence, memory data can not be guaranteed."
	■ FRAM CHARACTERISTICS	Revised the table and Note



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