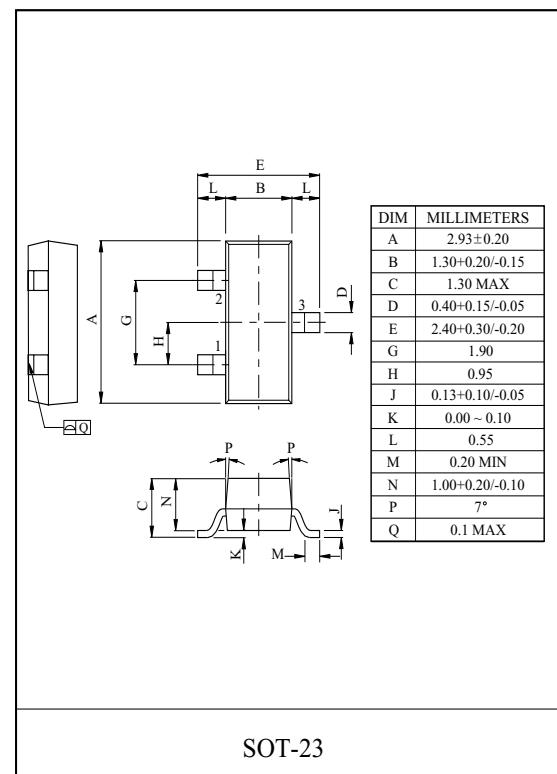


**General Description**

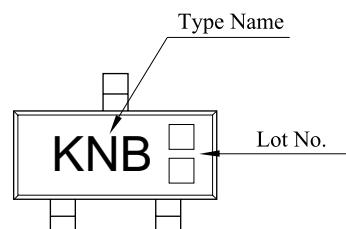
This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for portable equipment.

**FEATURES**

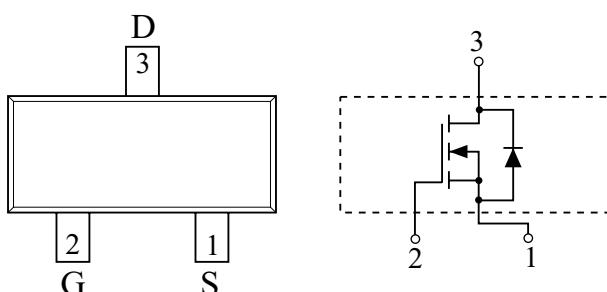
- $V_{DSS}=20V$ ,  $I_D=3A$
- Drain to Source on-state Resistance  
 $R_{DS(ON)}=55m\Omega$  (Max.) @  $V_{GS}=4.5V$   
 $R_{DS(ON)}=110m\Omega$  (Max.) @  $V_{GS}=2.5V$
- Super Hige Dense Cell Design

**MAXIMUM RATING (Ta=25 °C)**

CHARACTERISTIC		SYMBOL	N-Ch	UNIT
Drain to Source Voltage		$V_{DSS}$	20	V
Gate to Source Voltage		$V_{GSS}$	± 12	V
Drain Current	DC@ $T_a=25$ (Note1)	$I_D$	3	A
	Pulsed (Note1)	$I_{DP}$	12	
Drain Power Dissipation	$T_a=25$ (Note1)	$P_D$	1.25	W
	$T_a=70$ (Note1)		0.8	
Maximum Junction Temperature		$T_j$	150	
Storage Temperature Range		$T_{stg}$	-55 ~ 150	
Thermal Resistance, Junction to Ambient (Note1)		$R_{thJA}$	100	/W

**Marking**

Note1) Surface Mounted on 1 "x 1 "FR4 Board, t = 5sec.

**PIN CONNECTION (TOP VIEW)**

# KMA3D0N20SA

## ELECTRICAL CHARACTERISTICS (Ta=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain to Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>DS</sub> =250 μA, V <sub>GS</sub> =0V,	20	-	-	V
Drain Cut-off Current	I <sub>DS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =16V	-	-	1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate to Source Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	0.5	0.8	1.5	V
Drain to Source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.5A (Note2)	-	38	55	m
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =1A (Note2)	-	55	110	
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =5V (Note2)	12	-	-	A
Forward Transconductance	g <sub>f</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =2.5A (Note2)	-	6	-	S
<b>Dynamic</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	-	280	-	pF
Output Capacitance	C <sub>oss</sub>		-	64	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	34	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.5A (Note2)	-	4.0	-	nC
Gate to Source Charge	Q <sub>gs</sub>		-	0.9	-	
Gate to Drain Charge	Q <sub>gd</sub>		-	0.9	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V I <sub>D</sub> =1A, R <sub>G</sub> =6 (Note2)	-	6.3	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	7.0	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	7.3	-	
Turn-Off Fall Time	t <sub>f</sub>		-	6.2	-	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	I <sub>S</sub>	-	-	-	3.0	A
Pulsed Source Current	I <sub>SP</sub>	-	(Note2)	-	12	A
Source to Drain Forward Voltage	V <sub>SDF</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1.25A	-	-	1.2	V
NOTE 2) Pulse Test : Pulse width <300μs , Duty cycle < 2%						

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Fig1.  $I_D$  -  $V_{DS}$

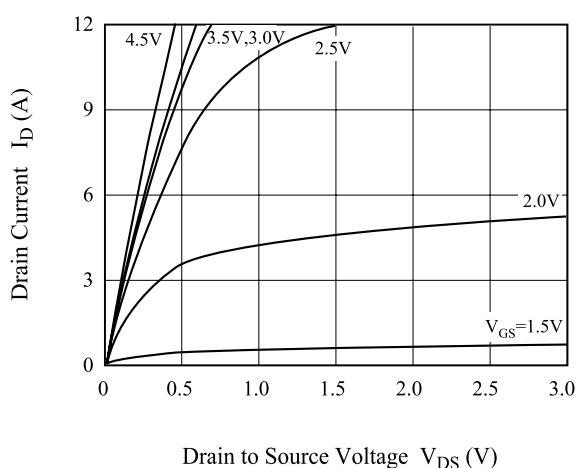


Fig2.  $R_{DS(on)}$  -  $I_D$

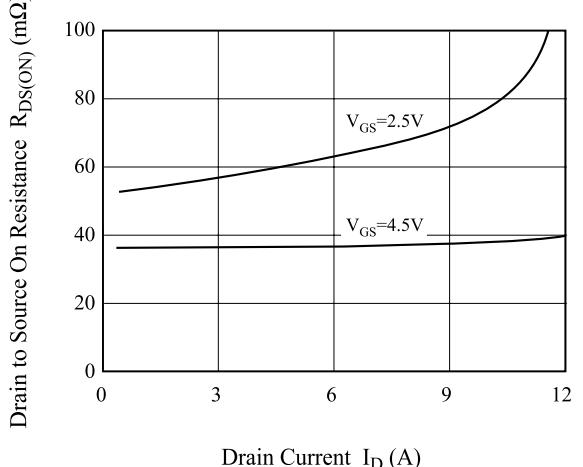


Fig3.  $I_D$  -  $V_{GS}$

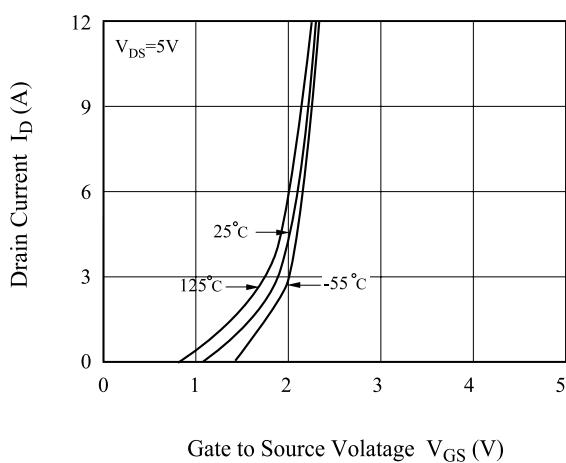


Fig4.  $R_{DS(on)}$  -  $T_j$

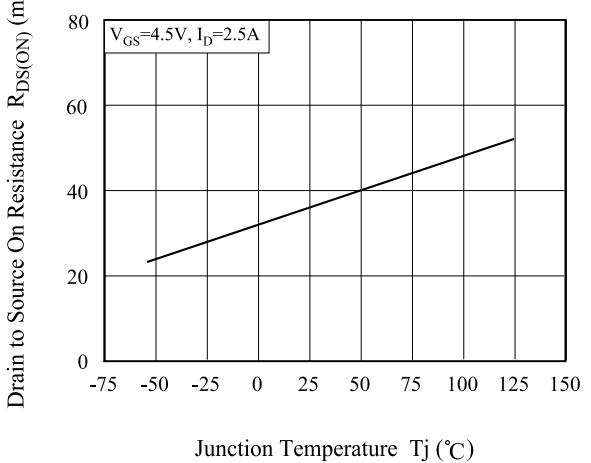


Fig5.  $V_{th}$  -  $T_j$

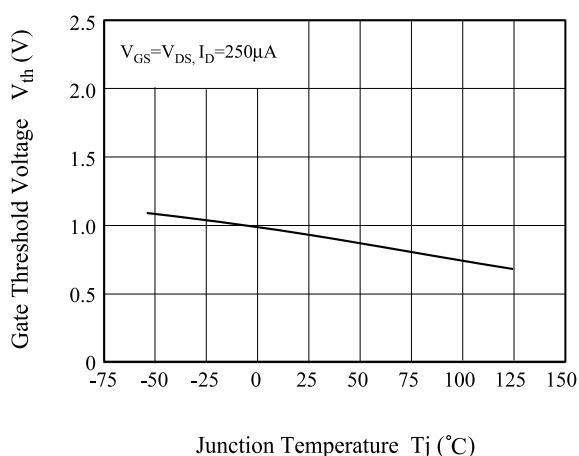
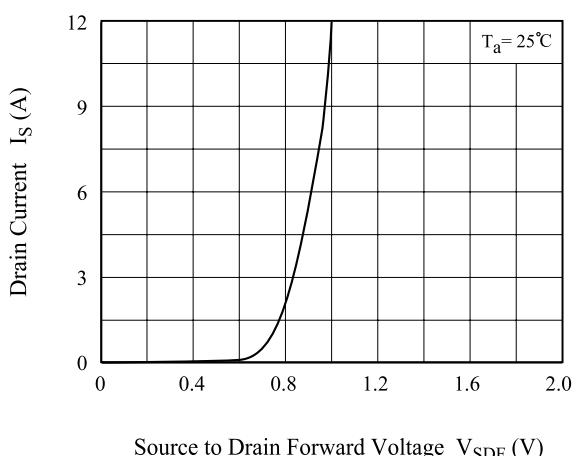


Fig6.  $I_S$  -  $V_{SDF}$



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Fig7. C - V<sub>DS</sub>

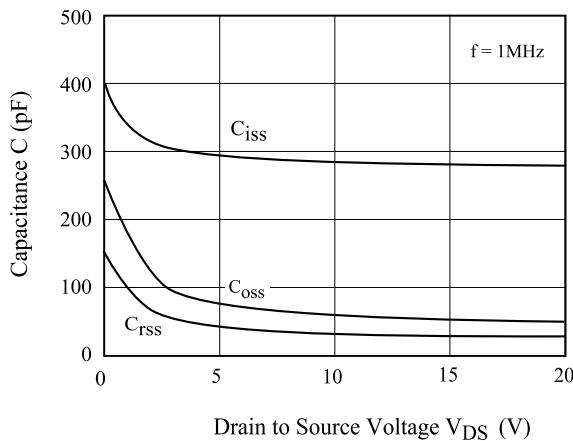


Fig8. Q<sub>g</sub> - V<sub>GS</sub>

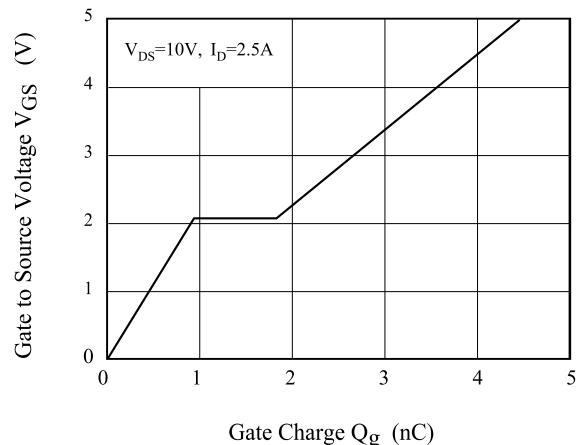


Fig9. Safe Operation Area

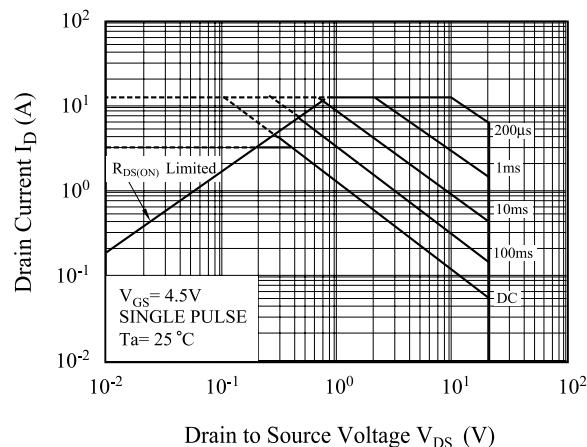
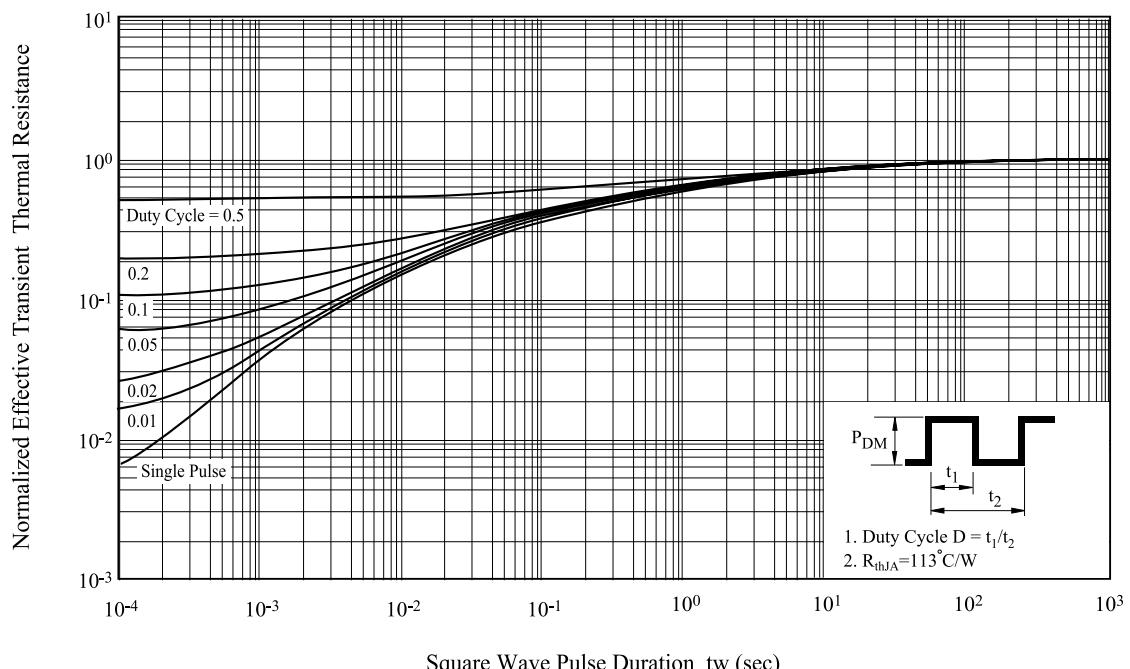


Fig10. Transient Thermal Response Curve



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