

Specification Sheet

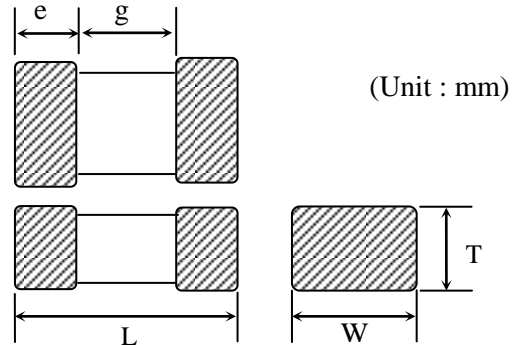
<Chip Monolithic Ceramic Capacitor>

Murata Global P/N : WBM155R71H103KA01D (0402,X7R,0.01 μ F,50V)

Corresponding products for RoHS directive

1. Dimensions (Unit : mm)

L	1.0+/-0.05
W	0.5+/-0.05
T	0.5+/-0.05
e	0.15 to 0.35
g	0.3 min.



2. Rated Value

TC code	R7
TC	X7R
Temp.Coeff or Cap.Change	+/-15% at -55 to 125°C
CAP. , CAP.TOL	0.01 μ F , +/-10%
DC Rated Voltage	50V
Size Code	0402

3. Packaging

Specification	Packaging unit [pcs/reel]
ϕ 180 Paper Tape Carrier Packaging	10000

4. Specification

Please refer to next page.

△Note

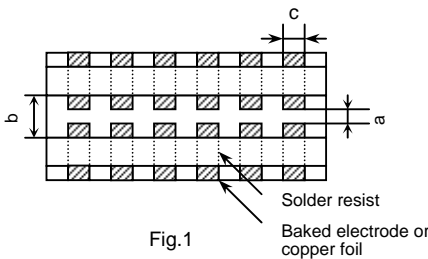
- (1) This specification sheet is applied for CHIP MONOLITHIC CERAMIC CAPACITOR "WBM series" used for General Electronics equipment for your design.
- (2) Please contact our sales representative or product engineers before using our products for the application listed below.
 - ① Aircraft equipment ② Aerospace equipment ③ Undersea equipment ④ Medical equipment
 - ⑤ Transportation equipment ⑥ Traffic signal equipment ⑦ Disaster prevention / crime prevention equipment
 - ⑧ Application of similar complexity and/or requirements to the applications listed in the above.
- (3) Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.
- (4) Use of Sn-Zn based solder will deteriorate reliability of MLCC. Please contact murata factory for the use of Sn-Zn based solder in advance.
- (5) This specification sheet has only typical specification because there is no space for detailed specifications.

Therefore, please approve our product specification or transact the approval sheet for product specification before your ordering.

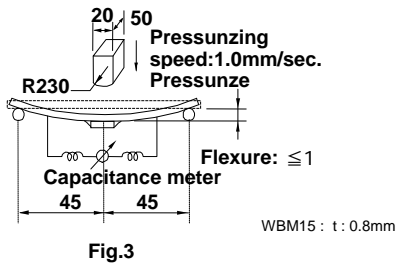
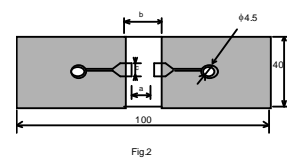
Especially, please read rating and CAUTION (for storage, operating, rating, soldering, mounting, and handling) in them to prevent smoking and/or burning, etc.
- (6) Product material and design are subject to change without advance notice even though product specification is no change.
- (7) This specification has no room to accept your special requests.

If there are any questions, please contact our sales representatives or product engineers.

■ SPECIFICATIONS AND TEST METHODS

No	Item	Specification	Test Method												
1	Operating Temperature Range	R6 : -55°C to +85°C R7 : -55°C to +125°C	Reference Temperature : 25°C												
2	Rated Voltage	See the previous pages	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, V^{P-P} or V^{O-P} , whichever is larger, should be maintained within the rated voltage range.												
3	Appearance	No defects or abnormalities	Visual inspection.												
4	Dimensions	Within the specified dimension	Using calipers or Microscope. (GRM02 size is based on Microscope)												
5	Dielectric Strength	No defects or abnormalities	No failure should be observed when 250% of the rated voltage is applied between the terminations for 1 to 5 seconds, provided the charge/discharge current is less than 50mA.												
6	Insulation Resistance	More than 50Ω·F	The insulation resistance should be measured with a DC voltage not exceeding the rated voltage at 25°C and 75%RH max. and within 1 minutes of charging.												
7	Capacitance	Within the specified tolerance	The capacitance/D.F. should be measured at 25°C at the frequency and voltage shown in the table.												
8	Dissipation Factor (D.F.)	R6, R7 : 0.1 max.	<table border="1"> <thead> <tr> <th>Capacitance</th> <th>Frequency</th> <th>Voltage</th> </tr> </thead> <tbody> <tr> <td>$C \leq 10\mu\text{F}$ (10V min.)</td> <td>$1 \pm 0.1\text{kHz}$</td> <td>$1.0 \pm 0.2 \text{ Vrms}$</td> </tr> <tr> <td>$C \leq 10\mu\text{F}$ (6.3V max.)</td> <td>$1 \pm 0.1\text{kHz}$</td> <td>$0.5 \pm 0.1 \text{ Vrms}$</td> </tr> <tr> <td>$C > 10\mu\text{F}$</td> <td>$120 \pm 24\text{Hz}$</td> <td>$0.5 \pm 0.1 \text{ Vrms}$</td> </tr> </tbody> </table>	Capacitance	Frequency	Voltage	$C \leq 10\mu\text{F}$ (10V min.)	$1 \pm 0.1\text{kHz}$	$1.0 \pm 0.2 \text{ Vrms}$	$C \leq 10\mu\text{F}$ (6.3V max.)	$1 \pm 0.1\text{kHz}$	$0.5 \pm 0.1 \text{ Vrms}$	$C > 10\mu\text{F}$	$120 \pm 24\text{Hz}$	$0.5 \pm 0.1 \text{ Vrms}$
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9	Capacitance Temperature Characteristics	<table border="1"> <thead> <tr> <th>Char.</th> <th>Temp. Range</th> <th>Reference Temp.</th> <th>Cap. Change</th> </tr> </thead> <tbody> <tr> <td>R6</td> <td>-55°C to +85°C</td> <td>25°C</td> <td>Within $\pm 15\%$</td> </tr> <tr> <td>R7</td> <td>-55°C to +125°C</td> <td>25°C</td> <td>Within $\pm 15\%$</td> </tr> </tbody> </table>	Char.	Temp. Range	Reference Temp.	Cap. Change	R6	-55°C to +85°C	25°C	Within $\pm 15\%$	R7	-55°C to +125°C	25°C	Within $\pm 15\%$	<p>The capacitance change should be measured after 5 min. at each specified temperature stage.</p> <p>The ranges of capacitance change compared with the 25°C value over the temperature ranges shown in the table shall be within the specified ranges.</p> <p>· Initial measurement for high dielectric constant type Perform a heat treatment at 150+0/-10°C for one hour and then set for 24±2 hour at room temperature. Perform the initial measurement.</p>
Char.	Temp. Range	Reference Temp.	Cap. Change												
R6	-55°C to +85°C	25°C	Within $\pm 15\%$												
R7	-55°C to +125°C	25°C	Within $\pm 15\%$												
10	Adhesive Strength of Termination	<p>No removal of the terminations or other defects should occur.</p>  <p>Fig.1</p>	<p>Solder the capacitor to the test jig (glass epoxy board) shown in Fig.1 using a eutectic solder. Then apply 5N force in parallel with the test jig for 10±1 sec. The soldering should be done either with an iron or using the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock.</p> <table border="1"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td>WBM15</td> <td>0.4</td> <td>1.5</td> <td>0.5</td> </tr> </tbody> </table> <p>(in:mm)</p>	Type	a	b	c	WBM15	0.4	1.5	0.5				
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11	Vibration	<table border="1"> <tbody> <tr> <td>Appearance</td> <td>No defects or abnormalities</td> </tr> <tr> <td>Capacitance</td> <td>Within the specified tolerance</td> </tr> <tr> <td>D.F.</td> <td>R6, R7 : 0.1 max.</td> </tr> </tbody> </table>	Appearance	No defects or abnormalities	Capacitance	Within the specified tolerance	D.F.	R6, R7 : 0.1 max.	<p>Solder the capacitor on the test jig (glass epoxy board) in the same manner and under the same conditions as (10).</p> <p>The capacitor should be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55Hz.</p> <p>The frequency range, from 10 to 55Hz and return to 10Hz, should be traversed in approximately 1 minute.</p> <p>This motion should be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).</p>						
Appearance	No defects or abnormalities														
Capacitance	Within the specified tolerance														
D.F.	R6, R7 : 0.1 max.														

■ SPECIFICATIONS AND TEST METHODS

No	Item	Specification		Test Method														
12	Deflection	Appearance	No defects or abnormalities	Solder the capacitor to the test jig (glass epoxy board) shown in Fig.2 using a eutectic solder. Then apply a force in the direction shown in Fig.3. The soldering should be done by the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock.														
		Capacitance Change	Within $\pm 10\%$															
		 <p>Fig.3</p>		 <p>Fig.2</p> <table border="1" data-bbox="1005 604 1436 694"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td>WBM15</td> <td>0.4</td> <td>1.5</td> <td>0.5</td> </tr> </tbody> </table> <p>(in:mm)</p>	Type	a	b	c	WBM15	0.4	1.5	0.5						
Type	a	b	c															
WBM15	0.4	1.5	0.5															
13	Solderability of Termination	75% of the terminations is to be soldered evenly and continuously		Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Preheat at 80 to 120°C for 10 to 30 seconds. After preheating, immerse in eutectic solder solution for 2 \pm 0.5 seconds at 230 \pm 5°C or Sn-3.0 Ag-0.5 Cu solder solution for 2 \pm 0.5 seconds at 245 \pm 5°C.														
14	Resistance to Soldering Heat	Appearance	No defects or abnormalities	Preheat the capacitor at 120 to 150°C for 1 minute. Immerse the capacitor in an eutectic solder solution or Sn-3.0 Ag-0.5 Cu solder solution at 270 \pm 5°C for 10 \pm 0.5 seconds. Set at room temperature for 24 \pm 2 hours, then measure. - Initial measurement for high dielectric constant type Perform a heat treatment at 150+0/-10°C for one hour and then set at room temperature for 24 \pm 2 hours. Perform the initial measurement.														
		Capacitance Change	R6, R7 : Within $\pm 7.5\%$															
		D.F.	R6, R7 : 0.1 max.															
		I.R.	More than 50 Ω · F															
		Dielectric Strength	No defects															
15	Temperature Sudden Change	Appearance	No defects or abnormalities	Fix the capacitor to the supporting jig in the same manner and under the same conditions as (10). Perform the five cycles according to the four heat treatments shown in the following table. Set for 24 \pm 2 hours at room temperature, then measure.														
		Capacitance Change	R6, R7 : Within $\pm 7.5\%$															
		D.F.	R6, R7 : 0.1 max.															
		I.R.	More than 50 Ω · F															
		Dielectric Strength	No defects															
		<table border="1" data-bbox="925 1254 1484 1400"> <thead> <tr> <th>Step</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> </tr> </thead> <tbody> <tr> <td>Temp.(°C)</td> <td>Min. Operating Temp.+0/-3</td> <td>Room Temp.</td> <td>Max. Operating Temp.+3/-0</td> <td>Room Temp.</td> </tr> <tr> <td>Time(min.)</td> <td>30\pm3</td> <td>2 to 3</td> <td>30\pm3</td> <td>2 to 3</td> </tr> </tbody> </table>		Step	1	2	3	4	Temp.(°C)	Min. Operating Temp.+0/-3	Room Temp.	Max. Operating Temp.+3/-0	Room Temp.	Time(min.)	30 \pm 3	2 to 3	30 \pm 3	2 to 3
Step	1	2	3	4														
Temp.(°C)	Min. Operating Temp.+0/-3	Room Temp.	Max. Operating Temp.+3/-0	Room Temp.														
Time(min.)	30 \pm 3	2 to 3	30 \pm 3	2 to 3														
		- Initial measurement for high dielectric constant type Perform a heat treatment at 150+0/-10°C for one hour and then set at room temperature for 24 \pm 2 hours. Perform the initial measurement.																
16	High Temperature High Humidity (Steady)	Appearance	No defects or abnormalities	Apply the rated voltage at 40 \pm 2°C and 90 to 95% humidity for 500 \pm 12 hours. The charge/discharge current is less than 50mA. - Initial measurement Perform a heat treatment at 150+0/-10°C for one hour and then set for 24 \pm 2 hours at room temperature. Perform the initial measurement. - Measurement after test Perform a heat treatment at 150+0/-10°C for one hour and then set for 24 \pm 2 hours at room temperature, then measure.														
		Capacitance Change	R6, R7 : Within $\pm 12.5\%$															
		D.F.	R6, R7 : 0.2max.															
		I.R.	More than 12.5 Ω · F															
17	Durability	Appearance	No defects or abnormalities	Apply 150% of the rated voltage for 1000 \pm 12 hours at the maximum operating temperature $\pm 3^\circ\text{C}$. Set for 24 \pm 2 hours at room temperature, then measure. The charge/ discharge current is less than 50mA. - Initial measurement Perform a heat treatment at 150+0/-10°C for one hour and then set for 24 \pm 2 hours at room temperature. Perform the initial measurement. - Measurement after test Perform a heat treatment at 150+0/-10°C for one hour and then set for 24 \pm 2 hours at room temperature, then measure.														
		Capacitance Change	R6, R7 : Within $\pm 12.5\%$															
		D.F.	R6, R7 : 0.2max.															
		I.R.	More than 25 Ω · F															

PACKAGING

WBM Type

There are three type of packaging for chip monolithic ceramic capacitor.
Please specify the packaging code.

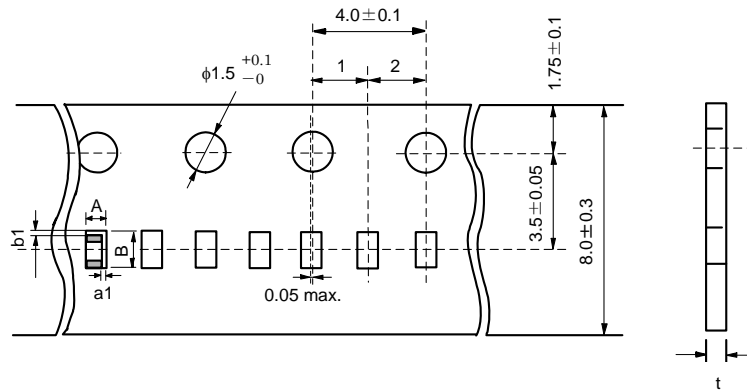
1. Bulk Packaging(Packaging Code=B):In a bag.
Minimum Quantity : 1000(pcs./bag)
 2. Tape Carrier Packaging(Packaging Code:D)
- 2.1 Minimum Quantity(pcs./reel)

Type	φ180 reel	φ330 reel
	Paper Tape	Paper Tape
	Code:D	Code:J
WBM15	10000	50000

2.2 Dimensions of Tape

(2)WBM15

(in : mm)



*1,2:2.0±0.05

Code	WBM15
A *3	0.65
B *3	1.15
a1,b1 *3	0.15
t	0.8 max.

*3 Nominal value

PACKAGING WBM Type

Fig.1 Package Chips

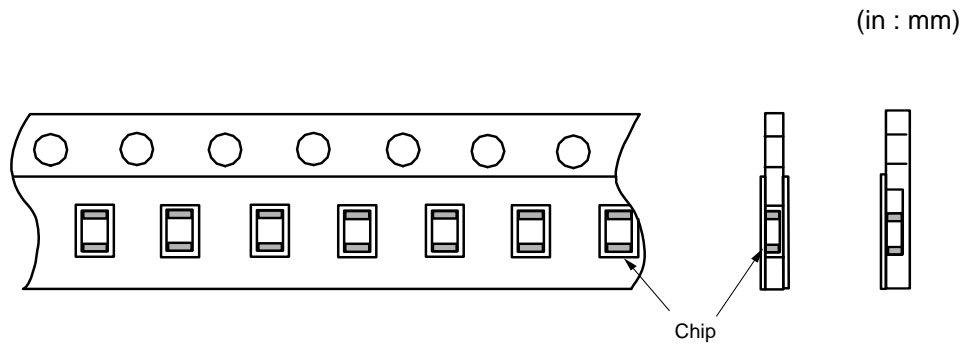


Fig.2 Dimensions of Reel

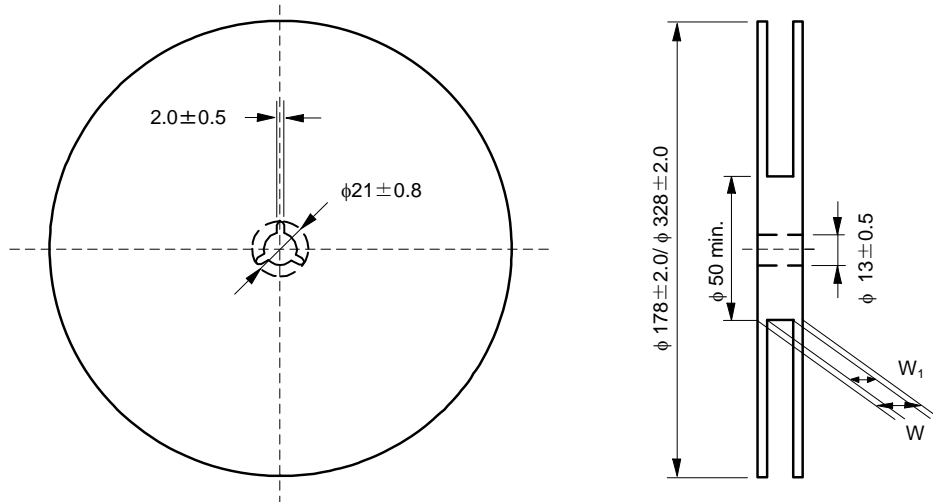
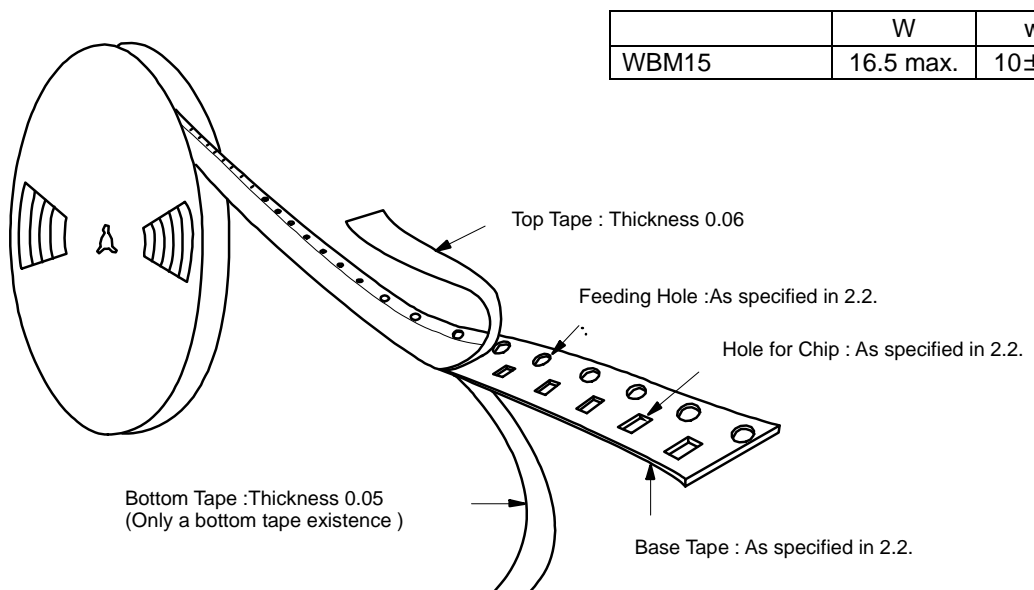


Fig.3 Taping Diagram



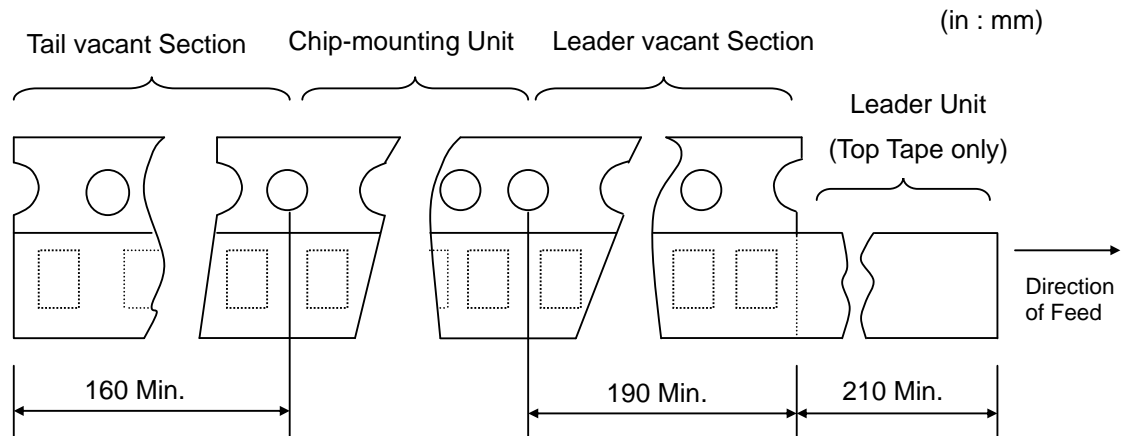
	W	w ₁
WBM15	16.5 max.	10 ± 1.5

PACKAGING WBM Type

2.3 Tapes for capacitors are wound clockwise shown in Fig.3.

(The sprocket holes are to the right as the tape is pulled toward the user.)

2.4 Part of the leader and part of the vacant section are attached as follows.



2.5 Accumulate pitch : 10 of sprocket holes pitch = $40 \pm 0.3\text{mm}$

2.6 Chip in the tape is enclosed by top tape and bottom tape as shown in Fig.1.

2.7 The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.

2.8 There are no jointing for top tape and bottom tape.

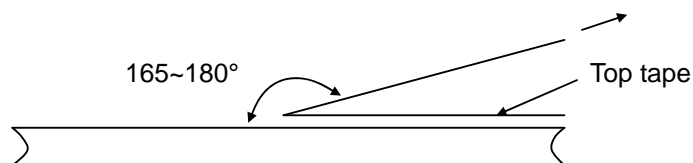
2.9 There are no fuzz in the cavity.

2.10 Break down force of top tape : 5N min.

Break down force of bottom tape : 5N min. (Only a bottom tape existence)

2.11 Reel is made by resin and appeaser and dimension is shown in Fig 2. There are possibly to change the material and dimension due to some impairment.

2.12 Peeling off force : 0.1 to 0.6N in the direction as shown below.



2.13 Label that show the customer parts number, our parts number, our company name, inspection number and quantity, will be put in outside of reel.

⚠CAUTION

◆Limitation of use

Please contact our sales representatives or product engineers before using our products for the applications listed below which require of our products for other applications than specified in this product.

- ①Aircraft equipment ②Aerospace equipment ③Undersea equipment ④Power plant control equipment
- ⑤Medical equipment ⑥Transportation equipment(vehicles, trains, ships, etc.) ⑦Traffic signal equipment
- ⑧Disaster prevention / crime prevention equipment ⑨Data-processing equipment
- ⑩Application of similar complexity and/or requirements to the applications listed in the above

⚠CAUTION

◆Storage and Operating Conditions

1. Chip monolithic ceramic capacitors(chips) can experience degradation of termination solderability when subjected to high temperature or humidity, or if exposed to sulfur or chlorine gases. Storage environment must be at an ambient temperature of 5-40 °C. and an ambient humidity of 20-70%RH. Use chip within 6 months. If 6 months or more have elapsed, check solderability before use. (Reference Data 1/ Solderability) Insulation Resistance should be deteriorated on specific condition of high humidity or incorrosion gas such as hydrogen sulfide, sulfurous acid gas, chlorine. Those condition are not suitable for use.
2. Use of Sn-Zn based solder will deteriorate reliability of MLCC. Please contact murata factory for the use of Sn-Zn based solder in advance.
3. Do not use under the condition that causes condensation.
Use dampproof countermeasure if using under the condition that causes condensation.

⚠CAUTION

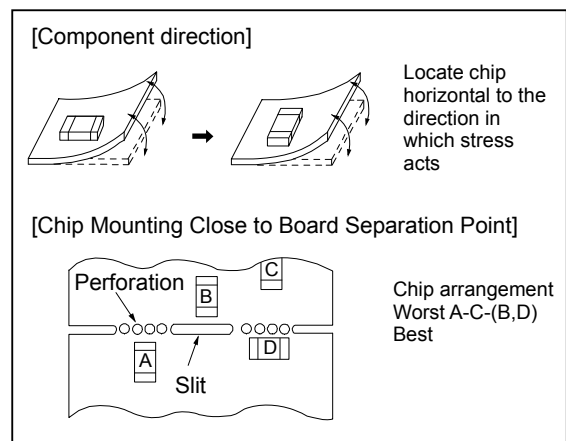
◆Handling

1. Inspection
 - Thrusting force of the test probe can flex the PCB, resulting in cracked chips or open solder joints. Provide support pins on the back side of the PCB to prevent warping or flexing.
2. Board Separation (or Depanelization)
 - Board flexing at the time of separation causes cracked chips or broken solder.
 - Severity of stresses imposed on the chip at the time of board break is in the order of: Pushback < Slitter < V Slot < Perforator.
 - Board separation must be performed using special jigs, not with hands.
3. Reel and bulk case
 - In the handling of reel and case, please pay attention not to drop it. Please do not use chip of the case which dropped.

⚠CAUTION

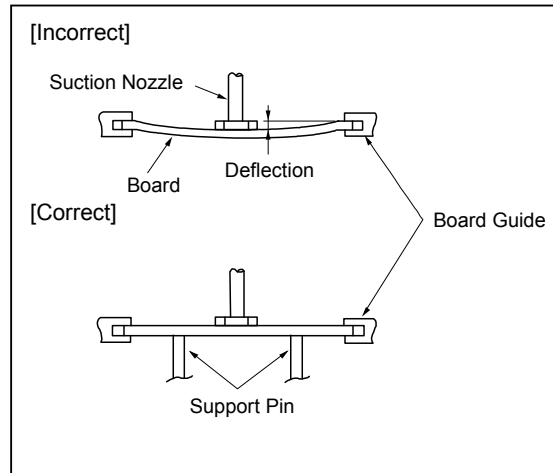
◆Soldering and Mounting

1. Mounting Position
Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.



2. Chip Placing

- An excessively low bottom dead point of the suction nozzle imposes great force on the chip during mounting, causing cracked chips. So adjust the suction nozzle's bottom dead point by correcting warp in the board.
Normally, the suction nozzle's bottom dead point must be set on the upper surface of the board. Nozzle pressure for chip mounting must be a 1 to 3N static load.
- Dirt particles and dust accumulated between the suction nozzle and the cylinder inner wall prevent the nozzle from moving smoothly. This imposes great force on the chip during mounting, causing cracked chips. And the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked and replaced periodically.



3. Caution for Soldering

(1) Reflow soldering

- When the sudden heat is given to the components, the mechanical strength of the components should go down because remarkable temperature change causes deformity of components inside. In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in table 1. It is required to keep temperature differential between the soldering and the components surface (ΔT) as small as possible.
- Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used.
Please confirm the solderability of Tin plating termination chip before use.
- When components are immersed in solvent after mounting, be sure to maintain the temperature difference (ΔT) between the component and solvent within the range shown in the table 1.

Table 1

Part Number	Temperature Differential
WBM15	$\Delta T \leq 190^\circ\text{C}$

Recommended Conditions

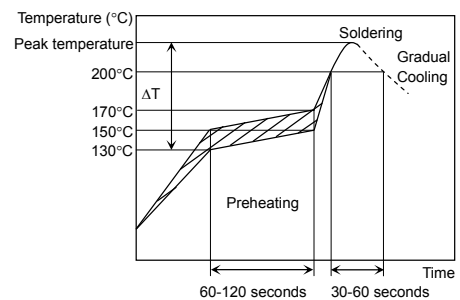
	Pb-Sn Solder		Lead Free Solder
	Infrared Reflow	Vapor Reflow	
Peak Temperature	230-250°C	230-240°C	240-260°C
Atmosphere	Air	Air	Air or N ₂

Pb-Sn Solder: Sn-37Pb

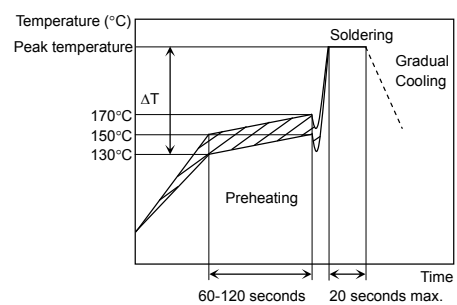
Lead Free Solder: Sn-3.0Ag-0.5Cu

[Standard Conditions for Reflow Soldering]

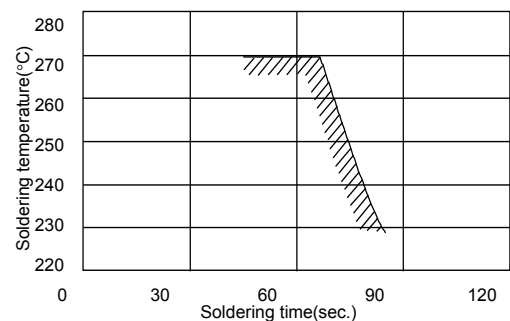
Infrared Reflow



Vapor Reflow

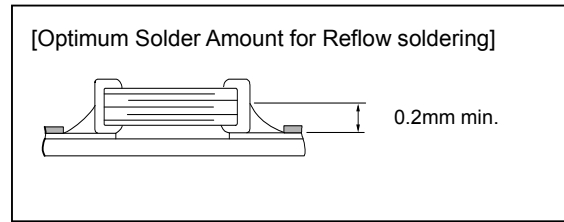


[Allowable Soldering Temperature and Time]



In case of repeated soldering, the accumulated soldering time must be within the range shown above.

- Optimum Solder Amount for Reflow Soldering
 - Overly thick application of solder paste results in excessive fillet height solder. This makes the chip more susceptible to mechanical and thermal stress on the board and may cause cracked chips.
 - Too little solder paste results in a lack of adhesive strength on the outer electrode, which may result in chips breaking loose from the PCB.
 - Make sure the solder has been applied smoothly to the end surface to a height of 0.2mm min.



Inverting the PCB

Make sure not to impose an abnormal mechanical shock on the PCB.

(2) Leaded Component Insertion

If the PCB is flexed when leaded components (such as transformers and ICs) are being mounted, chips may crack and solder joints may break. Before mounting leaded components, support the PCB using backup pins or special jigs prevent warping.

(3) Correction with a Soldering Iron

- When sudden heat is applied to the components by use of a soldering iron, the mechanical strength of the components will go down because the extreme temperature change causes deformations inside the components. In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB board. Preheating conditions, (The "Temperature of the Soldering Iron tip", "Preheating Temperature", "Temperature Differential" between the iron tip and the components and the PCB), should be within the conditions of table 3. It is required to keep the temperature differential between the soldering iron and the components surface (ΔT) as small as possible. After soldering, do not allow the component/PCB to cool down rapidly. The operating time for the re-working should be as short as possible. When re-working time is too long, it may cause solder leaching, and that will cause a reduction of the adhesive strength of the terminations.

Table 3

Part Number	Temperature of Soldering Iron tip	Preheating Temperature	Temperature Differential	Atmosphere
WBM15	350°C max	150°C min	$\Delta T \leq 190^\circ\text{C}$	Air

*Applicable for both Pb-Sn and Lead Free Solder

Pb-Sn Solder: Sn-37Pb

Lead Free Solder: Sn-3.0Ag-0.5Cu

- Optimum Solder Amount when re-working Using a Soldering Iron

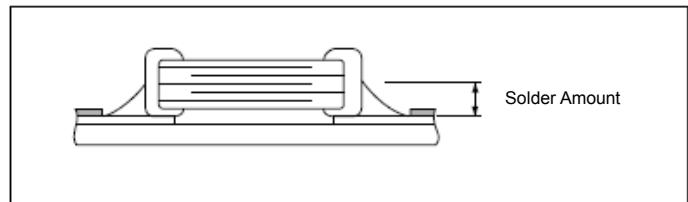
In case of smaller sizes than 0603, the top of the solder fillet should be lower than 2/3's of the thickness of the component or 0.5mm whichever is smaller.

If the solder amount is excessive, the risk of cracking is higher during board bending or under any other stressful conditions.

A Soldering iron $\phi 3\text{mm}$ or smaller should be used.

It is also necessary to keep the soldering iron from touching the components during the re-work.

Solder wire with $\phi 0.5\text{mm}$ or smaller is required for soldering.



4. Washing

Excessive output of ultrasonic oscillation during cleaning causes PCBs to resonate, resulting in cracked chips or broken solder. Take note not to vibrate PCBs.

Failure to follow the above cautions may result, worst case, in a short circuit and fuming when the products is used.

NOTICE

◆Soldering and Mounting

1.PCB Design

(1)Notice for Pattern Forms

●Unlike leaded components, chip components are susceptible to flexing stresses since they are mounted directly on the substrate.

They are also more sensitive to mechanical and thermal stresses than leaded components.

Excess solder fillet height can multiply these stresses and cause chip cracking. When designing substrates, take land patterns and dimensions into consideration to eliminate the possibility of excess solder fillet height.

●It has a possibility to happen the chip crack by the expansion and shrinkage of metal board. Please contact us if you want to use the ceramic capacitor on metal board such as Aluminum.

Pattern Forms

	Placing Close to Chassis	Placing of Chip Components and Leaded Components	Placing of Leaded Components after Chip Component	Lateral Mounting
prohibited				
Correct				

(2)Land Dimensions

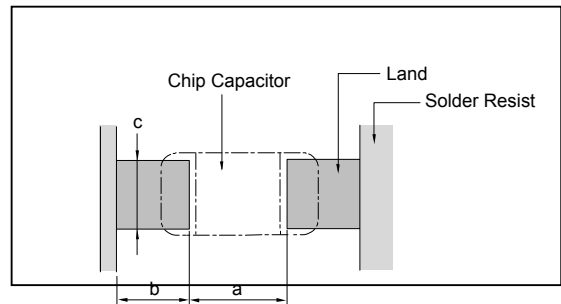


Table Reflow Soldering Method

Dimensions	Dimensions(L X W)	a	b	c
Part Number				
WBM15	1.0 X 0.5	0.3-0.5	0.35-0.45	0.4-0.6

(in : mm)

Inverting the PCB

Make sure not to impose an abnormal mechanical shock on the PCB.

4.Flux Application

●An excessive amount of flux generates a large quantity of flux gas, causing deteriorated solderability. So apply flux thinly and evenly throughout. (A foaming system is generally used for flow soldering).

●Flux containing too high a percentage of halide may cause corrosion of the outer electrodes unless sufficiently cleaning. Use flux with a halide content of 0.2% max.

But do not use strong acidic flux.

Do not use water-soluble flux*.

(*Water-soluble flux can be defined as non resin type flux including wash-type flux and non-wash-type flux.)

◆Others

1. Resin Coating

When selecting resin materials, select those with low contraction.

2. Circuit Design

These capacitors on this catalog are not safety recognized products.

3. Remarks

The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions. Select optimum conditions for operation as they determine the reliability of the product after assembly.

△ NOTE

- (1) This specification sheet is applied for CHIP MONOLITHIC CERAMIC CAPACITOR "WBM series" used for General Electronics equipment for your design.
- (2) Please contact our sales representative or product engineers before using our products for the application listed below.
 - ① Aircraft equipment
 - ② Aerospace equipment
 - ③ Undersea equipment
 - ④ Medical equipment
 - ⑤ Transportation equipment
 - ⑥ Traffic signal equipment
 - ⑦ Disaster prevention / crime prevention equipment
 - ⑧ Application of similar complexity and/or requirements to the applications listed in the above.
- (3) Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.
- (4) Use of Sn-Zn based solder will deteriorate reliability of MLCC. Please contact murata factory for the use of Sn-Zn based solder in advance.
- (5) This specification sheet has only typical specification because there is no space for detailed specifications. Therefore, please approve our product specification or transact the approval sheet for product specification before your ordering. Especially, please read rating and CAUTION (for storage, operating, rating, soldering, mounting, and handling) in them to prevent smoking and /or burning, etc.
- (6) Product material and design are subject to change without advance notice even though product specification is no change.
- (7) This specification has no room to accept your special requests.

If there are any questions, please contact our sales representatives or product engineers.

△ NOTE

1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
2. You are requested not to use our product deviating from this product specification.
3. Please return one copy of these specifications upon your acceptance.
If the copy is not returned by a day mentioned in a cover the specifications will be deemed to have been accepted.
4. We consider it not appropriate to include any terms and conditions with regard to the business transaction in the product specifications, drawings or other technical documents. Therefore, if your technical documents as above include such terms and conditions such as warranty clause, product liability clause, or intellectual property infringement liability clause, they will be deemed to be invalid.

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