

### **ES2ABF THRU ES2JBF**

DESCRIPTION

Cathode

Anode

Simplified outline SMBF and symbol

**PINNING** 

PIN

1

2

### **Surface Mount Superfast Recovery Rectifier**

Reverse Voltage – 50 to 600 V Forward Current -2 A

### **FEATURES**

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Superfast reverse recovery time
- Lead free in comply with EU RoHS 2011/65/EU directives

#### **MECHANICAL DATA**

• Case: SMBF

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 57mg / 0.002oz

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Absolute Maximum Ratings and Characteristics									
Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load									
derate current by 20%.									
Parameter	Symbols	E\$2ABF	ES2BBF	ES2CBF	E\$2DBF	E\$2EBF	ES2GBF	E\$2JBF	Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	150	200	300	400	600	٧
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	210	280	420	V
Maximum DC Blocking Voltage	VDC	50	100	150	200	300	400	600	V
Maximum Average Forward Rectified Current	I <sub>F(AV)</sub>				2				А

Maximum Average Forward Rectified Current at TL= 100 °C	I <sub>F(AV)</sub>	2				
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	IFSM	50				
Maximum Forward Voltage at 2A	V <sub>F</sub>	1	1.25	1.65	٧	
Maximum DC Reverse Current Ta = 25 °C at Rated DC Blocking Voltage Ta = 125 °C	I <sub>R</sub>	5 100			μA	
Typical Junction Capacitance at V <sub>R</sub> =4V, f=1MHz	Cj	45			рF	
Maximum Reverse Recovery Time	t <sub>rr</sub>	35			ns	

 $R_{\scriptscriptstyle{\theta JA}}$ 

 $T_j,\,T_{stg}$ 

Operating and Storage Temperature Range

at | F=0.5A, | R=1A, | FF=0.25A

Typical Thermal Resistance 2)

65

-55~+150

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°C/W

°C

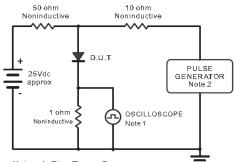
<sup>1)</sup> Measured with  $I_F = 0.5 A$ ,  $I_R = 1 A$ ,  $I_{rr} = 0.25 A$ 

<sup>2 )</sup> P.C.B. mounted with 0.5 X 0.5" (12.7 X 12.7 mm) copper pad areas.



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Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram



- Note: 1. Rise Time = 7ns, max.
  Input Impedance = 1megohm,22pF.
  - 2. Ries Time = 10ns, max. Source Impedance = 50 ohms.

Fig.2 Maximum Average Forward Current Rating

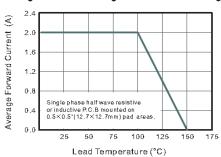


Fig.4 Typical Forward Characteristics

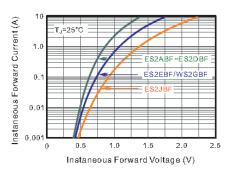
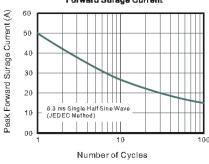


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current



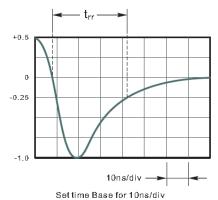


Fig.3 Typical Reverse Characteristics

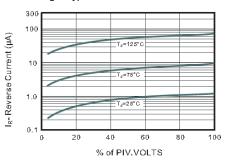
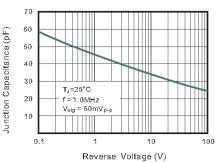


Fig.5 Typical Junction Capacitance



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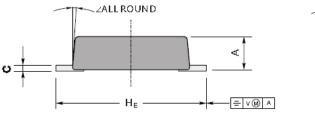


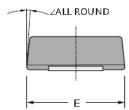
# **ES2ABF THRU ES2JBF**

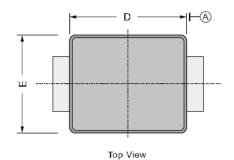
### PACKAGE OUTLINE

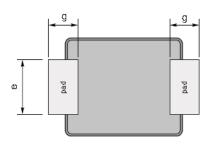
Plastic surface mounted package; 2 leads







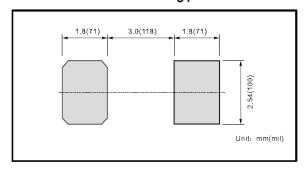




Bottom View

UNIT		Α	С	D	Е	H <sub>E</sub>	е	g	2
mm	max	1.3	0.26	4.4	3.7	5.5	2.2	1.0	9°
mm	min	1.1	0.18	4.2	3.5	5.1	1.9		
mil	max	51	10	173	146	216	86	40	9
'''''	min	43	7	165	138	200	75	40	

#### The recommended mounting pad size



### Marking

Type number	Marking code
ES2ABF	E2AB
ES2BBF	E2BB
ES2CBF	E2CB
ES2DBF	E2DB
ES2EBF	E2EB
ES2GBF	E2GB
ES2JBF	E2JB

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