



NCE N-Channel Enhancement Mode Power MOSFET

Description

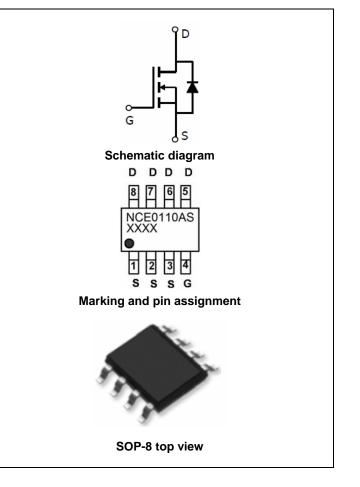
The NCE0110AS uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- $V_{DS} = 100V, I_D = 10A$ $R_{DS(ON)} < 17m\Omega @ V_{GS} = 10V$ (Typ:14m Ω) $R_{DS(ON)} < 20m\Omega @ V_{GS} = 4.5V$ (Typ:15.2m Ω)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

Application

- DC/DC Primary Side Switch
- Telecom/Server
- Synchronous Rectification



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0110AS	NCE0110AS	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	10	А
Drain Current-Continuous(T _C =100 ℃)	l _D (100℃)	7	А
Pulsed Drain Current	I _{DM}	70	А
Maximum Power Dissipation	PD	3.1	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ extsf{ heta}JA}$	40	°C /W



Electrical Characteristics (T_A=25 $^\circ\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	100	110	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·			•		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	0.9	1.3	1.8	V
Durain Courses On State Desistance	e R _{DS(ON)}	V _{GS} =10V, I _D =10A	-	14	17	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _D =10A	-	15.2	20	mΩ
Forward Transconductance	g fs	V _{DS} =10V,I _D =10A	-	26	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}		3000	3835	4200	PF
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,	-	178	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	153	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	13	-	nS
Turn-on Rise Time	tr	V_{DD} =50V,I _D =10A,R _L =5Ω,	-	14	-	nS
Turn-Off Delay Time	t _{d(off)}	R _G =1Ω,V _{GS} =10V	-	25	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg		-	90	-	nC
Gate-Source Charge	Q _{gs}	I _D =10A,V _{DD} =50V,V _{GS} =10V	-	10	-	nC
Gate-Drain Charge	Q _{gd}		-	24	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =10A	-	0.85	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	10	А
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 10A	-	33		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	54		nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

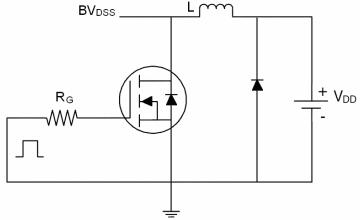


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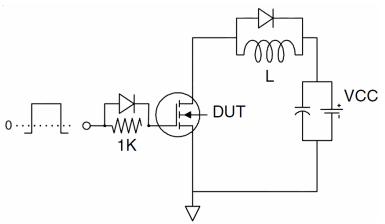


Test Circuit

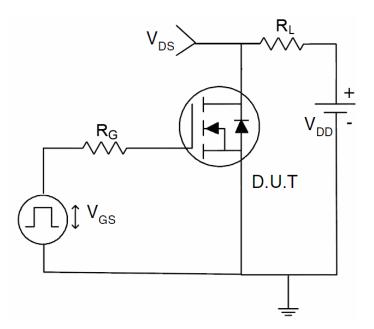
1) E_{AS} test Circuit



2) Gate charge test Circuit



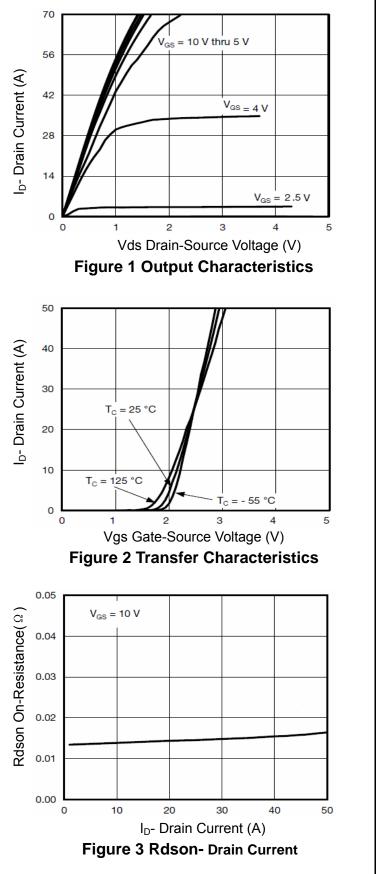
3) Switch Time Test Circuit

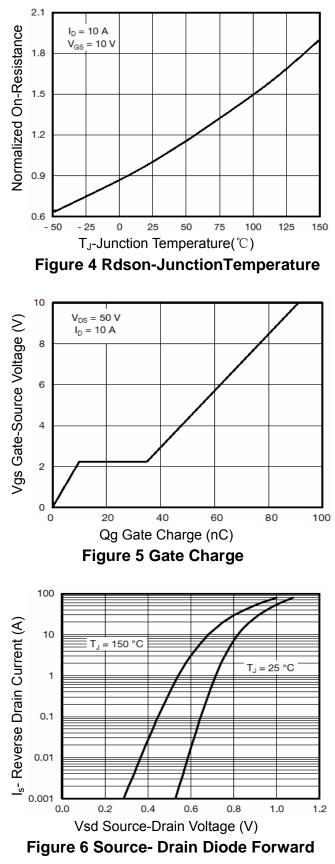






Typical Electrical and Thermal Characteristics (Curves)







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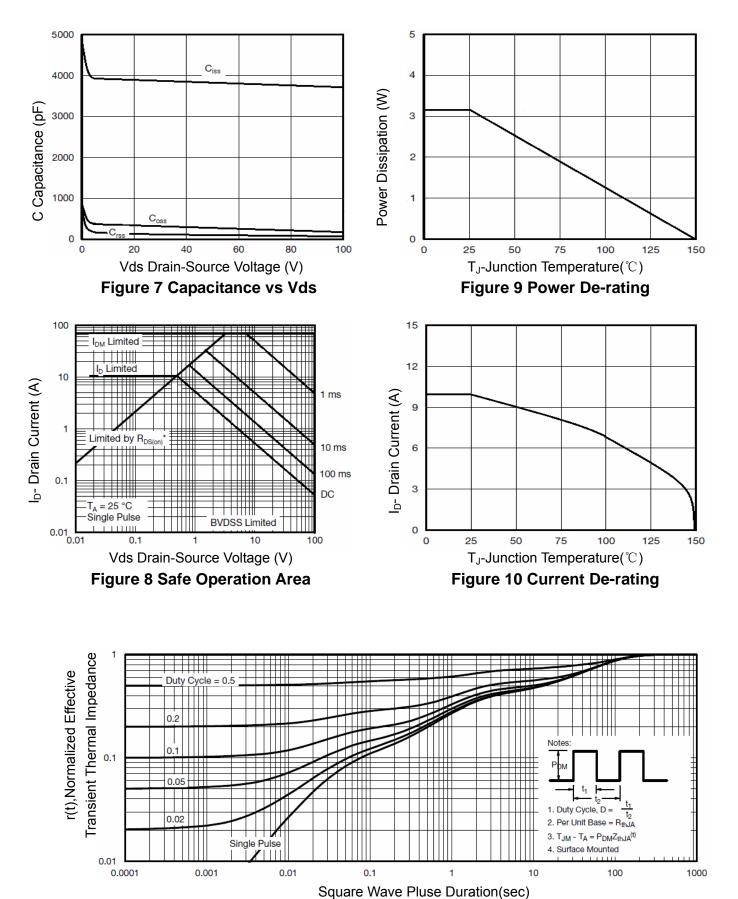


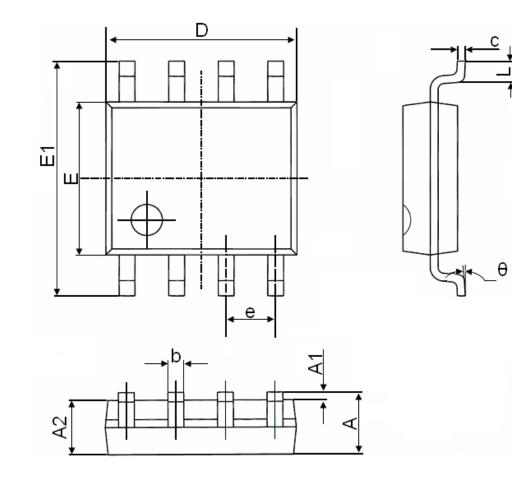
Figure 11 Normalized Maximum Transient Thermal Impedance



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SOP-8 Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	





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