SHANGHAI SINO-IC MICROELECTRONICS CO., LTD.

June 2007

SE2306

N-Channel Enhancement Mode Field Effect Transistor

			1			R	evision:B	
Features • $V_{DS} = 20V, I_D = 6A$ $R_{DS(ON)} < 34.5m\Omega @ V_{GS} = 2.5V$ $R_{DS(ON)} < 24.5m\Omega @ V_{GS} = 4.5V$ • High Power and current handing capability • Lead free product is acquired • Surface Mount Package								
 Applications ● Load switch ● Power management 			(SOT-23) Top View					
Construction● Silicon epitaxial planer				:	s –			
Absolute maximum ratings (Ta=25℃)								
Parameter		Symbol		Limits			Unit	
rain-Source Voltage		V _{DS}		20			V	
Gate-Source Voltage		V _{GS}		±10			V	
Drain Current-Continuous@		l _D		6			A	
Current-Pulsed (Note 1)		I _{DM}		25			A	
Maximum Power Dissipation		P _D		1.5			W	
Operating Junction and Storage Temperature Range			T _J ,T _{STG}		-55 To 150		°C	
THERMAL CHARACTERISTIC	S							
Thermal Resistance, Junction-to-Ambient (No			te 2) R _{eJA}		83		°C/W	
Electrical characteristics (Ta=	25℃))					1	
Parameter	Syn	nbol	Condition	S	Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV _{DSS}		V _{GS} =0V I _D =250µA		20			V
Zero Gate Voltage Drain Current	I _{DSS}		V _{DS} =20V,V _{GS} =0V				0.8	μA
Gate-Body Leakage Current	l _G	SS	V _{GS} =±10V,V _{DS} =0V				±80	nA
ON CHARACTERISTICS (Note 3)								

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Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.45	0.65	1.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.5A		21	24.5	mΩ
		V _{GS} =2.5V, I _D =3.5A		30	34.5	mΩ
Forward Transconductance	9 _{FS}	V _{DS} =5V,I _D =4.5A	3			S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{Iss}			600		PF
Output Capacitance	C _{oss}	V _{DS} =8V,V _{GS} =0V, F=1.0MHz		330		PF
Reverse Transfer Capacitance	C _{rss}			140		PF
SWITCHING CHARACTERISTICS (Note						
4)						
Turn-on Delay Time	t _{d(on)}			10	20	nS
Turn-on Rise Time	t _r	V _{DD} =10V,I _D =1A		11	25	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5V, R_{GEN} =6 Ω		35	70	nS
Turn-Off Fall Time	t _f			30	60	nS
Total Gate Charge	Q _g			10	15	nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =6A, V=4.5V		2.3		nC
Gate-Drain Charge	Q_{gd}			3		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	$V_{_{\rm SD}}$	V=0V,I=1.7A			1.2	V
Diode Forward Current (Note 2)	۱ _s			1.7		A

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NOTES:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t \leq 10 sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production testing.



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NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,1982

2. CONTROLLING DIMENSION: INCH.

DIM	IN	ICHES	MILLIMETERS		
	MIN	MAX	MIN	MAX	
А	0.1102	0.1197	2.80	3.04	
в	0.0472	0.0551	1.20	1.40	
С	0.0350	0.0440	0.89	1.11	
D	0.0150	0.0200	0.37	0.50	
G	0.0701	0.0807	1.78	2.04	
н	0.0005	0.0040	0.013	0.100	
J	0.0034	0.0070	0.085	0.177	
К	0.0140	0.0285	0.35	0.69	
L	0.0350	0.0401	0.89	1.02	
S	0.0830	0.1039	2.10	2.64	
v	0.0177	0.0236	0.45	0.60	

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