GENERAL DESCRIPTION

OB2225N is a high performance, high precision and low cost PWM Power switch for non-isolated buck and buck-boost application. It combines a dedicated current mode PWM controller with a high voltage power Mosfet in SOP8 package. Its built-in error amplifier is optimized for good overshoot and dynamic response for low cost and component count. With precise inner resistor divider, precise reference of EA， constant voltage regulation of 12V at universal AC input can be guaranteed. Frequency reduction and burst mode control is implemented for high efficiency at light load. Good EMI performance is achieved with On-Bright proprietary frequency shuffling technique and soft gate driver design. Low startup current and low operating current contribute to a reliable power on startup and low standby power consumption with OB2225N.

OB2225N offers power on soft start control and protection coverage with auto-recovery features including cycle-by-cycle current limiting, output short circuit protection, on-chip Over Temperature Protection (OTP), VDD Over Voltage Protection (OVP), Over Loading Protection(OLP) and VDD Under Voltage Lockout Protection (UVLO).

The tone energy at below 20KHz is minimized in the design so that audio noise is eliminated during operation.

OB2225N is offered in SOP8 package.

FEATURES

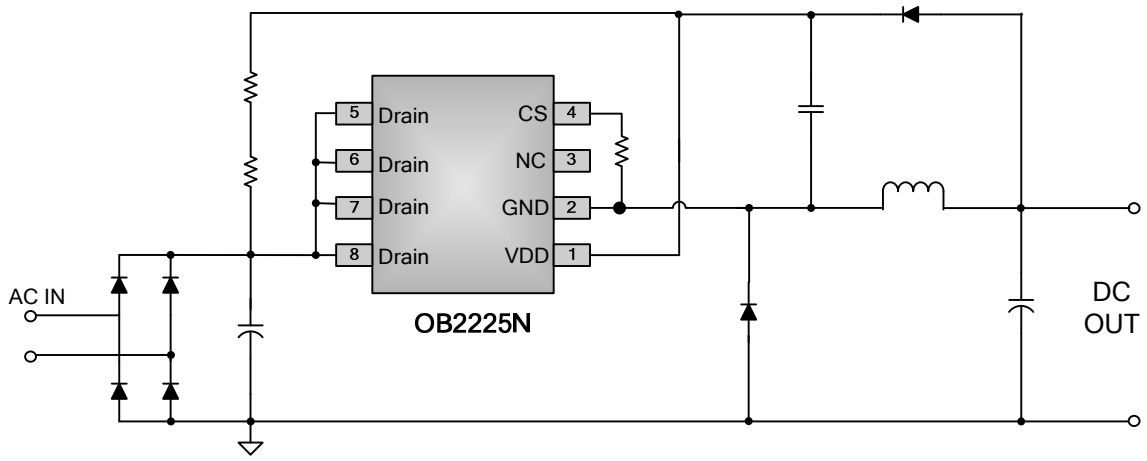
- Universal AC input range and 12V output voltage
- Low cost and less BOM for buck and buck-boost applications
- Current mode control
- 40kHz (typical) maximum switching frequency
- Frequency-reduction and burst mode control for high efficiency
- Frequency shuffling for EMI improvement
- Power on soft-start
- Built-in Leading Edge Blanking (LEB)
- Cycle-by-cycle current limiting
- Output short-circuit protection
- VDD Under Voltage Lockout with Hysteresis
- VDD OVP
- Over Loading Protection
- On-Chip OTP

APPLICATIONS

Low power AC/DC offline SMPS for

- Small home appliance
- Linear regulator/RCC replacement

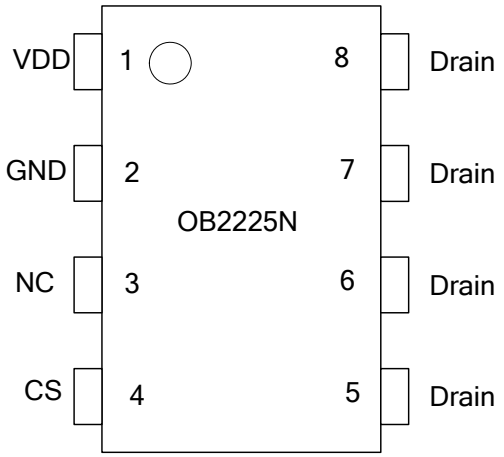
TYPICAL APPLICATION



GENERAL INFORMATION

Pin Configuration

The pin map is shown as below for SOP8



Ordering Information

Part Number	Description
OB2225NCP	SOP8, Pb-free, Tube
OB2225NCPA	SOP8, Pb-free, T&R

Package Dissipation Rating

Package	RθJA (℃/W)
SOP8	90

Note: Drain Pin Connected 100mm^2 PCB copper clad.

Output Power Table

Topology	90~264Vac (open frame)	176~264Vac (open frame)
Buck / Buck-Boost	350mA	400mA

Note: Maximum continuous power with drain pattern connected 100mm^2 PCB copper clad, at 50℃ ambient.

Topology	90~264Vac (open frame)	176~264Vac (open frame)
Buck / Buck-Boost	300mA	350mA

Note: Maximum continuous power with drain pattern connected 100mm^2 PCB copper clad, at 85℃ ambient.

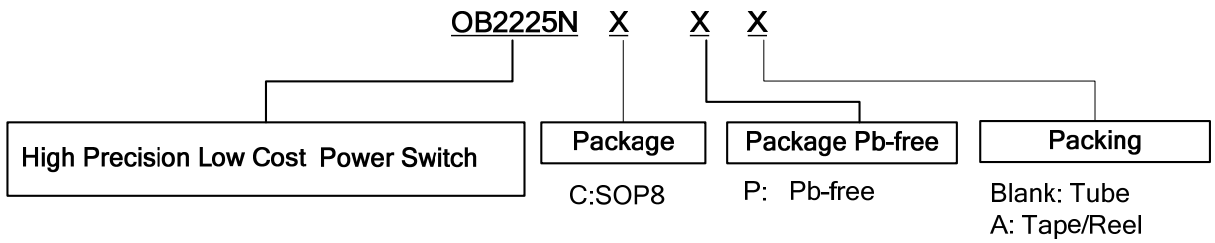
Absolute Maximum Ratings

Parameter	Value
Drain Voltage(off state)	-0.3V to Bvdss
VDD Voltage	-0.3 to 20V
CS Input Voltage	-0.3 to 7V
Min/Max Operating Junction Temperature T_J	-40 to 150 ℃
Operating Ambient Temperature T_A	-40 to 85 ℃
Min/Max Storage Temperature T_{stg}	-55 to 150 ℃
Lead Temperature (Soldering, 10secs)	260 ℃

Note: Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute maximum-rated conditions for extended periods may affect device reliability.

Recommended Operating Condition

Symbol	Parameter	Range
VDD	VDD Supply Voltage	8 to 12V



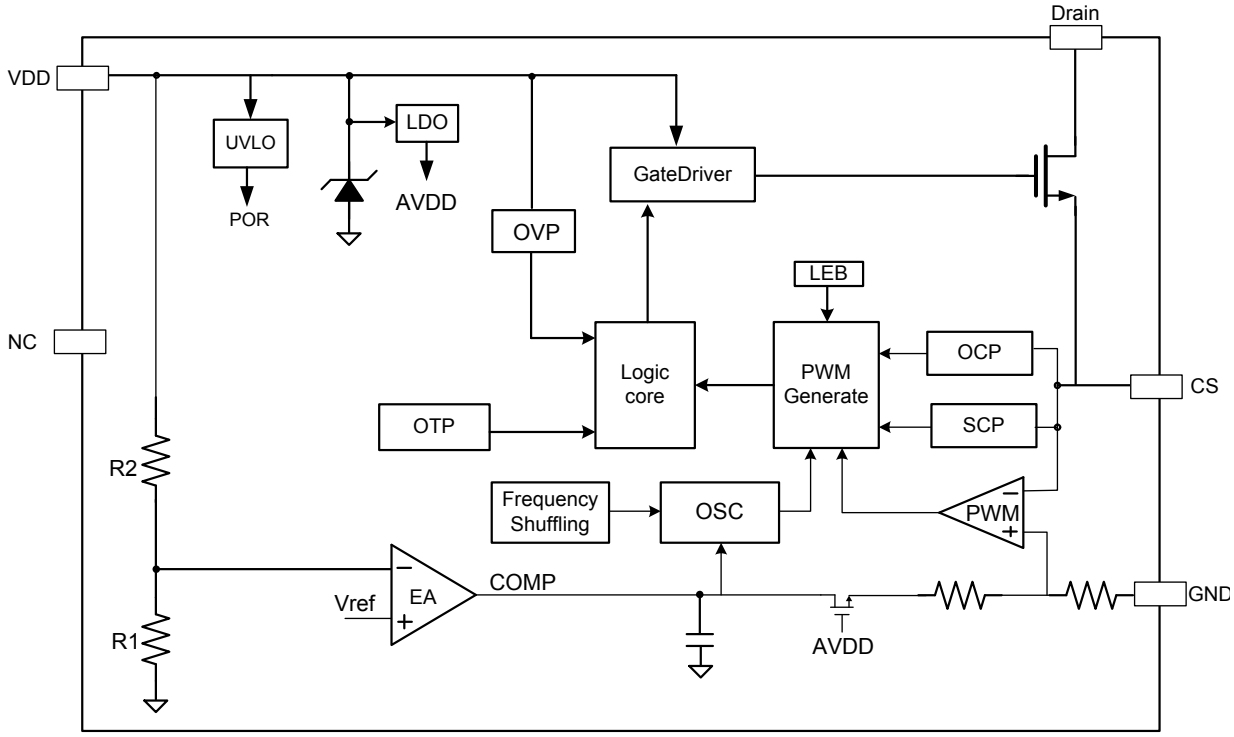
Marking Information

Y:Year Code
WW:Week Code(01-52)
ZZZ:Lot Code
C:SOP8 Package
P:Pb-free Package
S:Internal Code(Optional)

TERMINAL ASSIGNMENTS

Pin Num	Pin Name	I/O	Description
1	VDD	I	Power Supply and Output Voltage Feedback
2	GND	P	Ground
3	NC	NC	It should be floating or connect ground during normal operation state
4	CS	I	Current sense input
5/6/7/8	Drain	O	Power Mosfet Drain pins.

BLOCK DIAGRAM

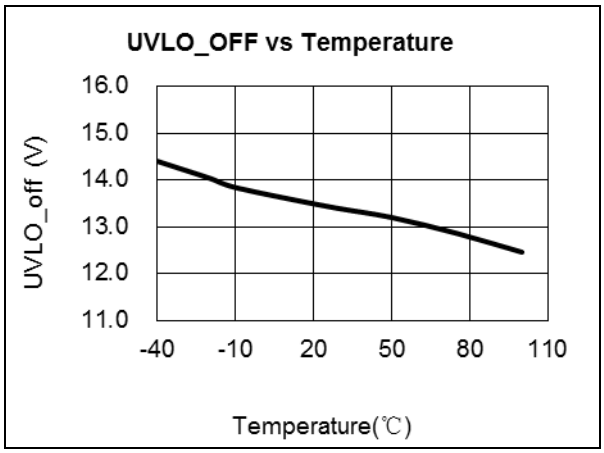


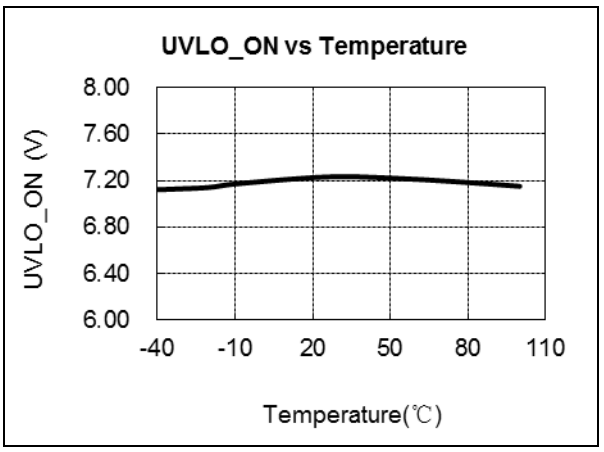
ELECTRICAL CHARACTERISTICS

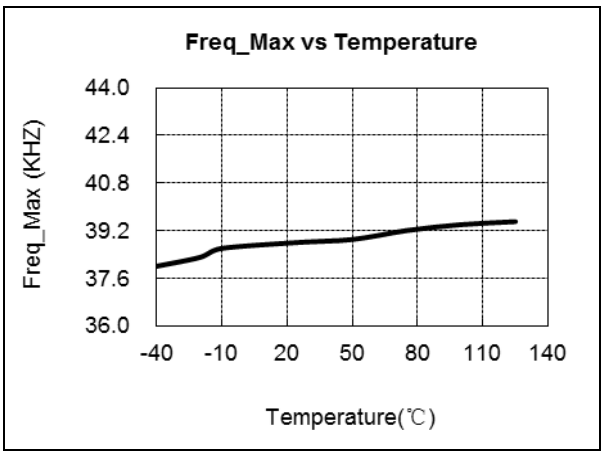
(T_A = 25℃, VDD=12V, if not otherwise noted)

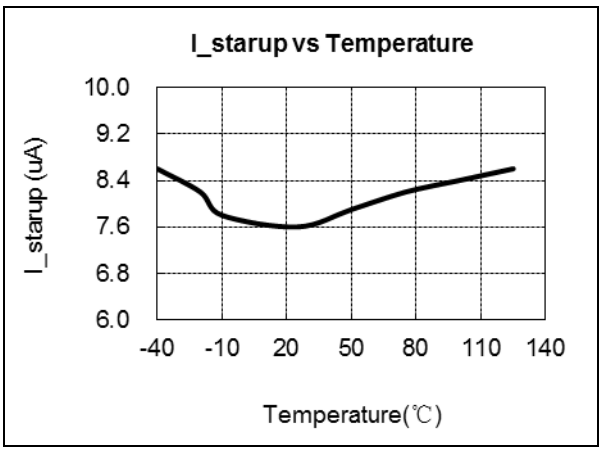
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
Supply Voltage (VDD) Section						
$I_{\_startup}$	Standby Current	VDD=UVLO(off) -1V			10	uA
$I_{\_VDD\_op}$	Operation Current	Operation supply current CS=0V	-	1.0	2.0	mA
		Operation supply current VDD=13V		0.45	0.6	mA
UVLO_ON	VDD Under Voltage Lockout Enter	VDD falling, gate disappear	6.8	7.2	7.6	V
UVLO_OFF	VDD Under Voltage Lockout Exit	VDD rising	12.5	13.5	14.5	V
OVP	Over voltage protection Threshold	Ramp VDD until gate shut down	15	16	17	V
VDD Regulation Voltage	In normal regulation, VDD will be regulated to average of 12.6V			12.6		V
Current Sense Input Section						
TLEB	LEB time		150	200	250	ns
Vth_oc	Over current detection Threshold voltage		550	575	600	mV
Td_oc	OCP propagation delay			200		ns
Vth_scp	Short Current protection threshold voltage		0.6	0.7	0.8	V
Vth_flt	CS floating protection threshold voltage		0.9	1.0	1.1	V
EA Section						
Vref	EA reference		1.44	1.5	1.56	V
Kvdd/vref	VDD divider coefficient			8.5		
Frequency Section						
Freq_Max	IC Maximum frequency		36	40	44	KHz
△f/Freq	Frequency shuffling range			+/-8		%
F_shuffling	Shuffling frequency			75		Hz
Dmax	Maximum Duty Cycle		47	50	53	%
F_Burst	Burst Mode Switch Frequency			20		KHz
Protection Section						
Td_olp	Over Loading Debounce Time			130		ms
OTP	Power MOSFET temperature for exiting over temperature protection			135		℃
	Power MOSFET temperature for entering over temperature protection			160		℃
Power Mosfet Section						
BVdss	MOSFET Drain-Source Breakdown Voltage		600			V
Rdson	On Resistance	Static, Id=0.45A			15	Ω

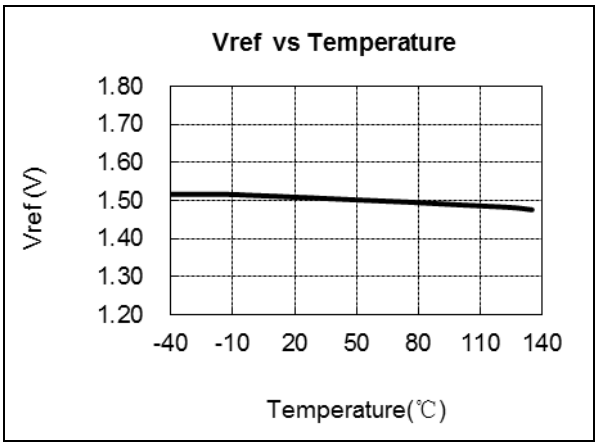
CHARACTERIZATION PLOTS

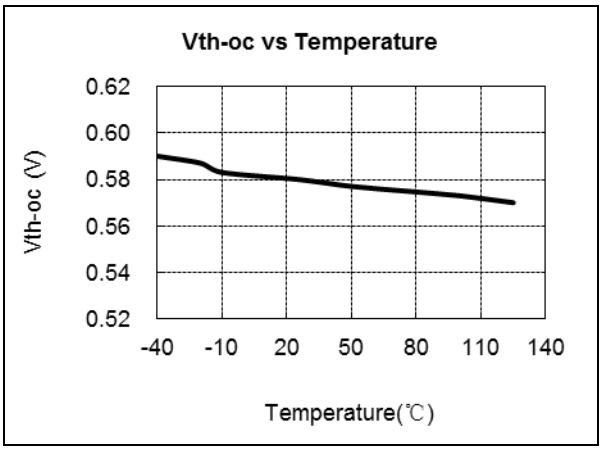












OPERATION DESCRIPTION

OB2225N is a cost effective PWM power switch optimized for off-line non-isolated buck or buck-boost applications for small home appliances and linear regulator replacement. It operates in current mode and regulates output voltage with dedicated features. High integration can afford low cost and component count solution.

Startup Current and Start up Control
Startup current of OB2225N is designed to be very low so that VDD could be charged up above UVLO threshold and starts up quickly. A large value startup resistor can therefore be used to minimize the power loss in application.

Operating Current
The Operating current of OB2225N is as low as 1.0mA (typical). Good efficiency is achieved with the low operation current together with 'Multi-mode' control features.

PWM operation
The maximum switching frequency of OB2225N is internally fixed at 40KHz (typical). No external frequency setting components are required for PCB design simplification.

At light load or zero load condition, most of the power dissipation in a switching mode power supply is from switching loss on the MOSFET. The magnitude of power loss is in proportion to the switching frequency. Lower switching frequency leads to the reduction on the power loss and thus conserves the energy. The frequency reduction and burst mode operation are implemented to achieve high efficiency at light load. The minimum switching frequency is 20KHz (typical).

Frequency shuffling for EMI improvement
The frequency shuffling (switching frequency modulation) is implemented in OB2225N. The oscillation frequency is modulated so that the tone energy is spread out. The spread spectrum minimizes the conduction band EMI and therefore eases the system design.

Soft Start
OB2225N features an internal 36 cycles (typical) soft start to soften the electrical stress occurring in the power supply during startup. It is activated during the power on sequence. After VDD reaches UVLO(OFF), the switching frequency is gradually increased from 10KHz to 40KHz. Every restart up is followed by a soft start.

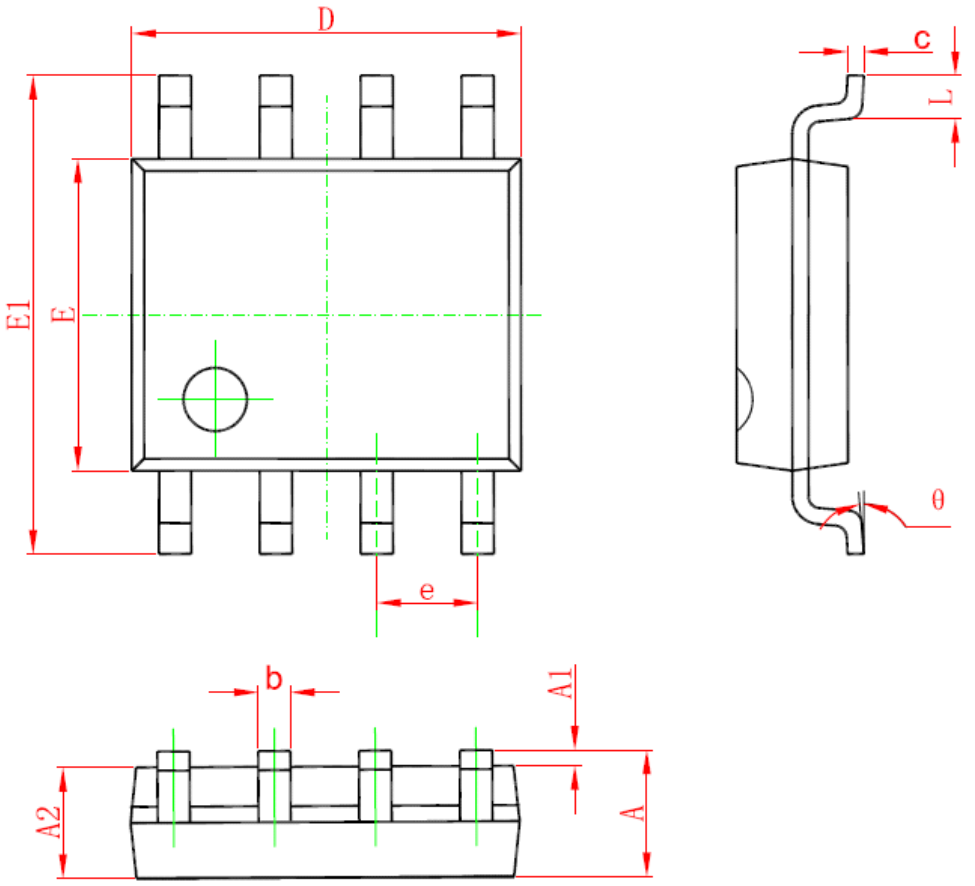
Current Sensing and Leading Edge Blanking
Cycle-by-Cycle current limiting is offered in OB2225N current mode PWM control. The switch current is detected by a sense resistor into the CS pin. An internal leading edge blanking circuit chops off the sensed voltage spike at initial internal MOSFET on state so that the external RC filtering on sense input is no longer needed. The PWM duty cycle is determined by the current sense input voltage and the EA output voltage.

Gate Driver
The internal power MOSFET in OB2225N is driven by a dedicated gate driver for power switch control. Too weak the gate drive strength results in higher conduction and switch loss of MOSFET while too strong gate drive compromises EMI.
A good tradeoff is achieved through the built-in totem pole gate design with right output strength control.

Protection Control
Good power supply system reliability is achieved with its rich protection features including cycle-by-cycle current limiting, Output short circuit protection, on-chip Over Temperature Protection (OTP), VDD Over Voltage Protection (OVP), Over Loading Protection(OLP) and VDD Under Voltage Lockout Protection (UVLO).

PACKAGE MECHANICAL DATA

SOP8 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Min
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
b	0.310	0.510	0.012	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.150	0.185	0.203
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.05 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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