

DESCRIPTION

The ICPL3120 consists of an Infrared Light Emitting Diode optically coupled to an Integrated Circuit with a Power Driving Output. ICPL3120 is ideally suitable to drive the Power IGBT and MOSFET in Inverters of Motor Controls and in Power Supplies.

The 2.5A peak output current is capable to direct drive IGBT/MOSFET up to ratings of 1200V/100A. For IGBTs with higher ratings, ICPL3120 can be used to drive a discrete power stage which drives the IGBT gate.

FEATURES

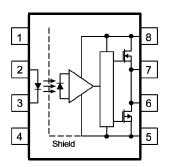
- ±2.5A Maximum Peak Output Current
- 35kV/μs Minimum Common Mode Rejection at V_{CM} 1500V
- Maximum Propagation Delay 500ns
- Maximum Propagation Delay Difference 100ns
- Wide Operating Voltage Range V_{CC} 15 to 30 V
- Maximum Supply Current I_{CC} 3.5mA
- Under Voltage Lock Out (UVLO) Protection with Hysteresis
- Guaranteed Performance over Temperature Range - 40°C to +105°C
- MSL 1
- Lead Free and RoHS Compliant
- Safety Approvals Pending

APPLICATIONS

- IGBT/MOSFET Gate Drive
- UPS
- Inverters
- AC Brushless and DC Motor Drives

ORDER INFORMATION

- Add G after PN for 10mm lead spacing
- Add SM after PN for Surface Mount
- Add SMT&R after PN for Surface Mount Tape & Reel





- 1 NC
- 2 Anode
- 3 Cathode
- 4 NC
- $5 \quad \text{GND} \left(V_{EE} \right)$

±2.5A

250mW

- 6 V₀
- $7 V_0$
- 8 V_{CC}

A 0.1µF bypass Capacitor must be connected between Pins 8 and 5.

ABSOLUTE MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Stresses exceeding the absolute maximum ratings can cause permanent damage to the device.

Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Input

20mA
1A
500ns
5V
45mW

Output

Peak Output Current

(Exponential waveform,

50kHz
0V to 35V
0V to V_{CC}

Total Package

Power Dissipation

Isolation Voltage	$5000V_{\text{RMS}}$
Total Power Dissipation	295mW
Operating Temperature	-40 to 105 °C
Storage Temperature	-55 to 125 °C
Junction Temperature	125 °C Max
Lead Soldering Temperature (10s)	260°C

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Truth Table

LED	V _{CC} – V _{EE} (Turn ON, +ve going)	V _{cc} – V _{EE} (Turn OFF –ve going)	Vo
OFF	0 – 30V	0 - 30V	LOW
ON	0 – 11.0V	0 – 9.5V	LOW
ON	11.0 – 13.5V	9.5 – 12.0V	TRANSITION
ON	13.5 – 30V	12 – 30V	HIGH

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Operating Temperature	T_{A}	- 40	105	°C
Supply Voltage	$V_{CC} - V_{EE}$	15	30	V
Input Current (ON)	$I_{F(ON)}$	7	16	mA
Input Voltage (OFF)	$V_{F(OFF)}$	0	0.8	V



ELECTRICAL CHARACTERISTICS (Typical Values at V_{CC} – V_{EE} = 30V and T_A = 25°C, Minimum and Maximum Values at Recommended Operating Conditions, unless otherwise specified)

INPUT

Parameter	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward Voltage	V_{F}	$I_F = 10 \text{mA}$	1.2	1.37	1.8	V
Forward Voltage Temperature Coefficient	$\Delta V_F/\Delta T$	$I_F = 10 \text{mA}$		-1.237		mV/°C
Reverse Voltage	V_R	$I_R = 10\mu A$	5			V
Input Threshold Current (Low to High)	I_{FLH}	$V_{CC} = 30V$ $V_{O} > 5V$		1.8	5	mA
Input Threshold Voltage (High to Low)	$ m V_{FHL}$	$V_{CC} = 30V$ $V_{O} < 5V$	0.8			V
Input Capacitance	C_{IN}	$V_F = 0V$, $f = 1MHz$		33		pF

OUTPUT

Parameter	Symbol	Test Condition	Min	Тур.	Max	Unit
High Level Supply Current	I_{CCH}	$I_F = 10$ mA, $V_{CC} = 30$ V $V_O = Open$		2.4	3.5	mA
Low Level Supply Current	I_{CCL}	$I_F = 0$ mA, $V_{CC} = 30$ V $V_O = O$ pen		2.5	3.5	mA
High Level Output Current	I_{OH}	Maximum Pulse Width = 50μ s $V_O = V_{CC} - 1.5V$			-1.0	A
		$\begin{aligned} \text{Maximum Pulse Width} &= 10 \mu s \\ V_O &= V_{CC} - 4 V \end{aligned}$			-2.5	
Low Level Output Current	I_{OL}	Maximum Pulse Width = 50μ s $V_O = V_{EE} + 1.5V$	1.0			A
		$\begin{aligned} \text{Maximum Pulse Width} &= 10 \mu s \\ V_O &= V_{EE} + 4 V \end{aligned}$	2.5			
High Level Output Voltage	V_{OH}	$I_F = 10mA, I_O = -100mA$	V _{CC} -0.3	V _{CC} -0.1		V
Low Level Output Voltage	V_{OL}	$I_F = 0 \text{mA}, I_O = 100 \text{mA}$		V _{EE} +0.1	V _{EE} +0.25	V
UVLO Threshold	$V_{\rm UVLO+}$	$V_O > 5V$, $I_F = 10mA$	11.0	12.7	13.5	V
	V _{UVLO-}	$V_{O} < 5V, I_{F} = 10mA$	9.5	11.2	12.0	V
UVLO Hysteresis	UVLO _{HYS}			1.5		V



$\begin{array}{c} \textbf{ELECTRICAL CHARACTERISTICS} \text{ (Typical Values at V}_{\text{CC}} - \text{V}_{\text{EE}} = 30\text{V and T}_{\text{A}} = 25^{\circ}\text{C}, \\ \text{Minimum and Maximum Values at Recommended Operating Conditions,} \end{array}$

unless otherwise specified)

SWITCHING

Parameter	Symbol	Test Condition	Min	Тур.	Max	Unit
Propagation Delay Time to High Output Level	t _{PLH}	$I_F = 7 \text{ to } 16\text{mA},$ $V_{CC} = 15 \text{ to } 30\text{V},$ $V_{EE} = 0\text{V},$	50	130	500	ns
Propagation Delay Time to Low Output Level	$t_{ m PHL}$	$Rg = 10\Omega,$ $Cg = 25nF,$	50	130	500	
Pulse Width Distortion t _{PHL} - t _{PLH} for any given device	PWD	f = 10kHz, Duty Cycle = 50%		5	70	
Propagation Delay Difference (t _{PHL} - t _{PLH}) between any two Devices	PDD		-100		100	
Output Rise Time (10% to 90%)	t _r			35		
Output Fall Time (90% to 10%)	t_{f}			35		
UVLO Turn On Delay	t _{UVLO ON}	$I_F = 10 \text{mA}, V_O > 5 \text{V}$		1.6		μs
UVLO Turn Off Delay	t _{UVLO OFF}	$I_F = 10 \text{mA}, V_O < 5 \text{V}$		0.4		μs
Common Mode Transient Immunity at High Output Level	CM _H	$I_F = 10 \text{ to } 16\text{mA},$ $V_{CC} = 30\text{V},$ $V_{CM} = 1500\text{V},$ $T_A = 25^{\circ}\text{C}$	35	50		kV/μs
Common Mode Transient Immunity at Low Output Level	CM_L	$V_F = 0V,$ $V_{CC} = 30V,$ $V_{CM} = 1500V,$ $T_A = 25^{\circ}C$	35	50		kV/μs



 $\begin{array}{c} \textbf{ELECTRICAL CHARACTERISTICS} \text{ (Typical Values at V}_{\text{CC}} - \text{V}_{\text{EE}} = 30\text{V and T}_{\text{A}} = 25^{\circ}\text{C}, \\ \text{Minimum and Maximum Values at Recommended Operating Conditions,} \end{array}$ unless otherwise specified)

ISOLATION

Parameter	Symbol	Test Condition	Min	Тур.	Max	Unit
Insulation Voltage	$V_{\rm ISO}$	R.H. = 40% - 60% , $T_A = 25$ °C t = 1 min,	5000			V
Input - Output Resistance	R _{I-O}	$V_{\text{I-O}} = 500 \text{VDC}$		10 ¹²		Ω
Input - Output Capacitance	C _{I-O}	f = 1MHz		0.92		pF

Note:

- 1. A 0.1uF or bigger bypass capacitor must be connected across pin 8 and pin 5.
- 2. PDD is the difference of t_{PHL} and t_{PLH} between any two ICPL3120 under same test conditions.
- 3. Common Mode Transient Immunity in High stage is the maximum tolerable negative dV_{CM}/dt on the trailing edge of the common mode impulse signal, V_{CM} , to assure that the output will remain high ($V_O > 15V$).
- Common Mode Transient Immunity in Low stage is the maximum tolerable positive dV_{CM}/dt on the leading edge of the common mode impulse signal, V_{CM} , to assure that the output will remain low $(V_O < 1V)$. 4.



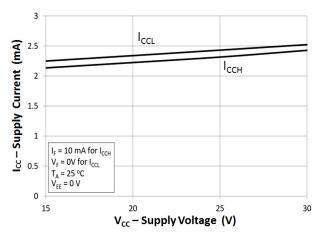


Fig 1 Supply Current vs Supply Voltage

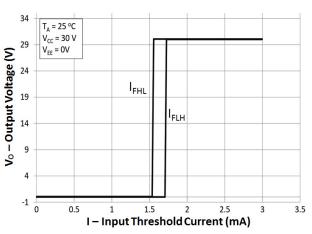


Fig 3 Transfer Characteristics

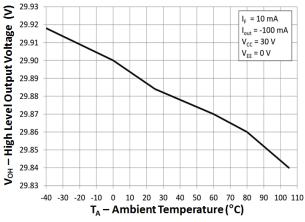


Fig 5 Output High Voltage vs Ambient Temperature

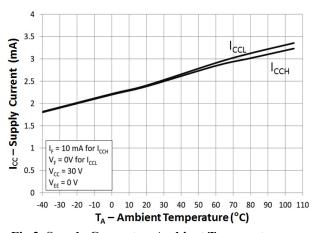


Fig 2 Supply Current vs Ambient Temperature

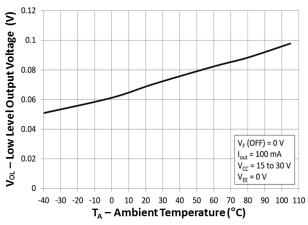


Fig 4 Output Low Voltage vs Ambient Temperature

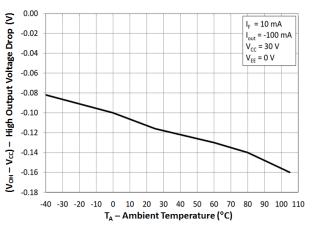


Fig 6 Output High Voltage Drop vs Ambient Temperature



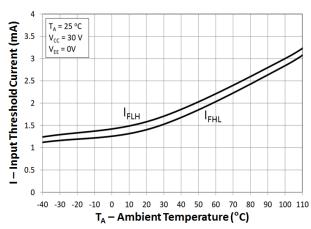


Fig 7 Input Threshold Current vs Ambient Temperature

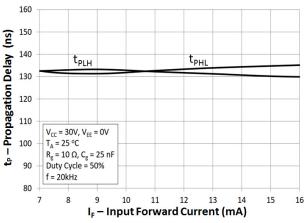


Fig 9 Propagation Delay vs Forward Current

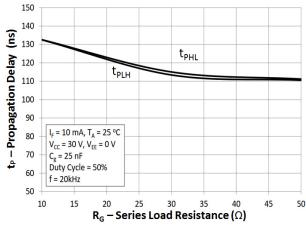


Fig 11 Propagation Delay vs Series Load Resistance

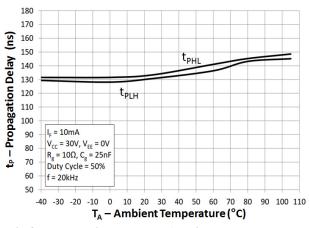


Fig 8 Propagation Delay vs Ambient Temperature

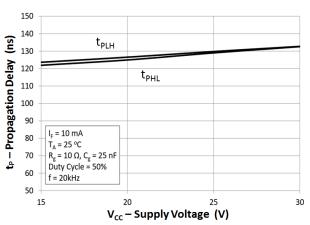


Fig 10 Propagation Delay vs Supply Voltage

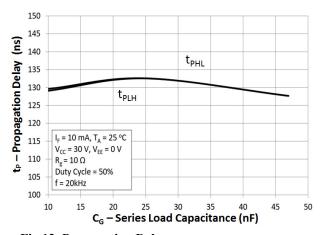


Fig 12 Propagation Delay vs Series Load Capacitance



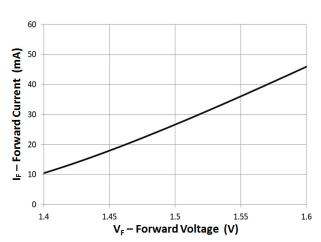
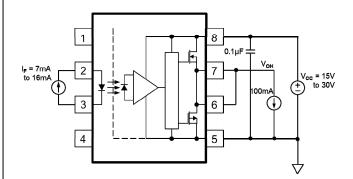
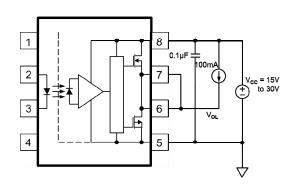


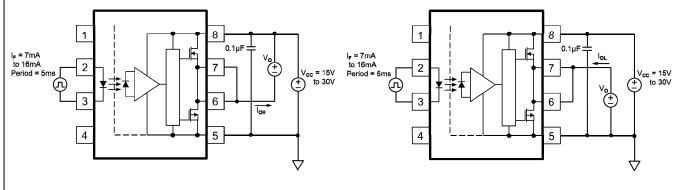
Fig 13 Forward Current vs Forward Voltage



V_{OH} Test Circuit



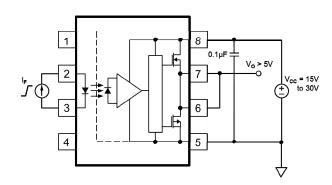
 $V_{OL} \ Test \ Circuit$

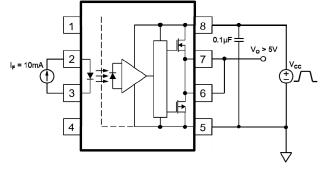


I_{OH} Test Circuit

I_{OL} Test Circuit

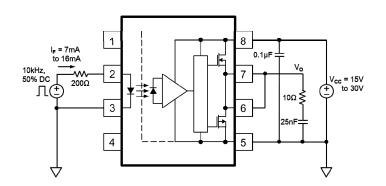


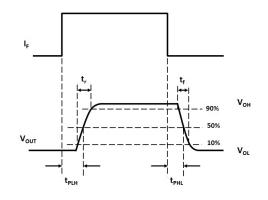




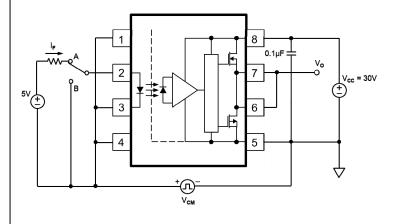
I_{FLH} Test Circuit

UVLO Test Circuit

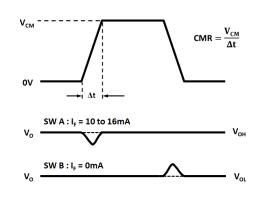




 $t_{r},\,t_{f},\,t_{PLH}$ and $t_{PHL}\,Test$ Circuit



CMR Test Circuit



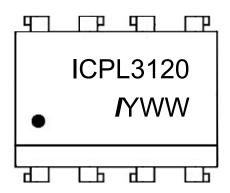
9 13/06/2018 DD93213



ORDER INFORMATION

	ICPL3120						
After PN	PN	Description	Packing quantity				
None	ICPL3120	Standard DIP8	50 pcs per tube				
G	ICPL3120G	10mm Lead Spacing	50 pcs per tube				
SM	ICPL3120SM	Surface Mount	50 pcs per tube				
SMT&R	ICPL3120SMT&R	Surface Mount Tape & Reel	1000 pcs per reel				

DEVICE MARKING



ICPL3120 denotes Device Part Number

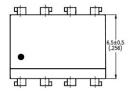
I denotes Isocom

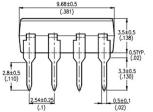
Y denotes 1 digit Year code WW denotes 2 digit Week code

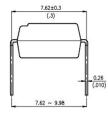


PACKAGE DIMENSIONS in mm (inch)

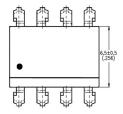
DIP

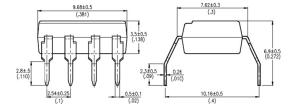




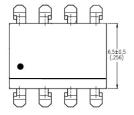


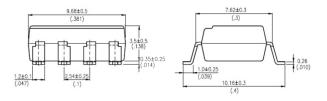
G Form





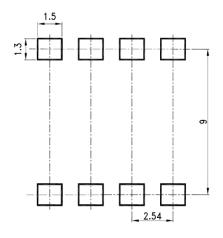
SMD



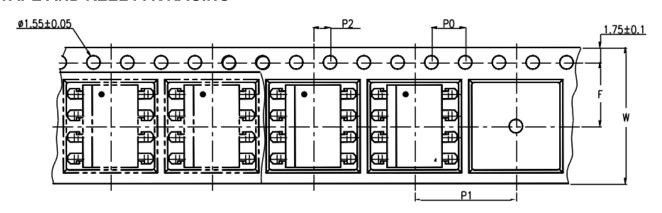


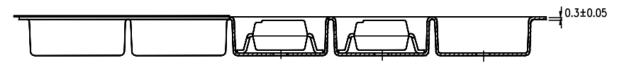


RECOMMENDED SOLDER PAD LAYOUT (mm)



TAPE AND REEL PACKAGING



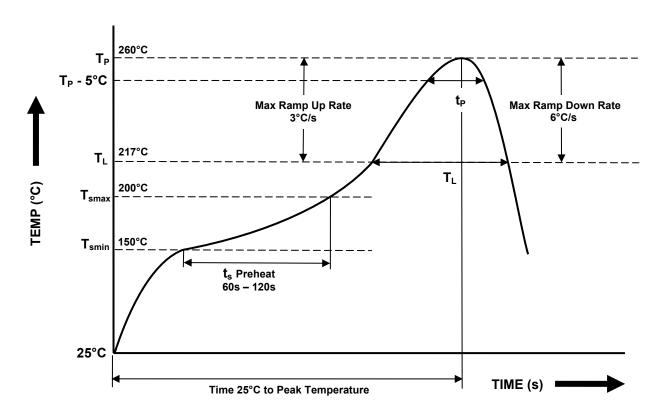


Description	Symbol	Dimension mm (inch)
Tape Width	W	16 ± 0.3 (0.63)
Pitch of Sprocket Holes	P ₀	4 ± 0.1 (0.15)
Distance of Compartment to Sprocket Holes	F	7.5 ± 0.1 (0.295)
Distance of Compartment to Sprocket Holes	P ₂	2 ± 0.1 (0.079)
Distance of Compartment to Compartment	P ₁	12 ± 0.1 (0.47)



IR REFLOW SOLDERING TEMPERATURE PROFILE

Note: One Time Reflow Soldering is Recommended.
Do Not Immerse Device Body in Solder Paste.



Profile Details	Conditions
$ \begin{array}{l} \textbf{Preheat} \\ \textbf{- Min Temperature } (T_{SMIN}) \\ \textbf{- Max Temperature } (T_{SMAX}) \\ \textbf{- Time } T_{SMIN} \text{ to } T_{SMAX} \left(t_s\right) \end{array} $	150°C 200°C 60s - 120s
$\begin{tabular}{ll} \textbf{Soldering Zone} \\ - & \begin{tabular}{ll} - & \begin{tabular}{ll} \textbf{Peak Temperature} & \begin{tabular}{ll} - & \begin{tabular}{ll} \textbf{Imperature} & \begin{tabular}{ll} \textbf{Peak Temperature} & \begin{tabular}{ll} \textbf{Imperature} & \begin{tabular}{ll} Imperature$	260°C 10s max 217°C 30s max 60s - 100s 3°C/s max 6°C/s max
Average Ramp Up Rate (T _{smax} to T _P)	3°C/s max
Time 25°C to Peak Temperature	8 minutes max



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