2

PTP40N20

 I_{D}

200V N-Channel MOSFET

General Features

- Proprietary New Planar Technology
- \triangleright R_{DS(ON),typ.}=50m Ω @V_{GS}=10V
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

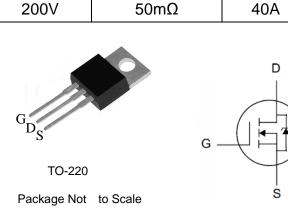
Applications

- DC-DC Converters
- DC-AC Inverters for UPS
- SMPS and Motor controls

Ordering Information

Part Number	Package	Brand
PTP40N20	TO-220	ľ

Absolute Maximum Ratings



🗭 Lead Free Package and Finish

R_{DS(ON),typ.}

 $T_C{=}25\,^\circ\!\mathrm{C}$ unless otherwise specified

BV_{DSS}

Symbol	Parameter	PTP40N20	Unit	
V _{DSS}	Drain-to-Source Voltage ^[1]	200	V	
V _{GSS}	Gate-to-Source Voltage	±20	v	
I _D	Continuous Drain Current	40		
I _{D @ Tc =100} ℃	Continuous Drain Current @ Tc=100℃	Figure 3	A	
I _{DM}	Pulsed Drain Current at V _{GS} =10V ^[2]	Figure 6		
E _{AS}	Single Pulse Avalanche Energy	1200	mJ	
dv/dt	Peak Diode Recovery dv/dt ^[3]	5.0	V/ns	
D	Power Dissipation	125	W	
P _D	Derating Factor above 25°C	1.0	W/°C	
T _L T _{PAK}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300 260	°C	
T _J & T _{STG}	Operating and Storage Temperature Range	-55 to 150		

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	PTP40N20	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	1.0	20.111
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62	°CM

Electrical Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	200			V	V_{GS} =0V, I _D =250uA
I _{DSS} Drain-to-Source Leakage Current	Drain to Course Lookana Current			1		V _{DS} =200V, V _{GS} =0V
			100	uA	V _{DS} =160V, V _{GS} =0V, T _J =125℃	
I _{GSS} G	Gate-to-Source Leakage Current			+100	nA	V_{GS} =+20V, V_{DS} =0V
				-100		V_{GS} =-20V, V_{DS} =0V

OFF Characteristics T_J =25°C unless otherwise specified

ON Characteristics

 $T_J = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
R _{DS(ON)}	Static Drain-to-Source On-Resistance ^[4]		50	65	mΩ	V _{GS} =10V, I _D =20A
V _{GS(TH)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS}=V_{GS}$, $I_{D}=250$ uA
gfs	Forward Transconductance ^[4]		65		S	VDS=15V,ID=20A

Dynamic Characteristics

Essentially independent of operating temperature Symbol Parameter Min. Unit **Test Conditions** Тур. Max. Ciss Input Capacitance --2800 3700 V_{GS}=0V, V_{DS}=25V, f=1.0MH_Z pF Crss **Reverse Transfer Capacitance** --110 150 $\boldsymbol{C}_{\text{oss}}$ 305 400 **Output Capacitance** -- \mathbf{Q}_{g} **Total Gate Charge** 97 120 -- $\label{eq:V_DD} \begin{array}{l} V_{DD} = 100V, \\ I_{D} = 20A, \ V_{GS} = 0 \ to \ 10V \end{array}$ Q_{gs} Gate-to-Source Charge 14 ----nC Gate-to-Drain (Miller) Charge 39 --- Q_{gd} --

Resistive Switching Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
td(ON)	Turn-on Delay Time		20		nS	V _{DD} =100V, I _D =20A, V _{GS} = 10V RG=3.9 Ω
trise	Rise Time		30			
td(OFF)	Turn-Off Delay Time		65			
tfall	Fall Time		25			

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Source-Drain Body Diode Characteristics

$T_J\!\!=\!\!25^\circ\!\!\mathbb{C}$ unless otherwise specified

Symbol	Parameter	Min	Тур.	Max.	Unit	Test Conditions	
I _{SD}	Continuous Source Current ^[4]			40	A	Integral PN-diode in MOSFET	
I _{SM}	Pulsed Source Current ^[4]			160			
V _{SD}	Diode Forward Voltage			1.5	V	I _S =40A, V _{GS} =0V	
trr	Reverse recovery time		280		ns	V _{GS} =0V ,IF=20A,	
Qrr	Reverse recovery charge		420		nC	di⊧/dt=100A/µs	

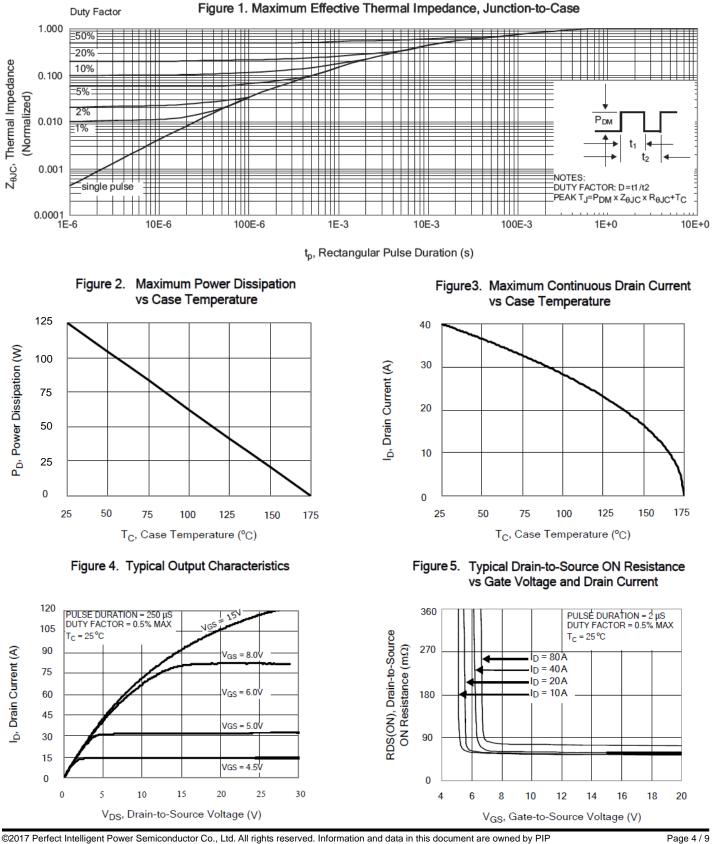
Note:

[1] T_J=+25℃ to +150℃

[2] Repetitive rating; pulse width limited by maximum junction temperature. [3] ISD= 20A di/dt < 100 A/ μ s, VDD < BVDss, TJ=+150°C.

- [4] Pulse width≤380µs; duty cycle≤2%.

Typical Characteristics



Typical Characteristics(Cont.)

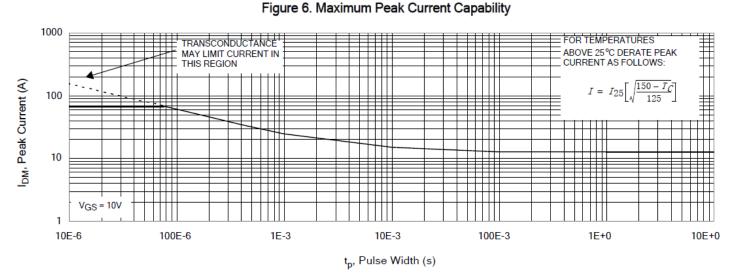


Figure 7. Typical Transfer Characteristics

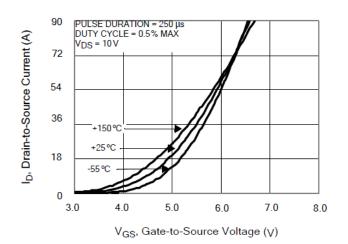


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

140

105

70

35

0.0

0

10

T_C=25°C

R_{DS(ON)}, Drain-to-Source

ON Resistance (mΩ)

Unclamped Inductive Figure 8. Switching Capability

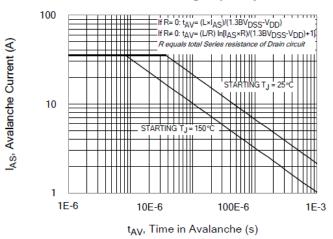
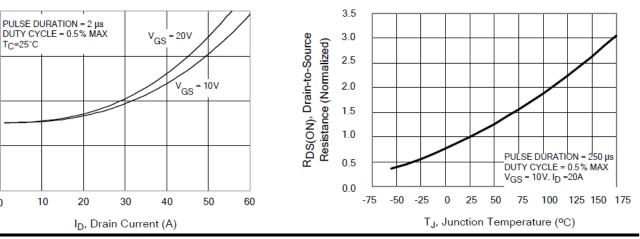
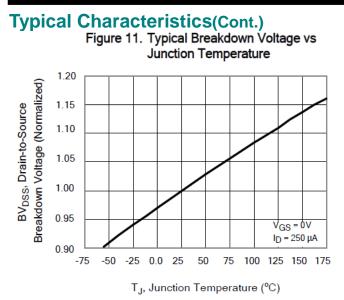


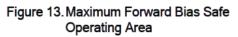
Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



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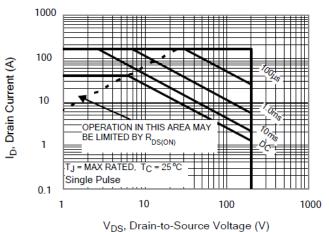
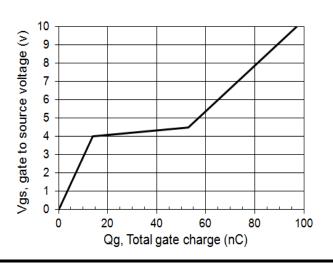


Figure 15 . Typical Gate Charge



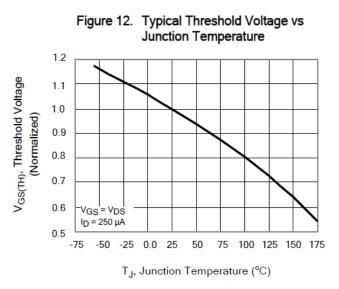


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

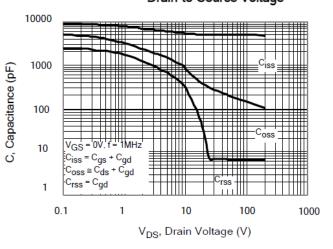
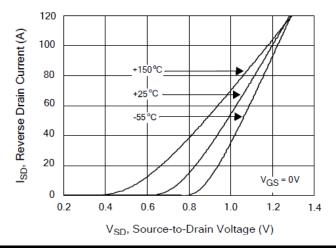


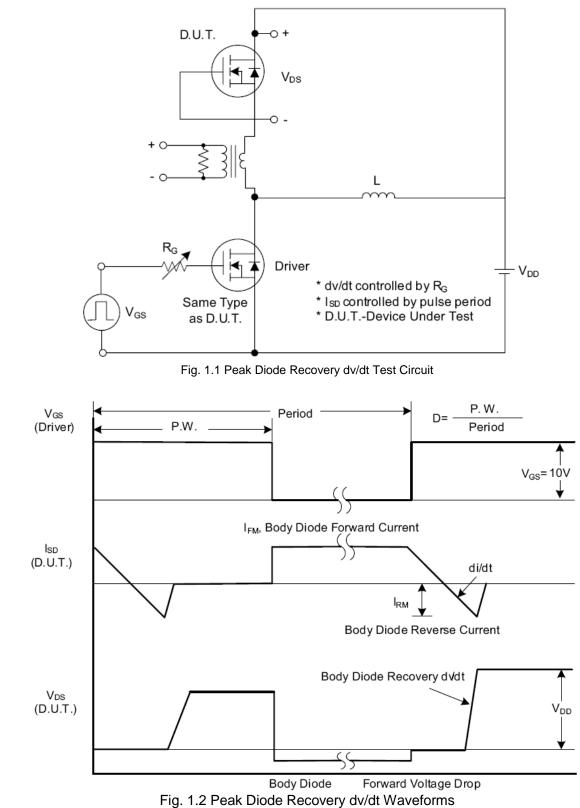
Figure 16. Typical Body Diode Transfer Characteristics



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Test Circuits and Waveforms



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Test Circuits and Waveforms (Cont.)

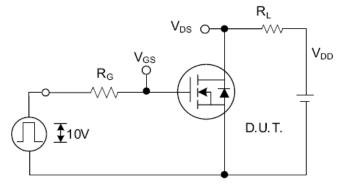


Fig. 2.1 Switching Test Circuit

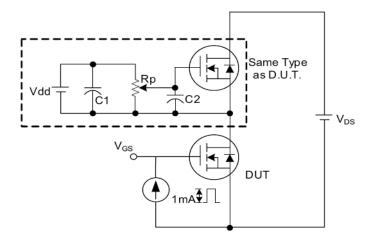


Fig. 3.1 Gate Charge Test Circuit

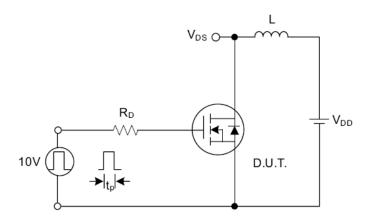


Fig. 4.1 Unclamped Inductive Switching Test Circuit

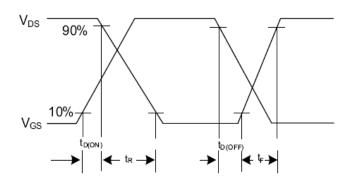


Fig. 2.2 Switching Waveforms

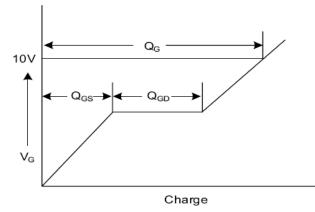
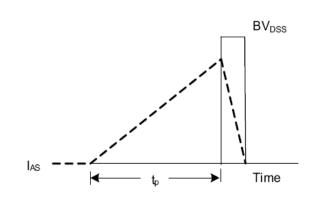


Fig. 3.2 Gate Charge Waveform





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