



**ALPHA & OMEGA**  
SEMICONDUCTOR

**AO4264E**

**60V N-Channel AlphaSGT™**

### General Description

- Trench Power AlphaSGT™ technology
- Low  $R_{DS(ON)}$
- Low Gate Charge
- ESD protected

### Applications

- High efficiency power supply
- Secondary synchronous rectifier

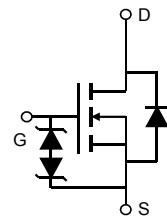
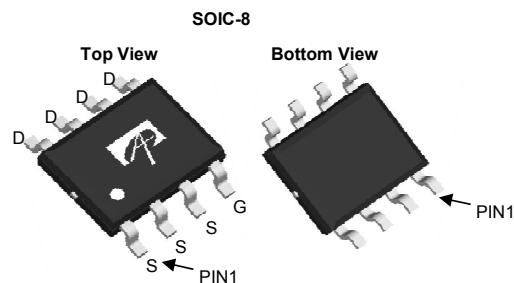
### Product Summary

$V_{DS}$	60V
$I_D$ (at $V_{GS}=10V$ )	13.5A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 9.8mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	< 13.5mΩ

### Typical ESD protection

HBM Class 2

100% UIS Tested  
100%  $R_g$  Tested



Orderable Part Number	Package Type	Form	Minimum Order Quantity
AO4264E	SO-8	Tape & Reel	3000

### Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>A</sup>	$I_D$	13.5	A
$T_A=70^\circ C$		10.5	
Pulsed Drain Current <sup>C</sup>	$I_{DM}$	54	
Avalanche Current <sup>C</sup>	$I_{AS}$	17	A
Avalanche energy $L=0.3mH$ <sup>C</sup>	$E_{AS}$	43	mJ
$V_{DS}$ Spike <sup>G</sup>	$V_{SPIKE}$	72	V
$T_A=25^\circ C$		3.1	W
Power Dissipation <sup>B</sup>	$P_D$	2.0	
$T_A=70^\circ C$		-55 to 150	°C
Junction and Storage Temperature Range	$T_J, T_{STG}$		

### Thermal Characteristics

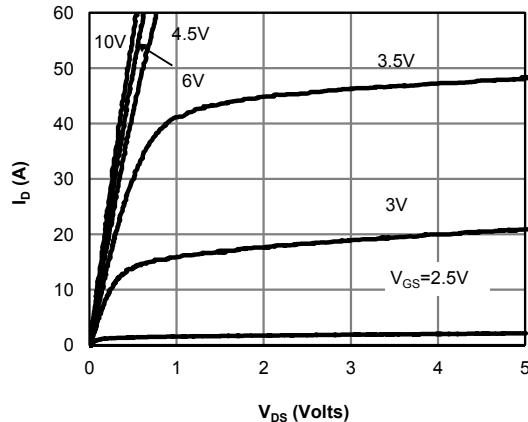
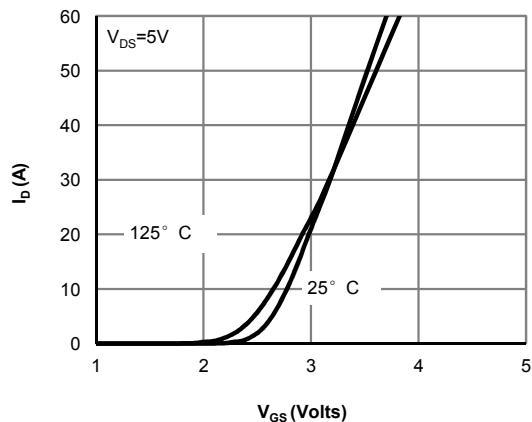
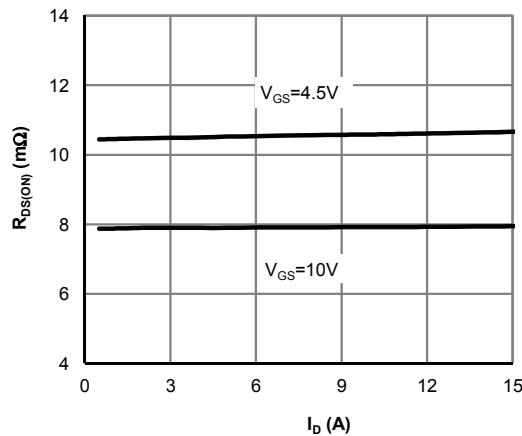
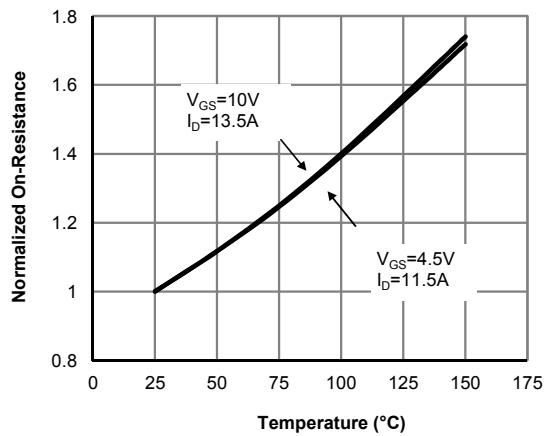
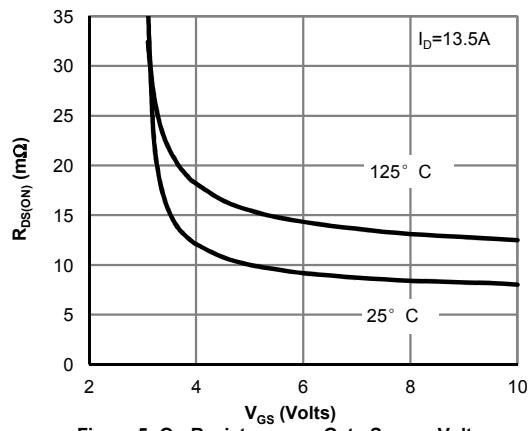
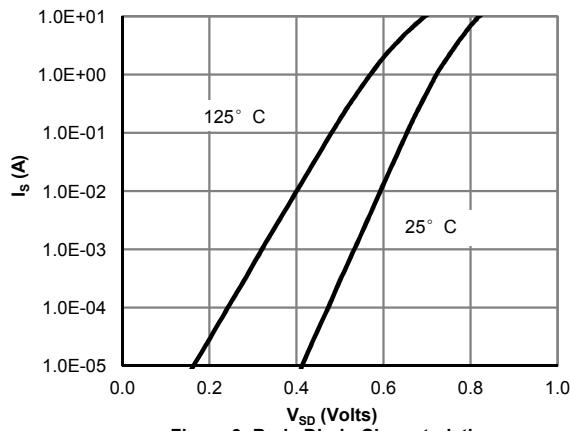
Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>A</sup> $t \leq 10s$	$R_{\theta JA}$	31	40	°C/W
Maximum Junction-to-Ambient <sup>A,D</sup> Steady-State		59	75	°C/W
Maximum Junction-to-Lead	$R_{\theta JL}$	16	24	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	60			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$		1	5	$\mu\text{A}$
$I_{GSS}$	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm20\text{V}$			$\pm10$	$\mu\text{A}$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.4	1.8	2.4	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=13.5\text{A}$ $T_J=125^\circ\text{C}$		8	9.8	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=11.5\text{A}$		12.5	15.0	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=5\text{V}, I_D=13.5\text{A}$		48		S
$V_{SD}$	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.72	1	V
$I_S$	Maximum Body-Diode Continuous Current				4	A
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$		1100		pF
$C_{oss}$	Output Capacitance			300		pF
$C_{rss}$	Reverse Transfer Capacitance			28		pF
$R_g$	Gate resistance	$f=1\text{MHz}$	0.6	1.2	2.0	$\Omega$
<b>SWITCHING PARAMETERS</b>						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=13.5\text{A}$		14.5	25	nC
$Q_g(4.5\text{V})$	Total Gate Charge			7	13	nC
$Q_{gs}$	Gate Source Charge			2.5		nC
$Q_{gd}$	Gate Drain Charge			3.5		nC
$t_{D(\text{on})}$	Turn-On DelayTime	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, R_L=2.2\Omega, R_{\text{GEN}}=3\Omega$		6.5		ns
$t_r$	Turn-On Rise Time			3.5		ns
$t_{D(\text{off})}$	Turn-Off DelayTime			22		ns
$t_f$	Turn-Off Fall Time			3		ns
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=13.5\text{A}, di/dt=500\text{A}/\mu\text{s}$		18.5		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=13.5\text{A}, di/dt=500\text{A}/\mu\text{s}$		59		nC

- A. The value of  $R_{\text{JJA}}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{ C}$ . The value in any given application depends on the user's specific board design.
- B. The power dissipation  $P_D$  is based on  $T_{J(\text{MAX})}=150^\circ\text{ C}$ , using  $\leq 10\text{s}$  junction-to-ambient thermal resistance.
- C. Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})}=150^\circ\text{ C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J=25^\circ\text{ C}$ .
- D. The  $R_{\text{JJA}}$  is the sum of the thermal impedance from junction to lead  $R_{\text{JLL}}$  and lead to ambient.
- E. The static characteristics in Figures 1 to 6 are obtained using <300µs pulses, duty cycle 0.5% max.
- F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, assuming a maximum junction temperature of  $T_{J(\text{MAX})}=150^\circ\text{ C}$ . The SOA curve provides a single pulse rating.
- G. The spike duty cycle 5% max, limited by junction temperature  $T_J(\text{MAX})=125^\circ\text{ C}$ .

APPLICATIONS OR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

**Figure 1: On-Region Characteristics (Note E)**

**Figure 2: Transfer Characteristics (Note E)**

**Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)**

**Figure 4: On-Resistance vs. Junction Temperature (Note E)**

**Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)**

**Figure 6: Body-Diode Characteristics (Note E)**

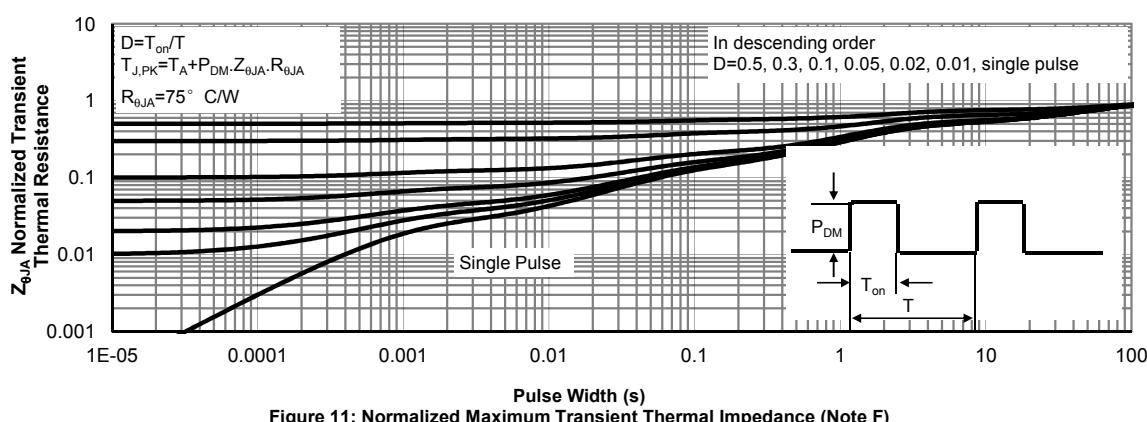
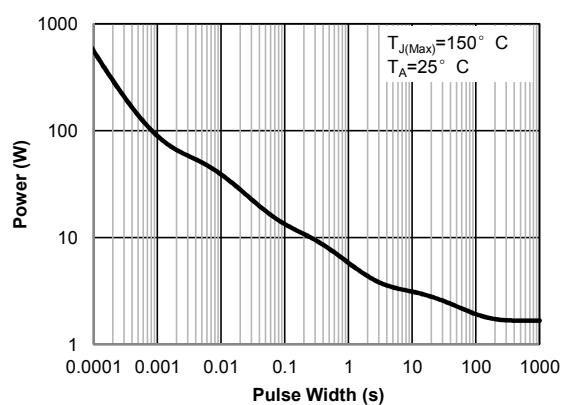
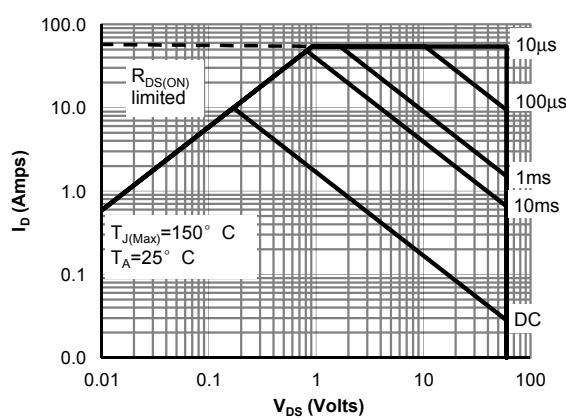
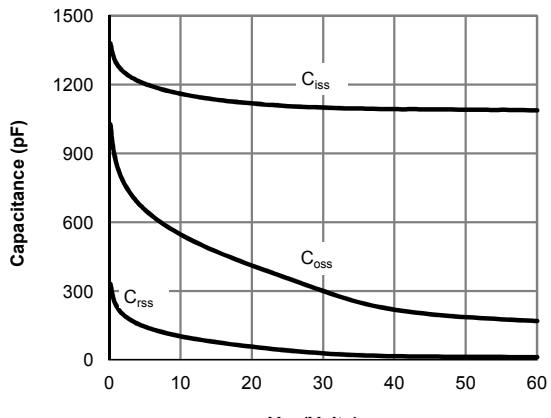
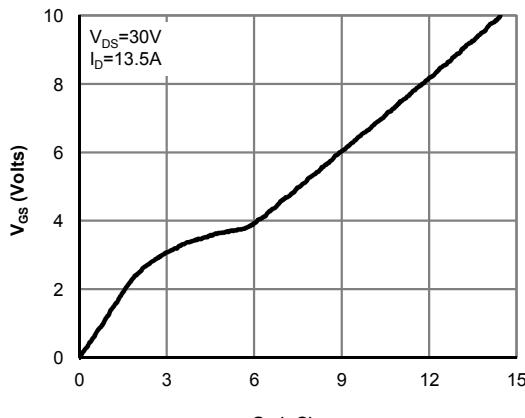
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**


Figure A: Gate Charge Test Circuit &amp; Waveforms

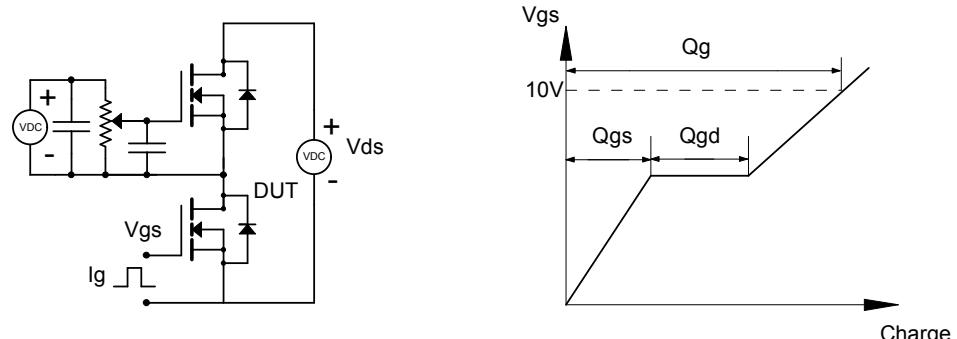


Figure B: Resistive Switching Test Circuit &amp; Waveforms

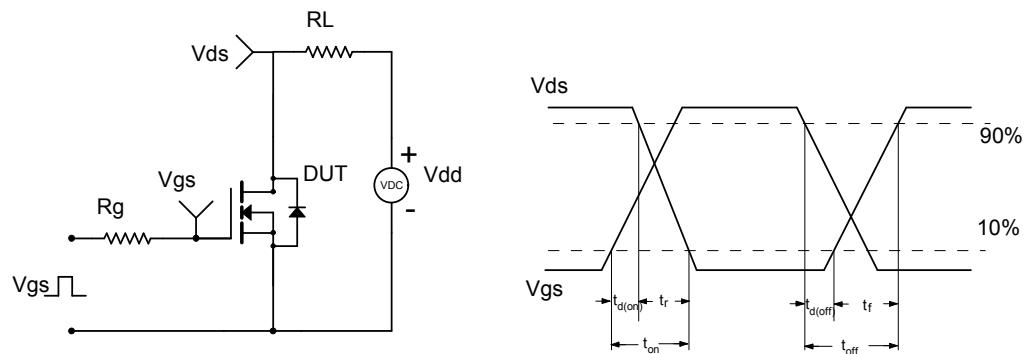


Figure C: Unclamped Inductive Switching (UIS) Test Circuit &amp; Waveforms

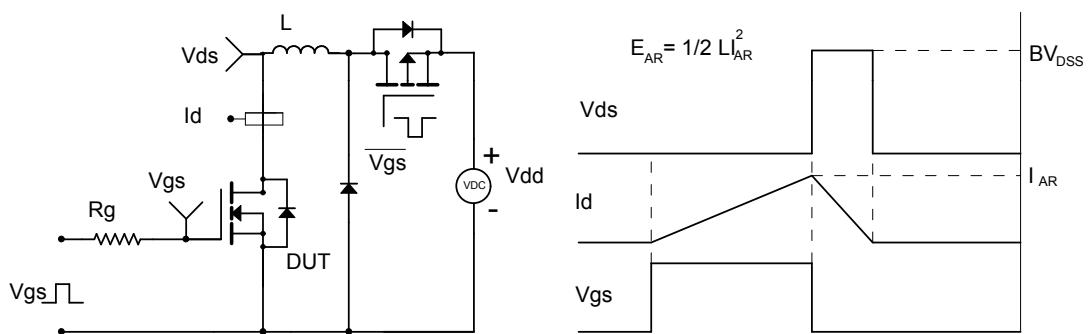
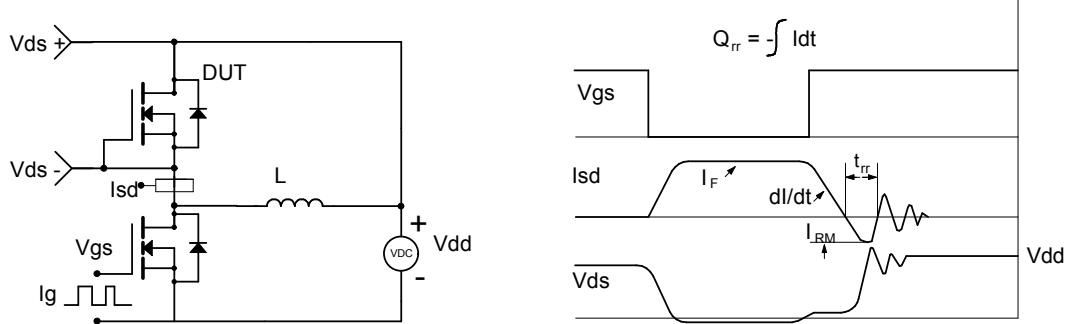


Figure D: Diode Recovery Test Circuit &amp; Waveforms



# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

***Click to view similar products for MOSFET category:***

***Click to view products by Alpha & Omega manufacturer:***

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)  
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60\\_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)  
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#)  
[424134F](#) [026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#)  
[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)  
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)