

产品规格书

批准	审核	校核	编制
朴雨根	纪春华	朴致均	郑羿
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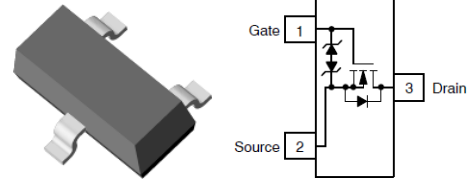
规格书更改履历:

序号	更改内容	履历号	更改时间	责任人
1	新规制定	000	2017.10.18	郑羿
2	增加决裁	001	2018.04.18	郑羿
3				

High Speed Switching Application

Features

- ESD rating: 1000V (HBM)
- Low On-Resistance: $R_{DS(on)} < 3\Omega @ V_{GS} = 10V$
- High power and current handling capability
- Very fast switching
- RoHS compliant device



Applications

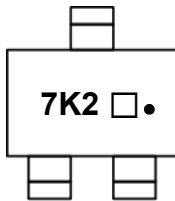
- High speed line driver

Ordering Information

SOT-23

Part Number	Marking Code	Package	Packaging
K2N7002K	7K2 □●	SOT-23	Tape & Reel

Marking Information


7K2 = Specific Device Code
□ = Year & Week Code Marking
● = Dalian

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Drain-Source voltage	V _{DS}	60	V
Gate-Source voltage	V _{GS}	±20	V
Maximum drain current ^(Note 1)	I _D	300	mA
Pulsed drain current ^(Note 1)	I _{DP}	800	mA
Power dissipation ^(Note 2)	P _D	350	mW
Operating junction temperature	T _j	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C
Thermal resistance junction to ambient ^(Note 2)	R _{th(j-a)}	350	°C/W

Note 1) Limited only maximum junction temperature

Note 2) Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-Source breakdown voltage	BV _{DSS}	I _D =250μA, V _{GS} =0	60	-	-	V
Gate-Source breakdown voltage	BV _{GSS}	I _G =250μA, V _{DS} =0	±20	-	-	V
Gate-Threshold voltage	V _{GS(th)}	I _D =250μA, V _{DS} =V _{GS}	1	-	2.5	V
Zero Gate voltage drain current	I _{DSS}	V _{DS} =60V, V _{GS} =0	-	-	1	μA
Gate-body leakage	I _{GSS}	V _{GS} =±5V, V _{DS} =0V	-	-	±100	nA
		V _{GS} =±10V, V _{DS} =0V	-	-	±150	nA
		V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
Drain-Source on-resistance (Note 3)	R _{DS(ON)}	V _{GS} =10V, I _D =0.5A	-	-	3	Ω
		V _{GS} =5V, I _D =0.05A	-	-	3.5	
Forward trans-conductance (Note 3)	g _{fs}	V _{DS} =10V, I _D =0.2A	0.08	-	-	S
Input capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0, f=1MHz	-	30	50	pF
Output capacitance	C _{oss}		-	7	-	
Reverse Transfer capacitance	C _{rss}		-	4	-	
Turn-on delay time (Note 3, 4)	t _{d(on)}	V _{DD} =30V, I _D =0.2A, V _{GS} =10V, R _G =10Ω	-	2	-	ns
Rise time (Note 3, 4)	t _r		-	15	-	
Turn-off delay time (Note 3, 4)	t _{d(off)}		--	8	-	
Fall time (Note 3, 4)	t _f		-	11	-	
Total gate charge (Note 3, 4)	Q _g	V _{DS} =10V, I _D =0.25A, V _{GS} =4.5V	-	0.6	0.8	nC
Gate-Source charge (Note 3, 4)	Q _{gs}		-	0.2	-	
Gate-Drain charge (Note 3, 4)	Q _{gd}		-	0.2	-	
Diode forward voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =0.2A	-	-	1.3	V

Note 3) Pulse test: Pulse width ≤ 300μs, Duty cycle ≤ 2%

Note 4) Essentially independent of operating temperature typical characteristics.

Electrical Characteristics Curves

Fig. 1 $I_D - V_{DS}$

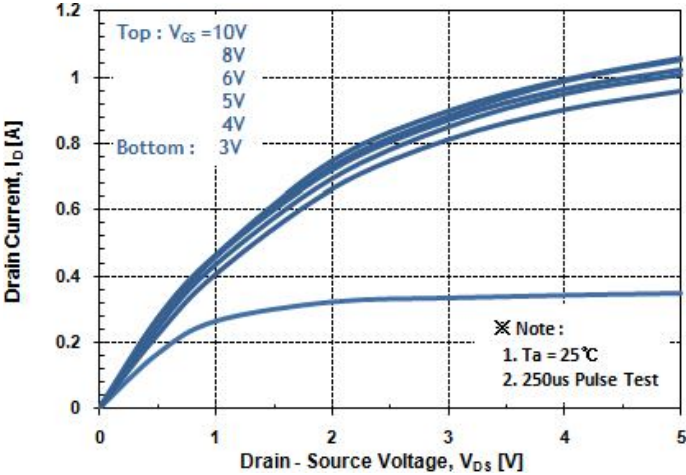


Fig. 2 $I_D - V_{GS}$

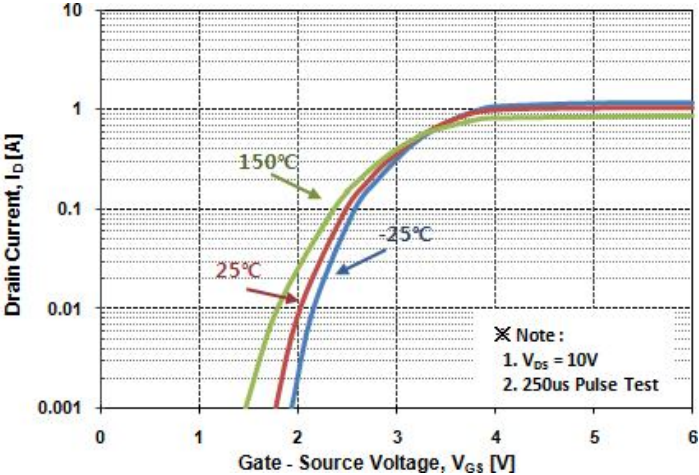


Fig. 3 $R_{DS(ON)} - I_D$

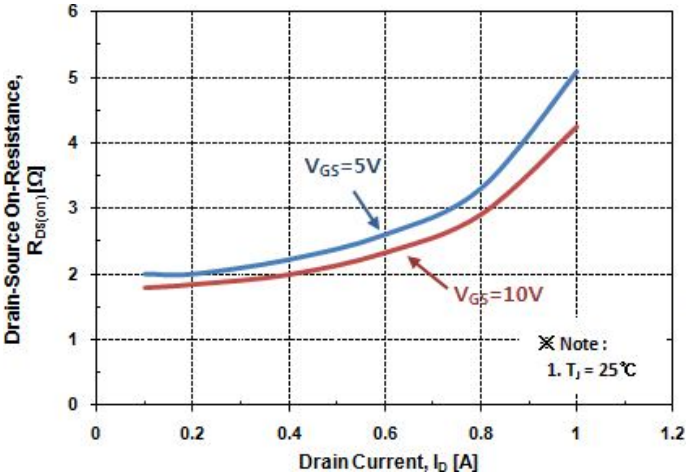


Fig. 4 $I_S - V_{SD}$

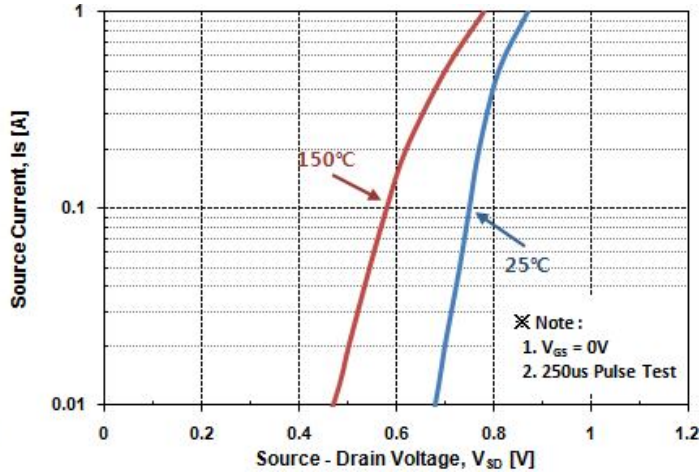


Fig. 5 Capacitance - V_{DS}

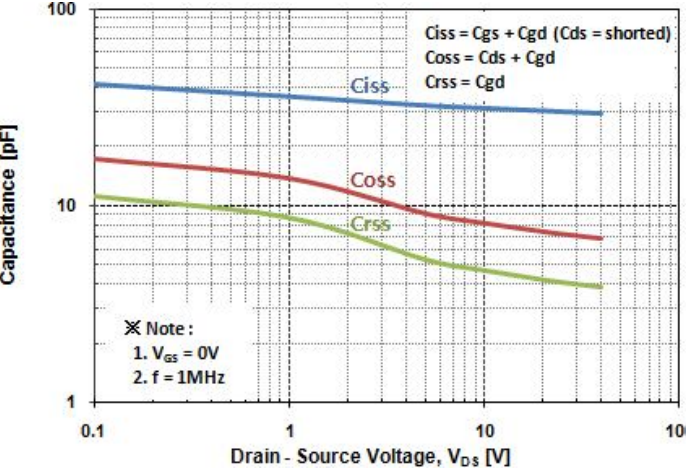


Fig. 6 $V_{GS} - Q_G$

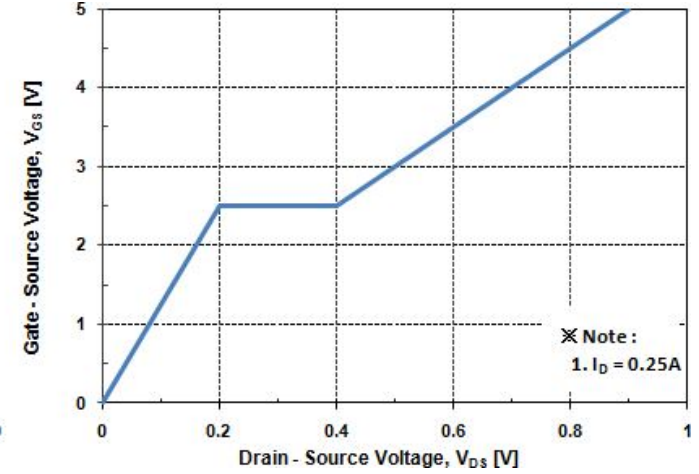


Fig. 7 $V_{DSS} - T_J$

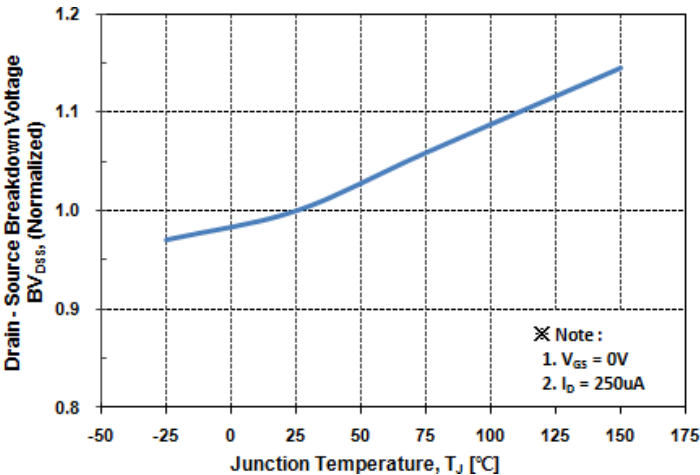


Fig. 8 $R_{DS(on)} - T_J$

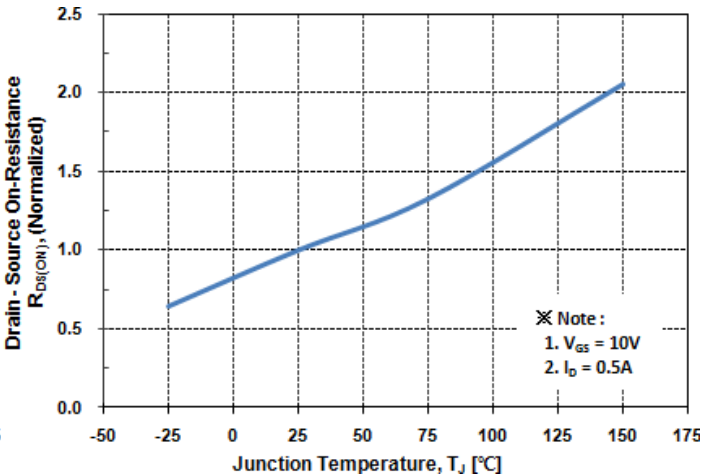


Fig. 9 $I_D - T_C$

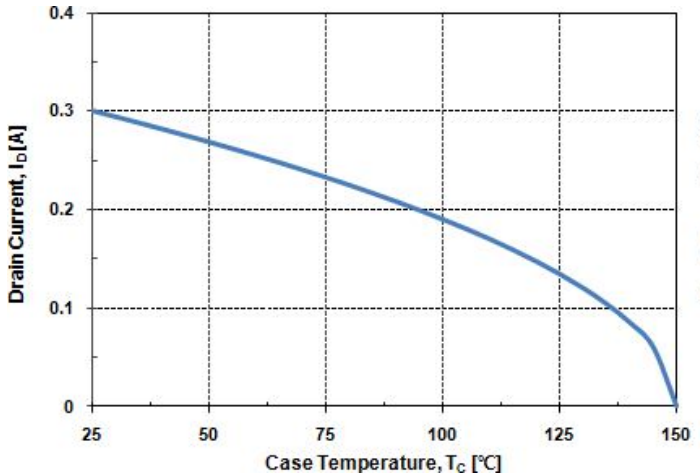


Fig. 10 Safe Operating Area

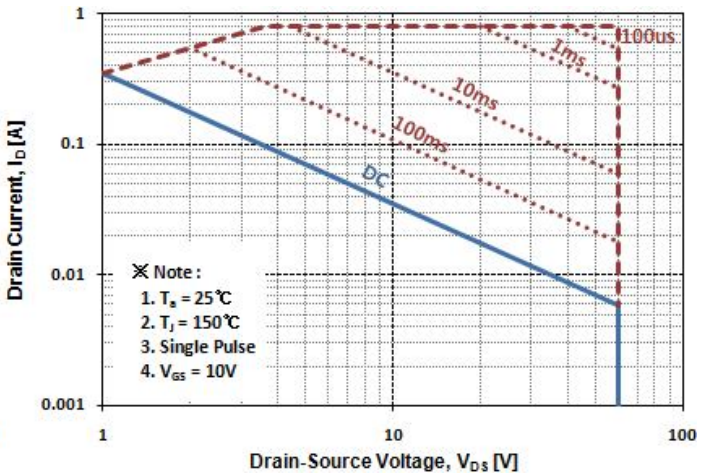
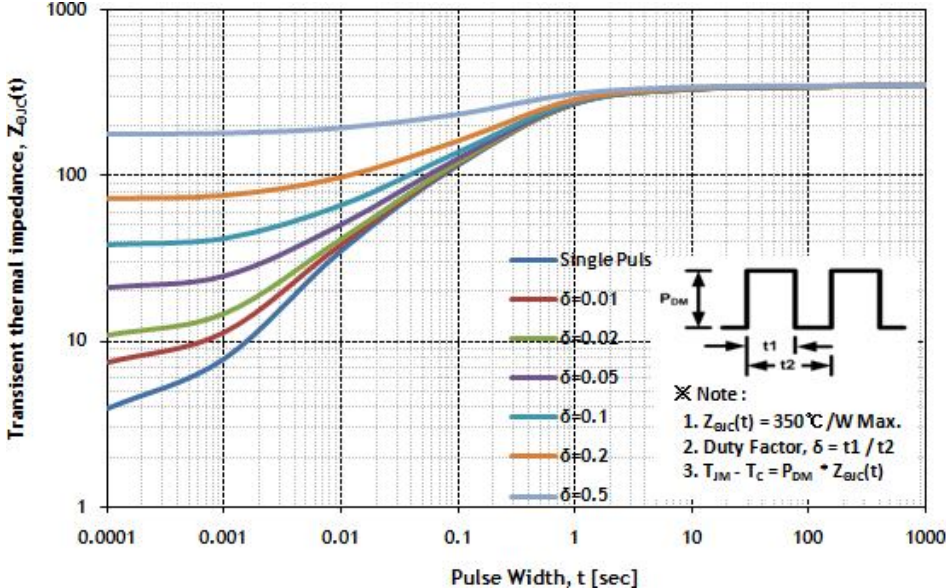
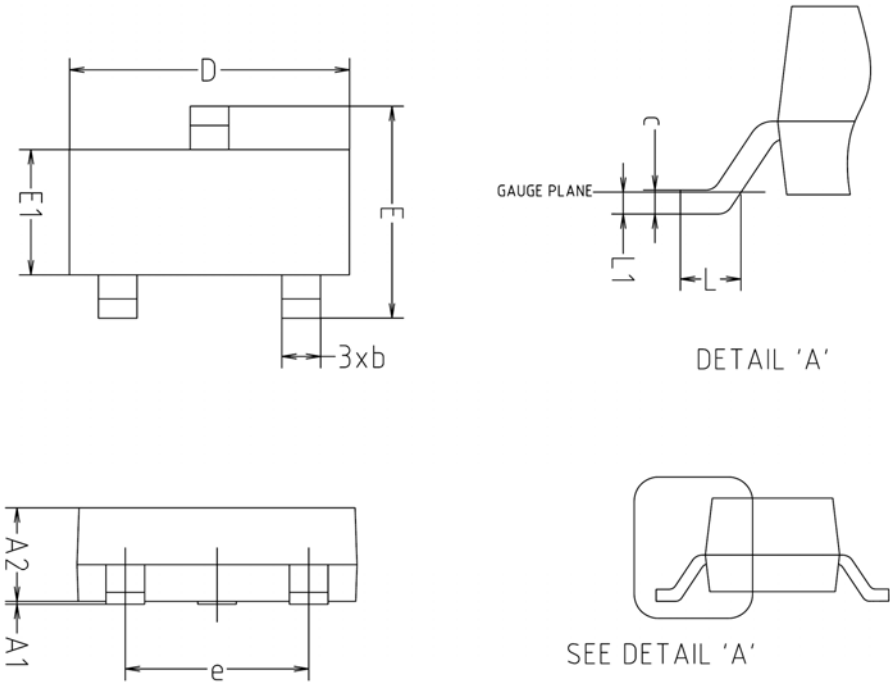


Fig. 11 Transient Thermal Impedance

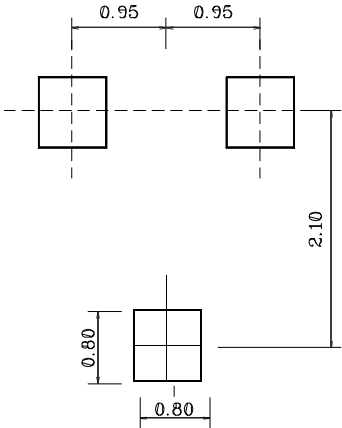


Package Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
c	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
e	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

※ Recommend PCB solder land (Unit: mm)



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