JIANGSU CHANGJIANG ELECTRONICS TECHNOLOGY CO., LTD

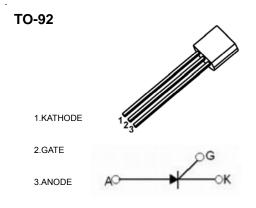


TO-92 Plastic-Encapsulate Thyristors

MCR100-6,-8 Silicon Controlled Rectifier

MAIN FEATURES

| | Symbol | value | unit |
|------------------------------------|----------------------|-----------|-------|
| I _{T(RMS)} | | 0.8 | Α |
| V _{DRM} /V _{RRM} | MCR100-6 | 400 | V |
| | MCR100-8 | 600 | \ \ \ |
| Tj | Junction Temperature | -40 ~ 125 | ℃ |
| T _{stg} | Storage Temperature | -55 ~ 150 | ° |



DESCRIPTION

Logic level sensitive gate triac intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

FEATURES

- Blocking voltage to 400 V (MCR100-6)
- RMS on-state current to 0.8 A
- General purpose switching

APPLICATIONS

- General purpose switching
- Phase control applications
- Solid state relays

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

| Parameter | | nbol | Test conditions | Min | Max | Unit |
|--|-----------|----------|---|-----|-----|------|
| On state voltage * | | ГМ | I _{TM} =1A | | 1.7 | V |
| Gate trigger voltage | | ЭT | V _{AK} =7V | | 0.8 | V |
| Peak Repetitive forward and reverse | | | I _{DRM} /I _{RRM} = 10 μA | | | |
| blocking voltage MCR100-6 | VDRM/VRRM | | | 400 | | V |
| MCR100-8 | | | | 600 | | |
| Peak forward or reverse blocking Current | | RM RM | V _{AK} = Rated V _{DRM} or V _{RRM} | | 10 | μΑ |
| Holding current | | Н | I _{HL} =20mA ,V _{AK} =7V | | 5 | mA |
| Gate trigger current | | A2 | - V _{AK} =7V | 5 | 15 | μA |
| | | A1 | | 15 | 30 | μA |
| | | Α | | 30 | 80 | μA |
| | | В | | 80 | 200 | μΑ |

^{*} Forward current applied for 1 ms maximum duration, duty cycle≤1%.

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