

# **General Description**

The WSD30160DN56 is the highest performance trench N-Ch MOSFET with extreme high celldensity ,which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSD30160DN56 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

# **Product Summery**

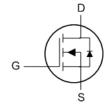
BVDSS	RDSON	ID
30V	1.9mΩ	120A

# **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

# **DFN5X6-8 Pin Configuration**





# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,7</sup>	120	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,7</sup> 68		А
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup> 300		А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	128	mJ
I <sub>AS</sub>	Avalanche Current 50		Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup> 62.5		W
T <sub>STG</sub>	Storage Temperature Range -55 to 150		$^{\circ}$
TJ	Operating Junction Temperature Range -55 to 150		${\mathbb C}$

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>		30	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		2	°C/W



**N-Ch MOSFET** 

# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.02		V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		1.9	2.5	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		2.9	3.5	
$V_{GS(th)}$	Gate Threshold Voltage		1.2	1.7	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			-6.1		mV/℃
	Drain-Source Leakage Current	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	uA
I <sub>DSS</sub>		$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =55 $^{\circ}$ C			5	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =10A		32		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		0.8	1.5	Ω
$Q_{g}$	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A		38		
$Q_gs$	Gate-Source Charge			10		nC
Q <sub>gd</sub>	Gate-Drain Charge			13		
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD}$ =15V , $V_{GEN}$ =10V , $R_{G}$ =6 $\Omega$ , $I_{D}$ =1A, $RL$ =15 $\Omega$ .		25		
Tr	Rise Time			23		
T <sub>d(off)</sub>	Turn-Off Delay Time			95		ns
T <sub>f</sub>	Fall Time			40		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		4900		
C <sub>oss</sub>	Output Capacitance			1180		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			530		

# **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	$V_G$ = $V_D$ = $0V$ , Force Current			20	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				200	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>				1.2	V

#### Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec.
- 2.The data tested by pulsed , pulse width  $\,\leq\,300\text{us}$  , duty cycle  $\,\leq\,2\%$
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =30A
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.
- 7.Package limitation current is 100A.



# **Typical Characteristics**

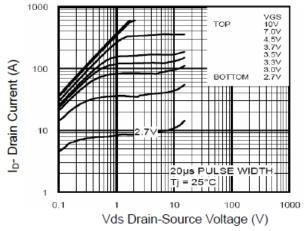


Figure 1 Output Characteristics

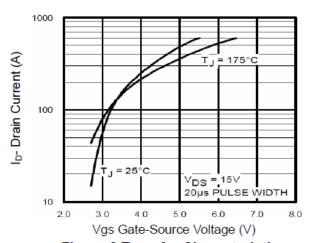


Figure 2 Transfer Characteristics

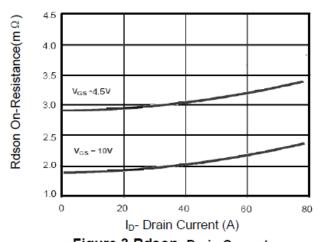


Figure 3 Rdson- Drain Current

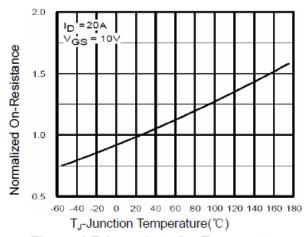


Figure 4 Rdson-JunctionTemperature

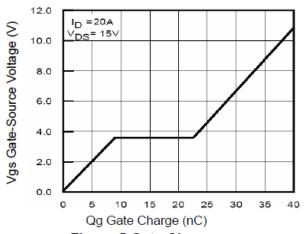


Figure 5 Gate Charge

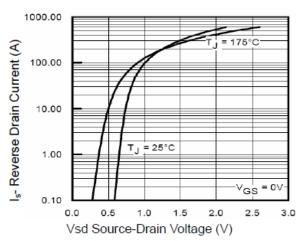


Figure 6 Source- Drain Diode Forward



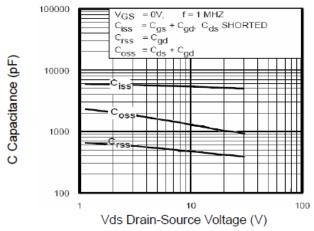
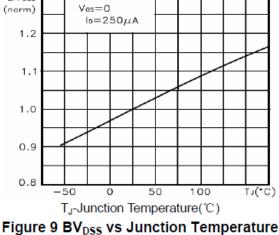


Figure 7 Capacitance vs Vds



BVoss

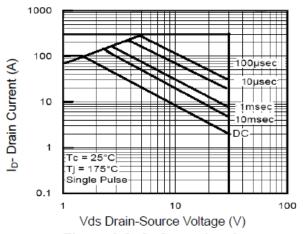


Figure 8 Safe Operation Area

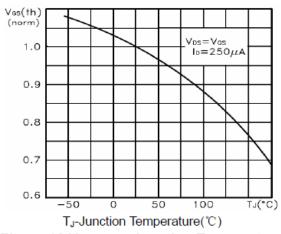


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

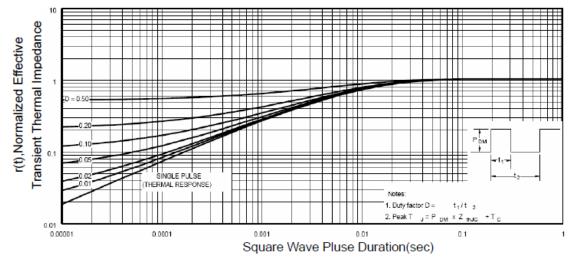


Figure 11 Normalized Maximum Transient Thermal Impedance



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