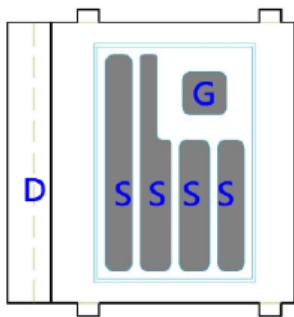


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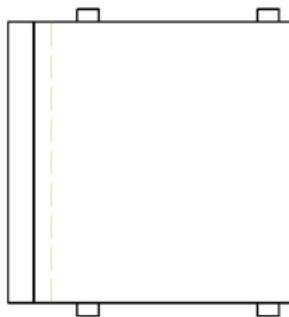
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	1.4mΩ @ $V_{GS} = 10V$	149A



Bottom View



Top View



Side View

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>4</sup>	$I_D$	149	A
		94	
		37	
		29	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	200	
Avalanche Current	$I_{AS}$	51	
Avalanche Energy	$E_{AS}$	130	
Power Dissipation <sup>3</sup>	$P_D$	50	
		20	
		3.1	
		2	
Operating Junction & Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	°C

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## N-Channel Enhancement Mode MOSFET

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$t \leq 10s$	$R_{\theta JA}$		40	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient <sup>2</sup>	Steady-State	$R_{\theta JA}$		60	
Junction-to-Case	Top	$R_{\theta JC}$		2.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^{\circ}\text{C}$ .

<sup>3</sup>The Power dissipation is based on  $R_{\theta JA} t \leq 10s$  value.

<sup>4</sup>The maximum current rating is package limited.

### ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.6	2.3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$			1	$\mu\text{A}$
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^{\circ}\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = 4.5V, I_D = 16A$		1.9	2.5	$\text{m}\Omega$
		$V_{GS} = 10V, I_D = 20A$		1.1	1.4	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 20A$		87		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1\text{MHz}$		3322		pF
Output Capacitance	$C_{oss}$			1513		
Reverse Transfer Capacitance	$C_{rss}$			59		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1\text{MHz}$		0.8		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g(V_{GS}=10V)$	$V_{DS} = 15V, V_{GS} = 10V, I_D = 20A$		62.8		nC
	$Q_g(V_{GS}=4.5V)$			31.1		
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			8		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			13.7		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, I_D \approx 20A, V_{GS} = 10V, R_{\text{GEN}} = 6\Omega$		26		nS
Rise Time <sup>2</sup>	$t_r$			130		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			95		
Fall Time <sup>2</sup>	$t_f$			148		

## PR804BA33

### N-Channel Enhancement Mode MOSFET

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ )						
Continuous Current <sup>3</sup>	$I_S$				41	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 20\text{A}, V_{GS} = 0\text{V}$			1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 20\text{A}, dI_F/dt = 100\text{A} / \mu\text{s}$		60		nS
Reverse Recovery Charge	$Q_{rr}$			66		nC

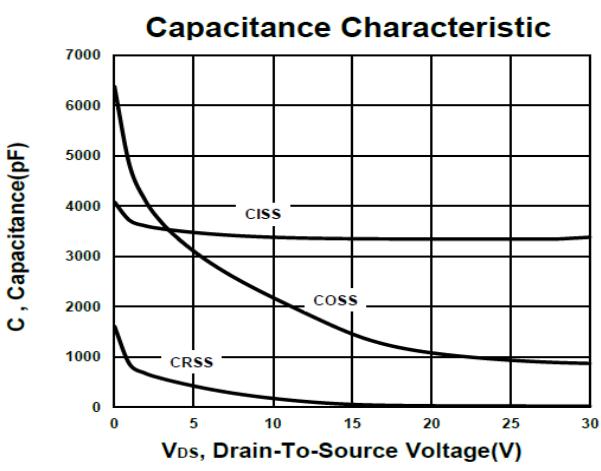
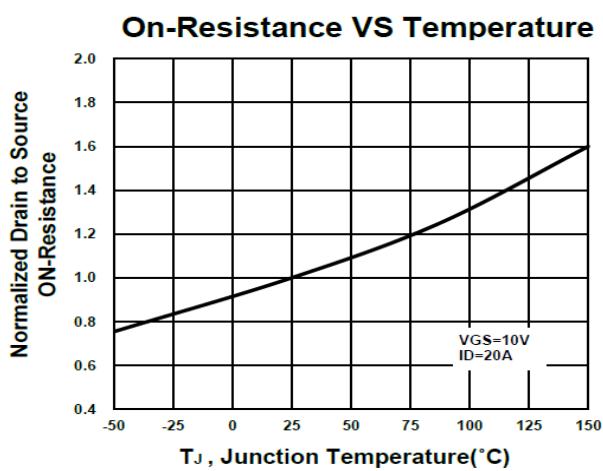
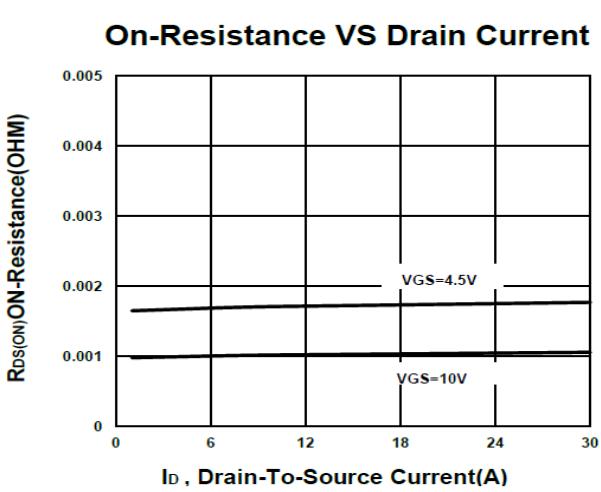
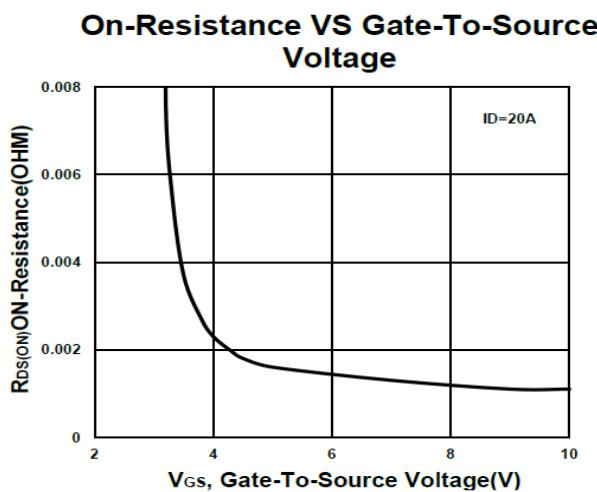
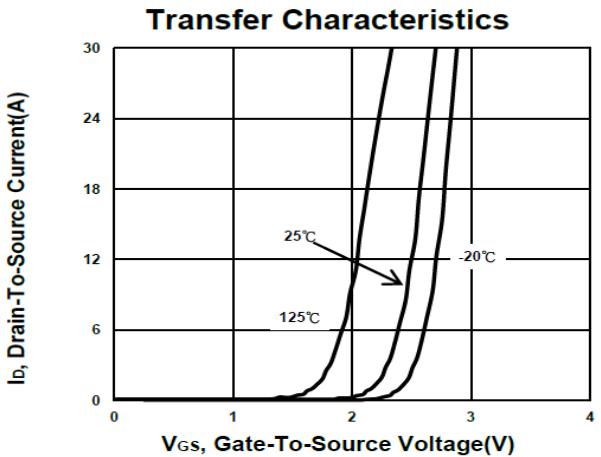
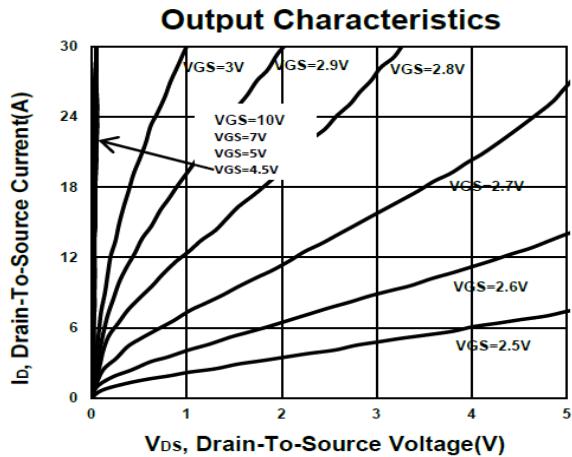
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

<sup>3</sup>The maximum current rating is package limited.

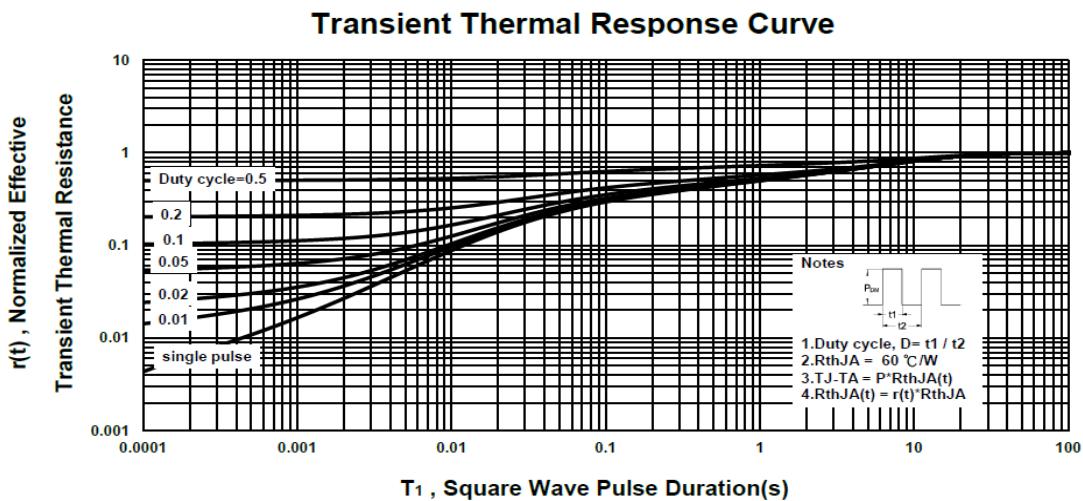
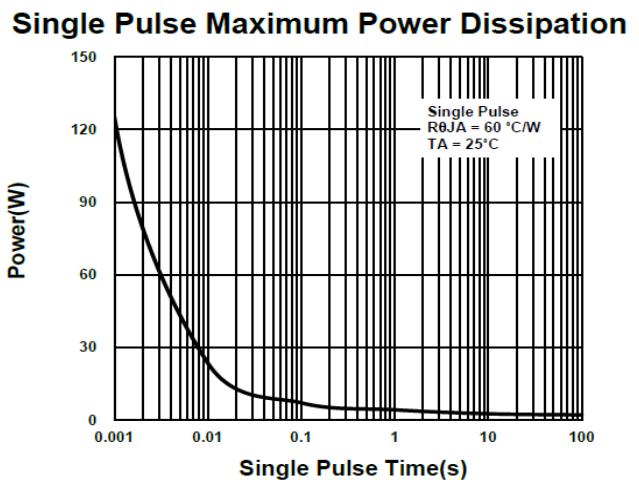
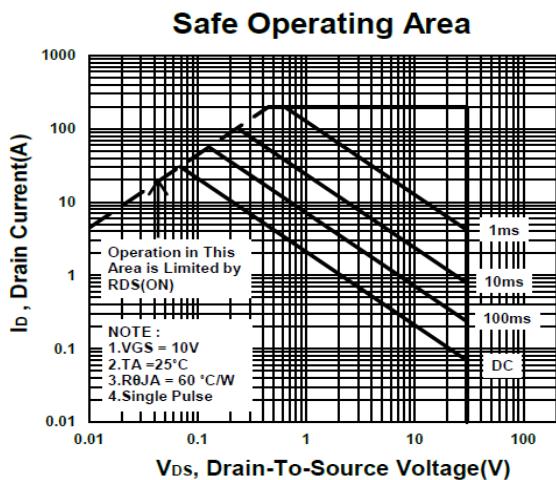
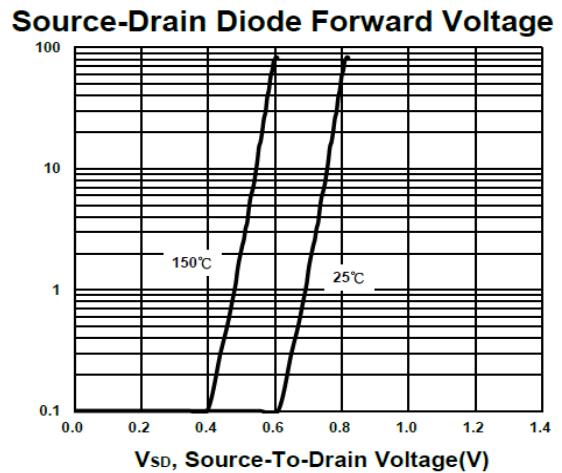
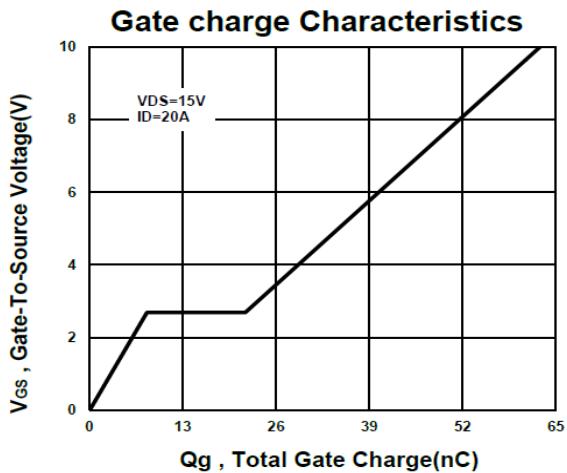
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### N-Channel Enhancement Mode MOSFET



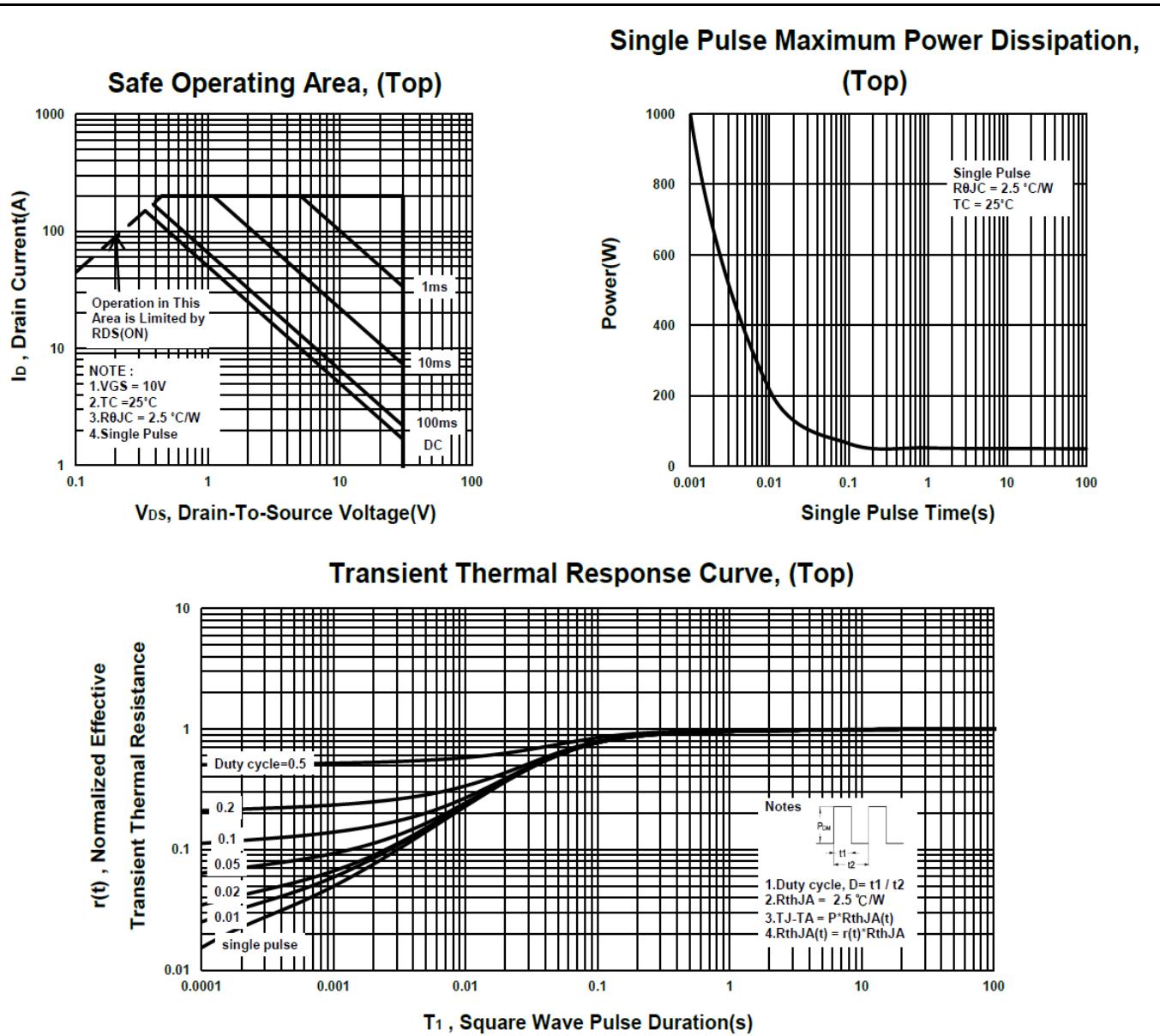
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### N-Channel Enhancement Mode MOSFET



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### N-Channel Enhancement Mode MOSFET



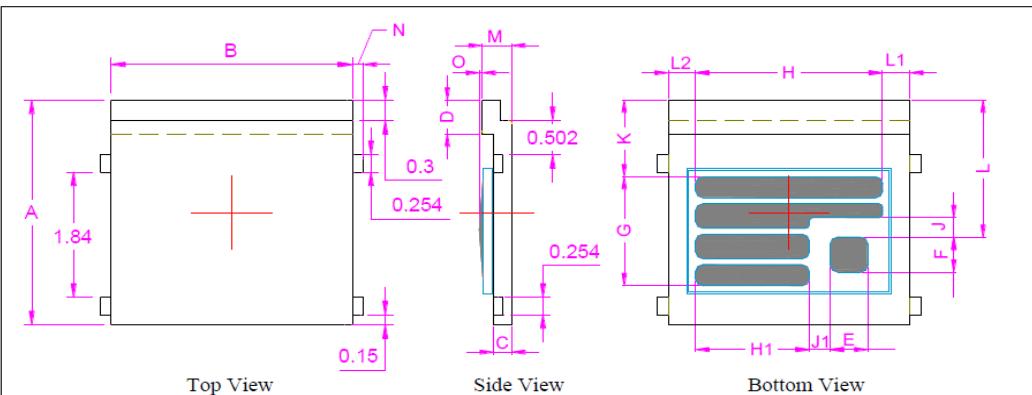
# PR804BA33

## N-Channel Enhancement Mode MOSFET

### Package Dimension

#### PowerFET 3x3 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	3.20	3.30	3.40	J	0.27	0.29	0.31
B	3.20	3.30	3.40	J1	0.27	0.29	0.31
C		0.254		K	1.03	1.13	1.23
D	0.45	0.50	0.55	L	1.91	2.01	2.11
E	0.49	0.51	0.53	L1	0.28	0.38	0.48
F	0.49	0.51	0.53	L2	0.28	0.38	0.48
G	1.58	1.60	1.62	M		0.40	0.45
H	2.53	2.55	2.57	N			0.15
H1	1.53	1.55	1.57	O	0.02		0.08

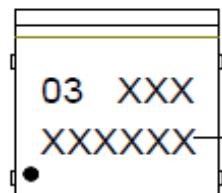


Dimensions Are Exclusive  
Of Burrs ,And Tie Bar  
Protrusions

## **PR804BA33**

### **N-Channel Enhancement Mode MOSFET**

#### **A. Marking Information**



Lot.No(上下排相加共9碼)

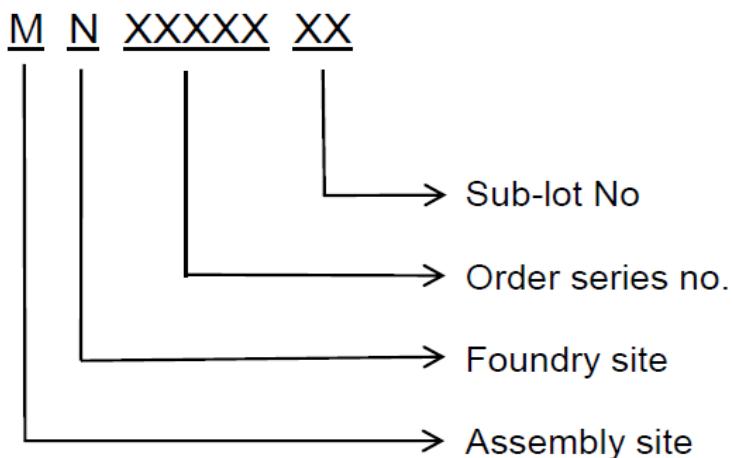
03 : PR804BA33

## **PR804BA33**

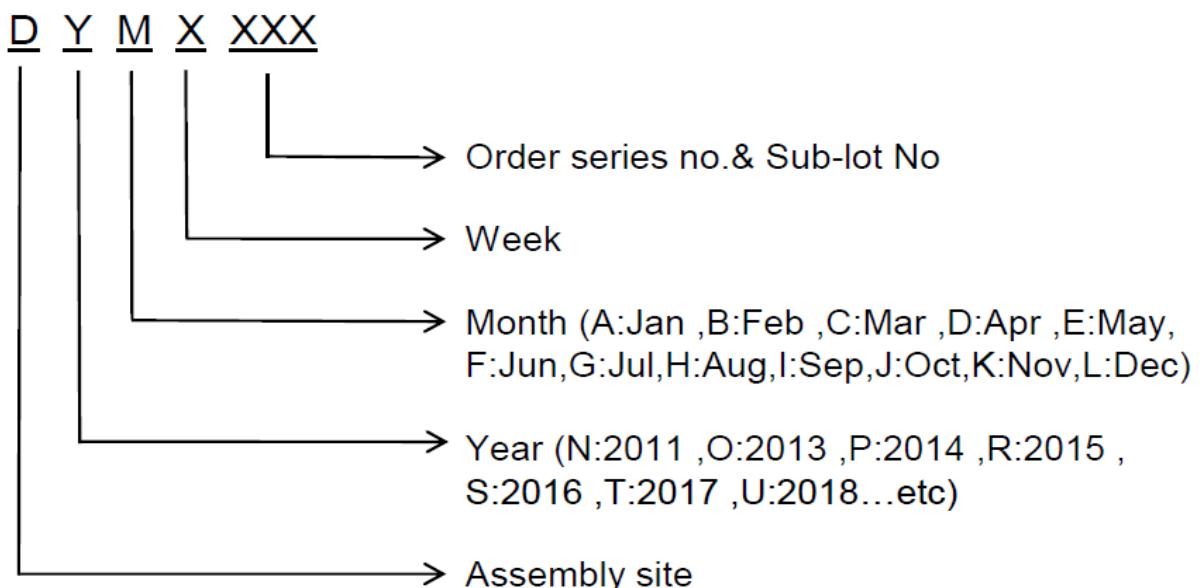
### **N-Channel Enhancement Mode MOSFET**

#### **C. Lot No.&Date Code rule**

##### **1.Lot No.**



##### **2.Date Code**



## PR804BA33 N-Channel Enhancement Mode MOSFET

### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm	
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)	
3	U-NIKC	Height: 4 mm	
4	Package	Height: 2 mm	
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12	
6	Device	Height: 3 mm (Max: 16 Digit)	
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot	
8	D/C	Height: 3 mm (Max: 7 Digit)	
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed	
10	RoHS label	 long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial	
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial	
12	Scan information	Device / Lot / D/C / QTY , Insert “ / ” between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least	

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