



## N-Channel Enhancement Mode Field Effect Transistor

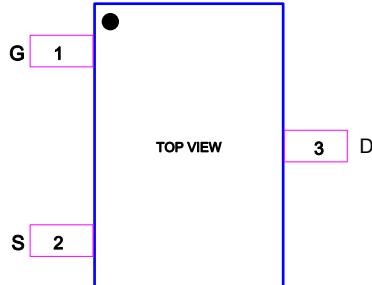
### General Description

The LPM3400 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 1.1V. This device is suitable for use as a load switch or in PWM applications. Standard Product LPM3400 is Pb-free.

### Order Information

LPM3400 □ □ □  
F: Pb-Free  
Package Type:  
B3: SOT23

### Pin Configurations



### Features

- ◆ 20V/5A,  $R_{DS(ON)} < 33m\Omega(\text{max.})$  @  $VGS=4.5V$
- ◆ 20V/4A,  $R_{DS(ON)} < 52m\Omega(\text{max.})$  @  $VGS=2.5V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ SOT23 Package

### Applications

- ✧ Driver for Relay, Solenoid, Motor, LED etc.
- ✧ DC-DC converter circuit
- ✧ Power Switch
- ✧ Load Switch
- ✧ Charging

### Marking Information

Device	Marking	Package	Shipping
LPM340B3F	A2SXX	SOT23	3K



## Absolute Maximum Ratings

Parameter		Symbol	Maximum	Unit
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage		V <sub>GS</sub>	±12	
Continuous Drain Current	TA=25°C	I <sub>D</sub>	5.8	A
	TA=70°C		4.9	
Pulsed Drain Current		I <sub>DM</sub>	30	W
Power Dissipation	TA=25°C	P <sub>D</sub>	1.4	
	TA=70°C		1	
Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

## Thermal resistance ratings

Parameter	Symbol	MAX	Unit
Junction-to-Case Thermal Resistance	R <sub>θJA</sub>	125	°C/W





## Electrical Characteristics

Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	20			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1	$\mu\text{A}$
					5	
$I_{\text{GSS}}$	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=12\text{V}$			$\pm 100$	nA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.7	1.1	1.4	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=5.8\text{A}$		22	28	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=5\text{A}$		27	33	
		$V_{GS}=2.5\text{V}, I_D=4\text{A}$		43	52	
$g_{\text{FS}}$	Forward Transconductance	$V_{DS}=5\text{V}, I_D=5\text{A}$	10	15		S
$V_{\text{SD}}$	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.7	1	V
$I_S$	Maximum Body-Diode Continuous Current				2.5	A
<b>DYNAMIC PARAMETERS</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$		400		pF
$C_{\text{oss}}$	Output Capacitance			99		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			77		pF
$R_g$	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		1.2	3.6	$\Omega$
<b>SWITCHING PARAMETERS</b>						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=4.5\text{V}, V_{DS}=15\text{V}, I_D=5.8\text{A}$		9.7		nC
$Q_g(4.5\text{V})$	Total Gate Charge			1.6		nC
$Q_{\text{gs}}$	Gate Source Charge			3.1		nC
$t_{\text{D}(\text{on})}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DS}=15\text{V}, R_L=2.7\Omega, R_{\text{GEN}}=3\Omega$		3.3		ns
$t_r$	Turn-On Rise Time			4.8		ns
$t_{\text{D}(\text{off})}$	Turn-Off Delay Time			26.3		ns
$t_f$	Turn-Off Fall Time			4.1		ns
$t_{\text{rr}}$	Body Diode Reverse Recovery Time	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		16		ns
$Q_{\text{rr}}$	Body Diode Reverse Recovery Charge	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		8.9		nC



## Typical Characteristics

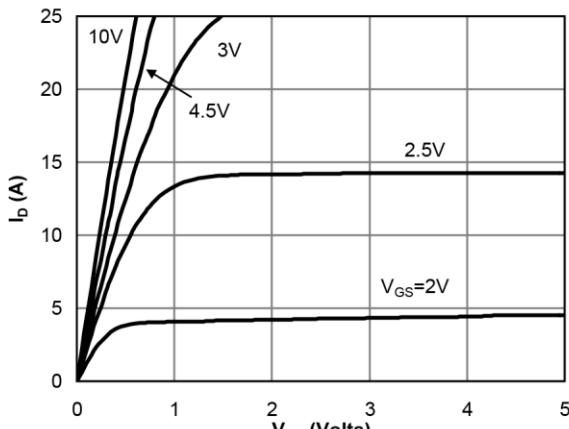


Fig 1: On-Region Characteristics

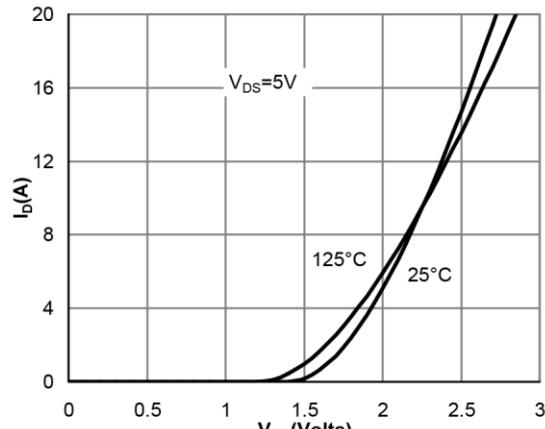


Figure 2: Transfer Characteristics

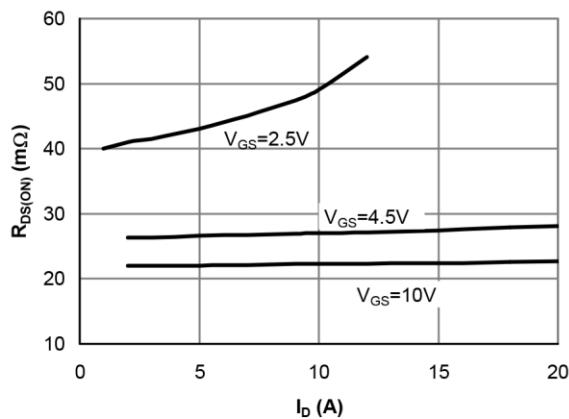


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

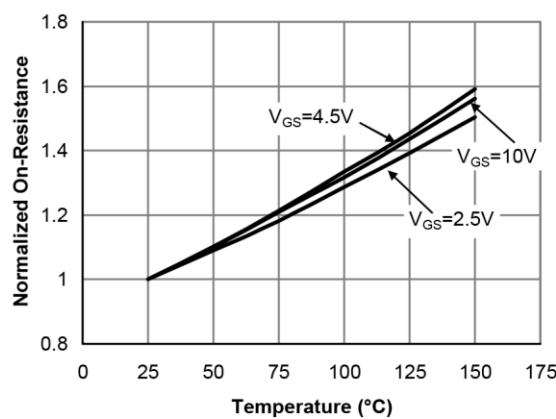


Figure 4: On-Resistance vs. Junction Temperature

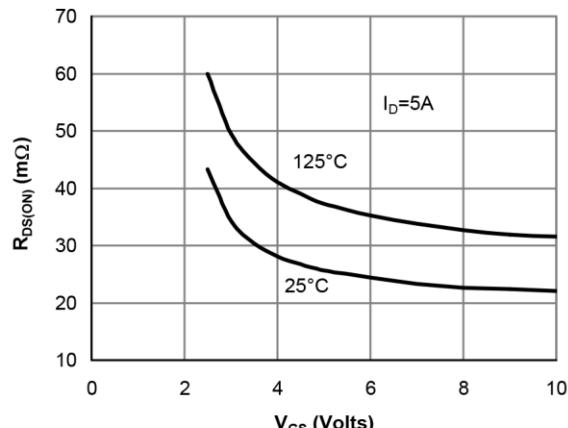


Figure 5: On-Resistance vs. Gate-Source Voltage

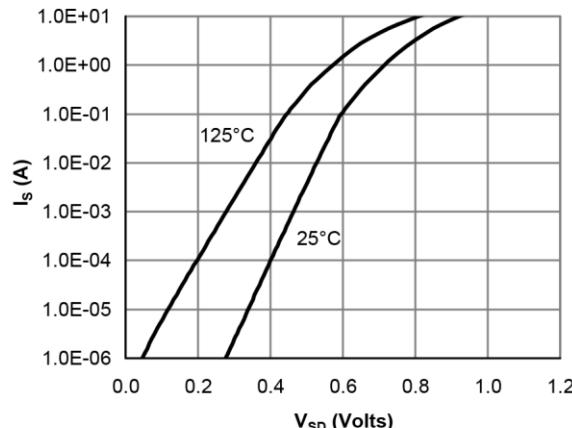


Figure 6: Body-Diode Characteristics

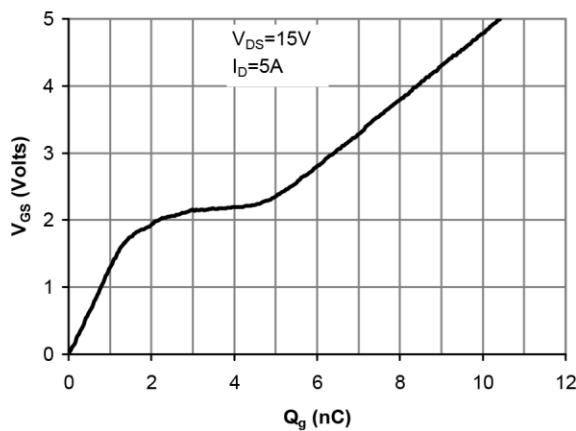


Figure 7: Gate-Charge Characteristics

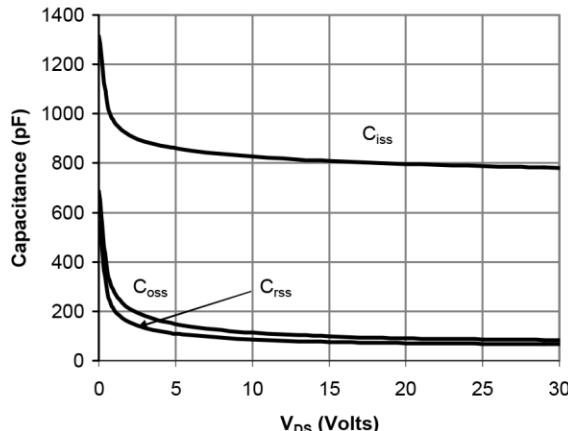


Figure 8: Capacitance Characteristics

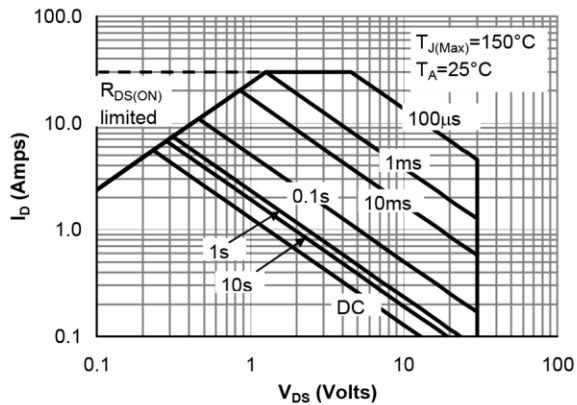


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

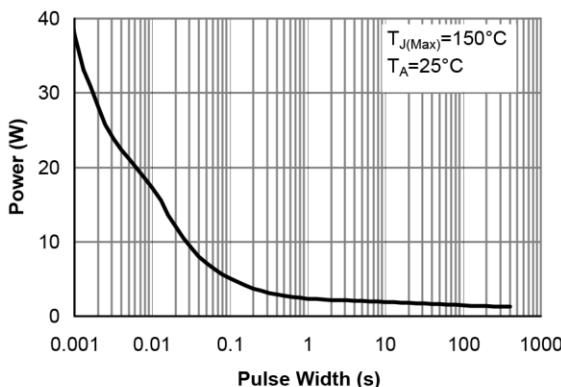


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

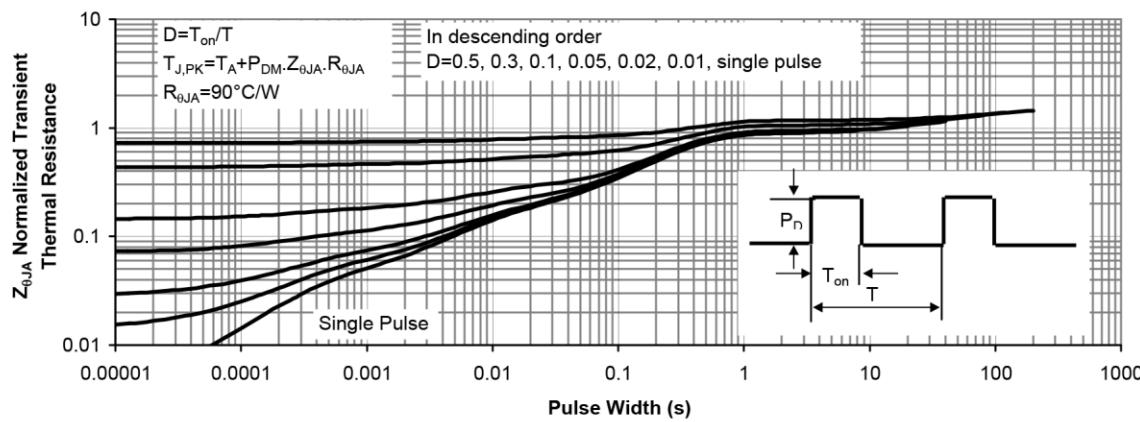
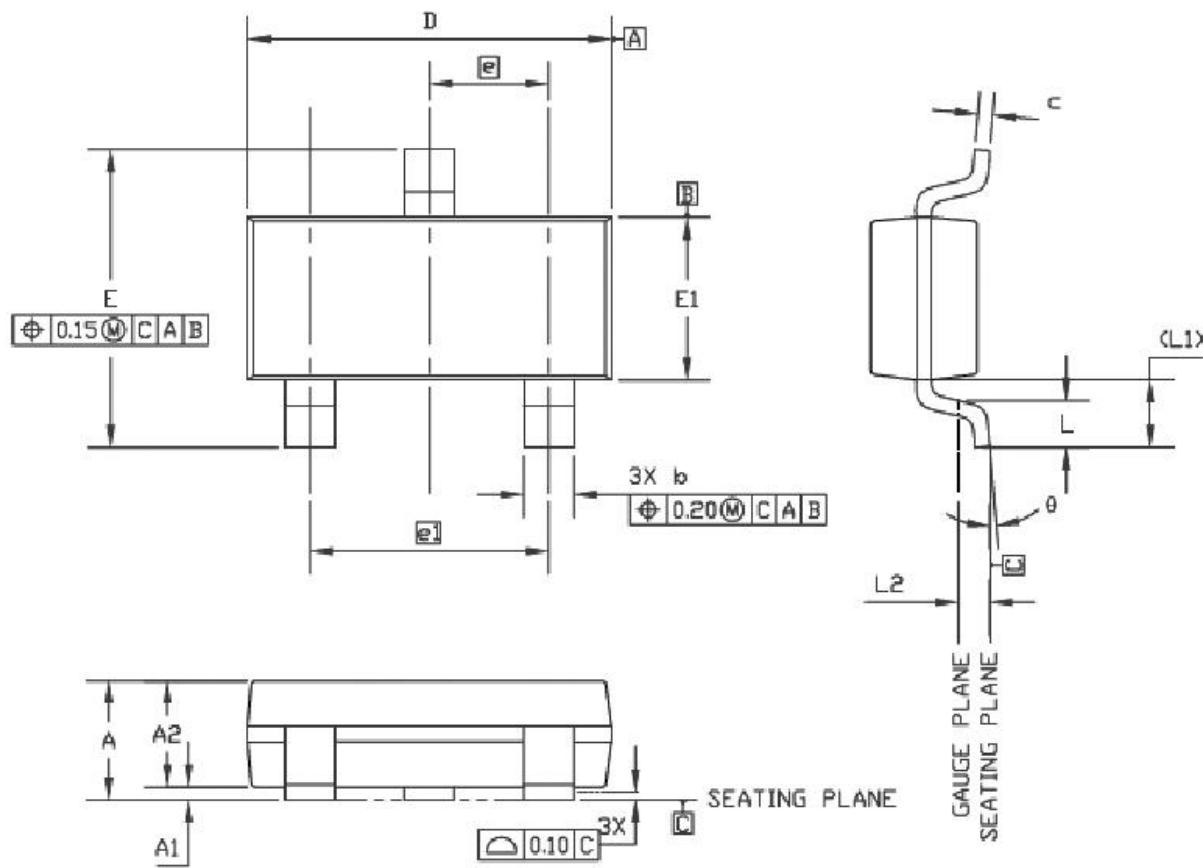


Figure 11: Normalized Maximum Transient Thermal Impedance

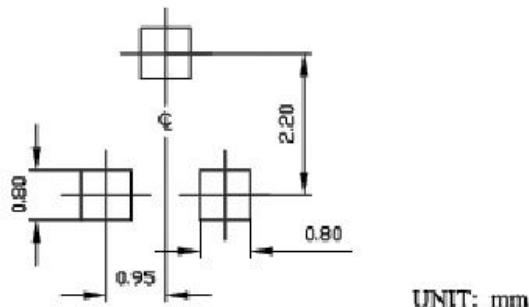


## Packaging Information

### SOT-23 STANDARD PACKAGE OUTLINE



#### RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.75	—	1.17	0.030	—	0.046
A1	0.05	—	0.15	0.002	—	0.006
A2	0.70	0.85	1.02	0.028	0.033	0.040
b	0.30	—	0.50	0.012	—	0.020
c	0.08	—	0.20	0.003	—	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	2.10	—	2.64	0.083	—	0.104
E1	1.20	1.30	1.40	0.047	0.051	0.055
e	0.95 BSC			0.037 BSC		
el	1.90 BSC			0.075 BSC		
L	0.40	0.50	0.60	0.016	0.020	0.024
L1	0.54 REF			0.021REF		
L2	0.25			0.010		
θ1	0°	—	8°	0°	—	8°

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