

## General Description

The WSD4062DN56 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The WSD4062DN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

## Product Summary

BVDSS	RDSON	ID
40V	7.0mΩ	62A

## Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application

## DFN5X6-8L Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	62	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	29	A
$I_{DM}$	Pulsed Drain Current <sup>a</sup>	60	A
EAS	Single Pulse Avalanche Energy <sup>b</sup>	42	mJ
$I_{AS}$	Avalanche Current	13	A
$P_D@T_a=25^\circ\text{C}$	Total Power Dissipation	2.1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	60	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	3.9	$^\circ\text{C}/\text{W}$

Note a : Package is limited to 60A.

Note b : UIS tested and pulse width limited by maximum junction temperature  $150^\circ\text{C}$  (initial temperature  $T_J=25^\circ\text{C}$ ).

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.043	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	7.0	10.5	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	---	9.0	13.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.5	1.8	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-6.94	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	2	uA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	10	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	---	33	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	1.1	2	Ω
Q <sub>g</sub>	Total Gate Charge (10V)	V <sub>DS</sub> =20V, V <sub>GS</sub> =10V, I <sub>D</sub> =40A	---	76	91	nC
Q <sub>gs</sub>	Gate-Source Charge		---	12	14.4	
Q <sub>gd</sub>	Gate-Drain Charge		---	15.5	18.6	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V, V <sub>GEN</sub> =10V, R <sub>G</sub> =1Ω, I <sub>D</sub> =1A, RL=15Ω.	---	12	16	ns
T <sub>r</sub>	Rise Time		---	9	12	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	25	32	
T <sub>f</sub>	Fall Time		---	6	9	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz	---	1150	---	pF
C <sub>oss</sub>	Output Capacitance		---	140	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	90	---	

**Diode Characteristics**

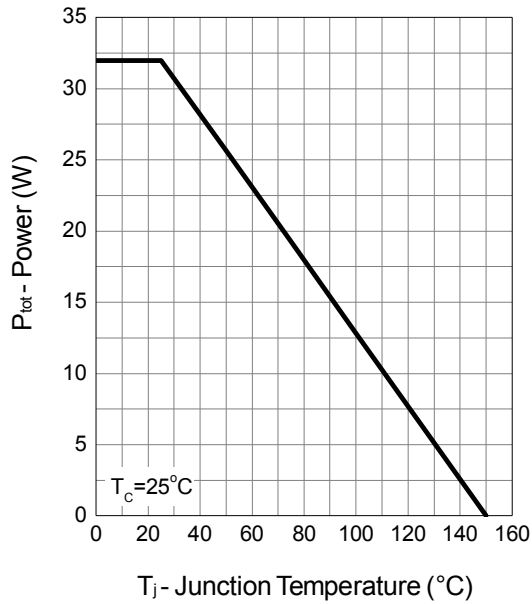
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	20	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>		---	---	60	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A, T <sub>J</sub> =25°C	---	---	1.3	V

Note :

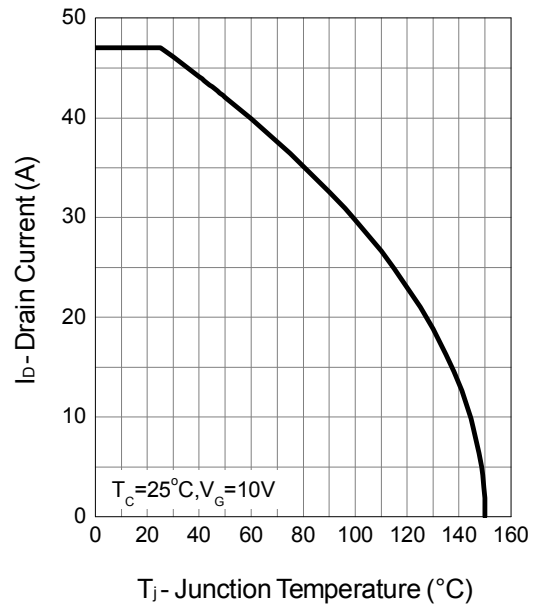
- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper, t<10sec .
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.5mH, I<sub>AS</sub>=13A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.
- 7.Package limitation current is 60A.

**Typical Characteristics**

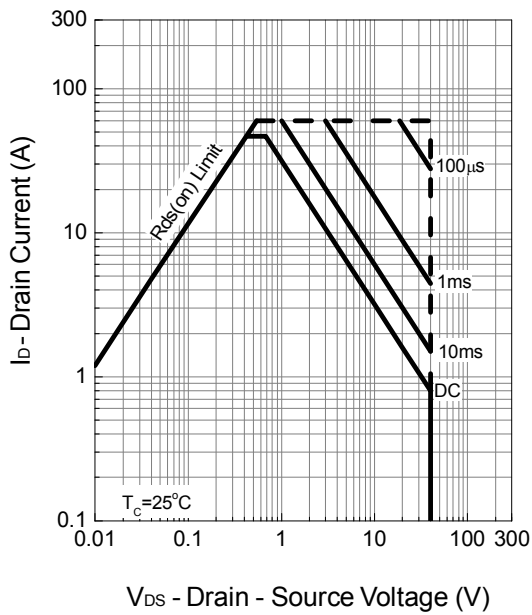
**Power Dissipation**



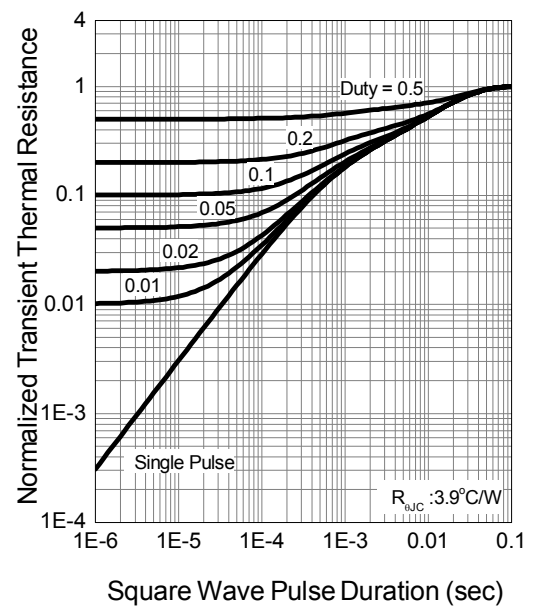
**Drain Current**



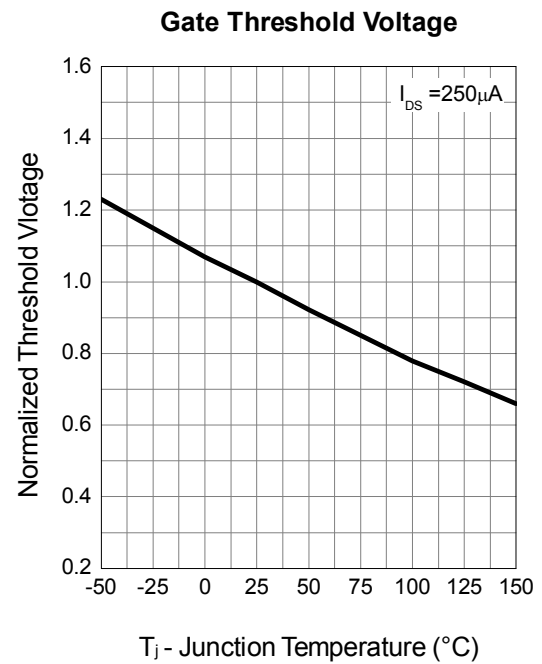
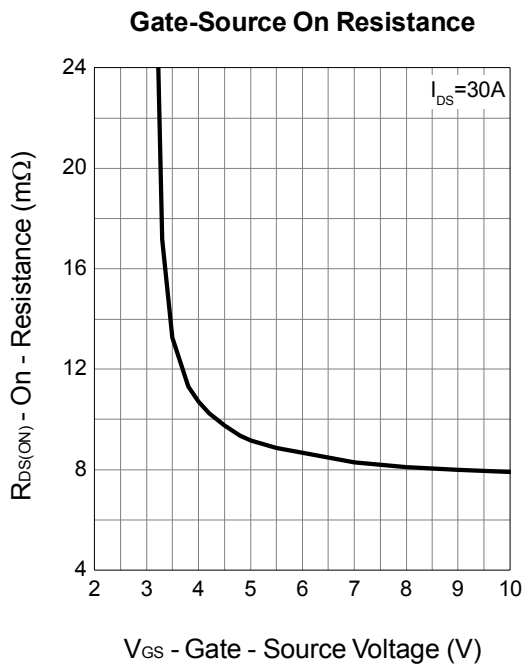
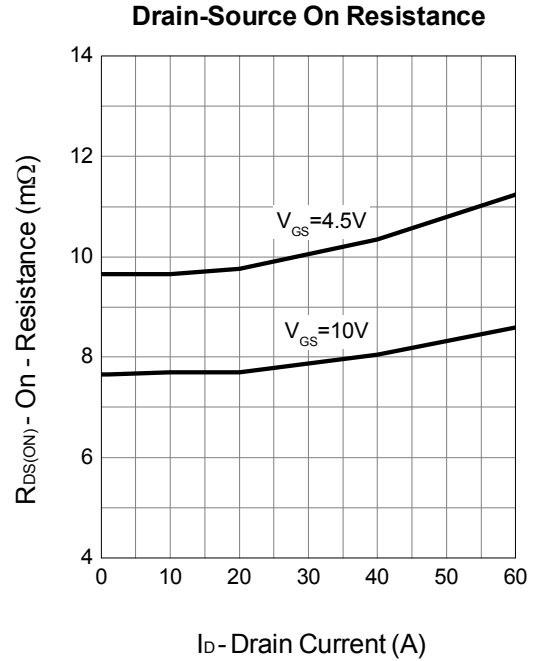
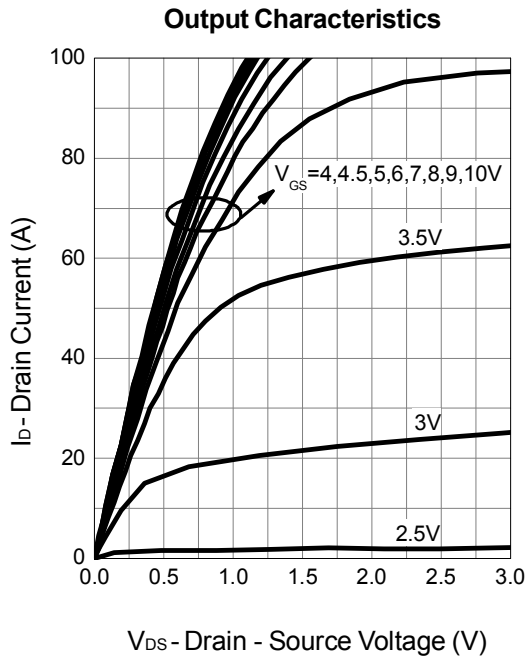
**Safe Operation Area**



**Thermal Transient Impedance**

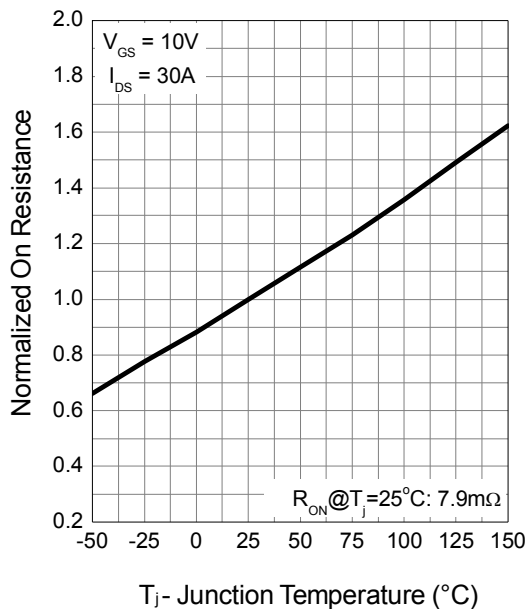


**Typical Characteristics**

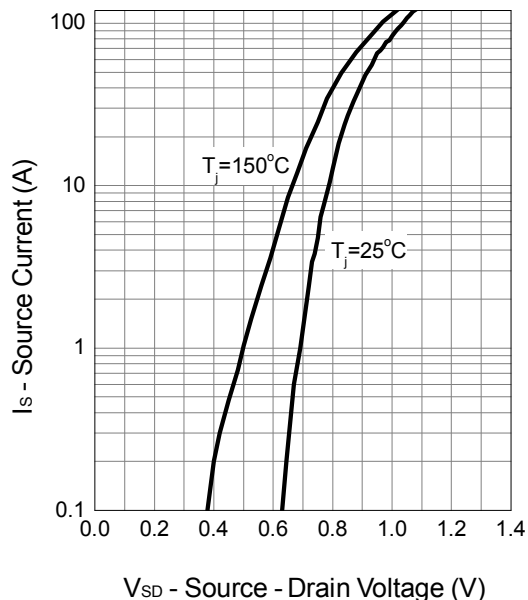


**Typical Characteristics**

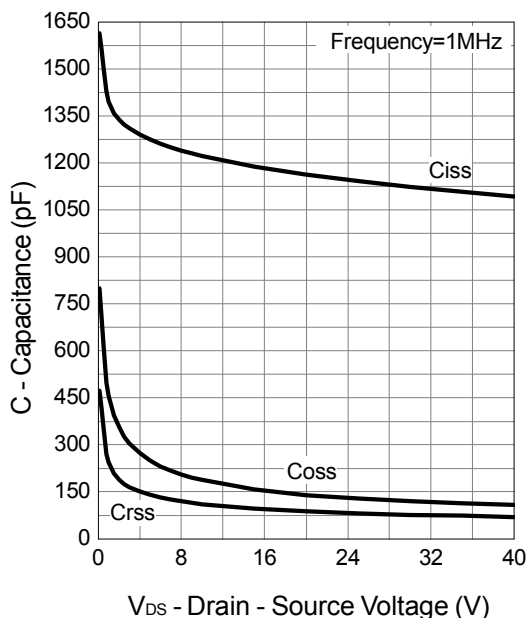
**Drain-Source On Resistance**



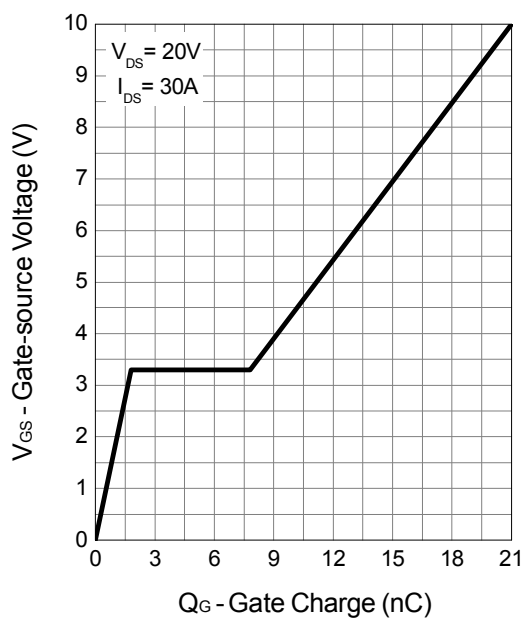
**Source-Drain Diode Forward**



**Capacitance**

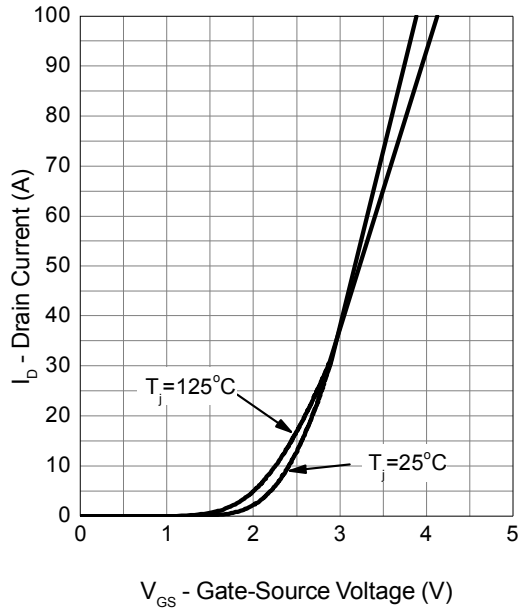


**Gate Charge**

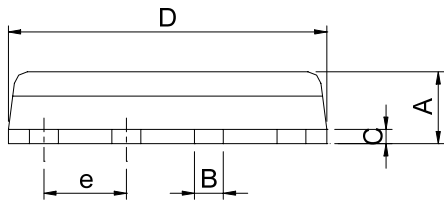
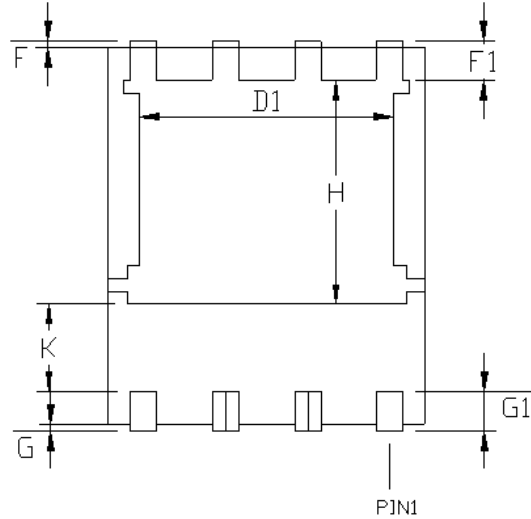
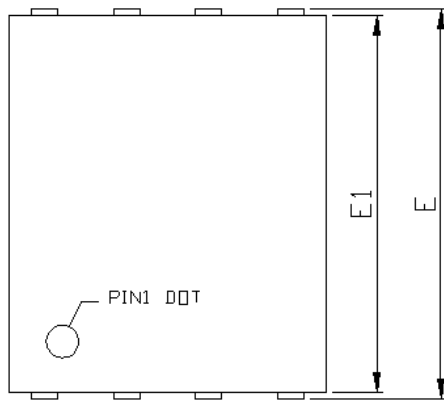


**Typical Characteristics**

**Transfer Characteristics**

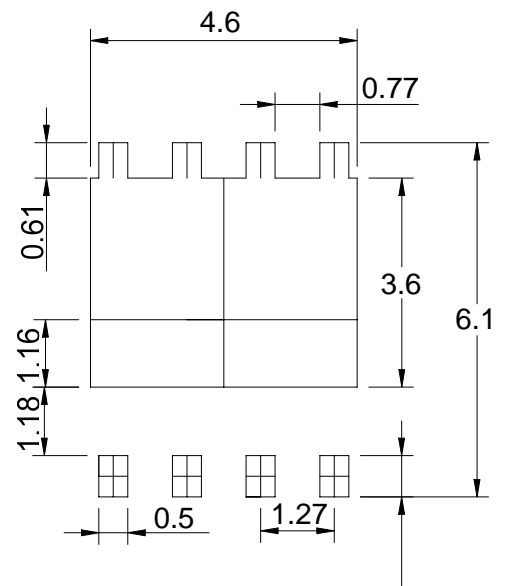


Package DFN5x6-8L



Symbol	DFN5x6-8L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.20	0.035	0.047
B	0.3	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	3.6	4.40	0.141	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27 BSC		0.050 BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.9	0.131	0.154
K	0.762	-	0.03	-

**RECOMMENDED LAND PATTERN**



UNIT: mm

Note : 1.Dimension D, D1 and E1 do not include mold flash or protrusions.

Mold flash or protrusions shall not exceed 10 mil.



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