

Dual Enhancement Mode MOSFET (N-and P-Channel)

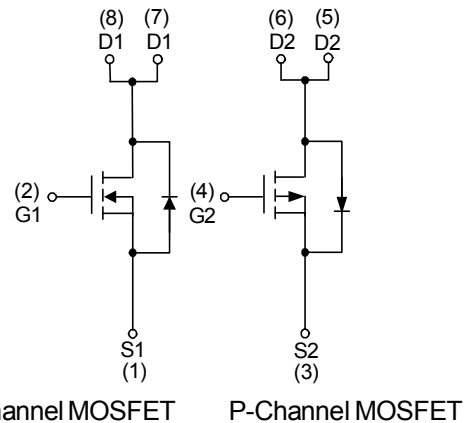
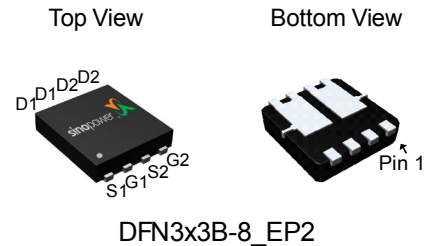
Features

- N Channel**
 30V/11A,
 $R_{DS(ON)} = 34.5m\Omega$ (max.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 60m\Omega$ (max.) @ $V_{GS} = 4.5V$
- P Channel**
 -30V/-13.3A,
 $R_{DS(ON)} = 39m\Omega$ (max.) @ $V_{GS} = -10V$
 $R_{DS(ON)} = 61m\Omega$ (max.) @ $V_{GS} = -4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)

Applications

- Synchronous Rectification.
- Motor Control.
- High Current, High Speed Switching.
- Portable equipment application.

Pin Description



Ordering and Marking Information

<p>SM8404CS □□□-□□□</p> <p>Assembly Material Handling Code Temperature Range Package Code</p>	<p>Package Code QA : DFN3x3B-8_EP2 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device</p>			
<p>SM8404CS QA :</p> <table border="1" data-bbox="454 1579 566 1668"> <tr><td>SM</td></tr> <tr><td>8404</td></tr> <tr><td>XXXXX</td></tr> </table>	SM	8404	XXXXX	<p>XXXXX - Lot Code</p>
SM				
8404				
XXXXX				

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		N Channel	P Channel	Unit
Common Ratings					
V_{DSS}	Drain-Source Voltage		30	-30	V
V_{GSS}	Gate-Source Voltage		± 20	± 20	
T_J	Maximum Junction Temperature		150		$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-55 to 150		
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	5.5	-6.6	A
I_D^d	Continuous Drain Current	$T_C=25^\circ\text{C}$	11	-13.3	A
		$T_C=100^\circ\text{C}$	7	-8.4	
I_{DM}^a	Pulse Drain Current Tested ($V_{GS}=\pm 10\text{V}$)	$T_C=25^\circ\text{C}$	44	-53	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	6.25	10.4	W
		$T_C=100^\circ\text{C}$	2.5	4.2	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	20	12	$^\circ\text{C/W}$
I_D^d	Continuous Drain Current ($V_{GS}=\pm 10\text{V}$)	$T_A=25^\circ\text{C}$	6.3	-6	A
		$T_A=70^\circ\text{C}$	5	-4.8	
P_D^b	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	2.1	2.1	W
		$T_A=70^\circ\text{C}$	1.3	1.3	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	60	60	$^\circ\text{C/W}$
		Steady State	100	100	
I_{AS}^c	Avalanche Current, Single pulse	$L=0.5\text{mH}$	5.4	-9	A
E_{AS}^c	Avalanche Energy, Single pulse	$L=0.5\text{mH}$	7.3	20	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : $R_{\theta JA}$ steady state $t=999\text{s}$. $R_{\theta JA}$ is measured with the device mounted on 1in^2 , FR-4 board with 2oz. Copper.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Note d : $t < 10\text{s}$.

N Channel Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	N Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.3	1.8	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =6.3A	-	28.5	34.5	mΩ
		V _{GS} =4.5V, I _{DS} =4.8A	-	45	60	
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =1A, V _{GS} =0V	-	0.75	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =6.3A, dI _{SD} /dt=100A/μs	-	9	-	ns
Q _{rr}	Reverse Recovery Charge		-	4.5	-	nC
Dynamic Characteristics^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.7	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz	-	250	-	pF
C _{oss}	Output Capacitance		-	40	-	
C _{rss}	Reverse Transfer Capacitance		-	30	-	
t _{d(ON)}	Turn-on Delay Time		V _{DD} =15V, R _L =15Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	5	9
t _r	Turn-on Rise Time	-		11	20	
t _{d(OFF)}	Turn-off Delay Time	-		11.5	20.5	
t _f	Turn-off Fall Time	-		2.6	4.8	
Gate Charge Characteristics^f						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _{DS} =6.3A	-	6.3	9.5	nC
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =4.5V, I _{DS} =6.3A	-	2.7	4	
Q _{gs}	Gate-Source Charge		-	1.3	-	
Q _{gd}	Gate-Drain Charge		-	1.7	-	

Note e : Pulse test ; pulse width ≤ 300μs, duty cycle ≤ 2%.

Note f : Guaranteed by design, not subject to production testing.

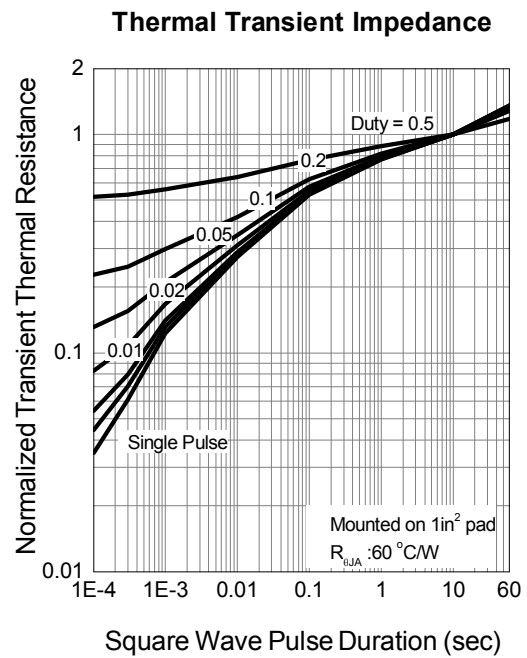
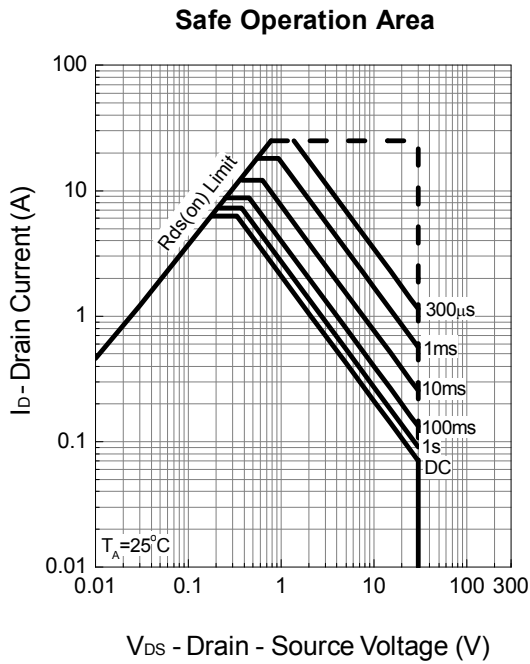
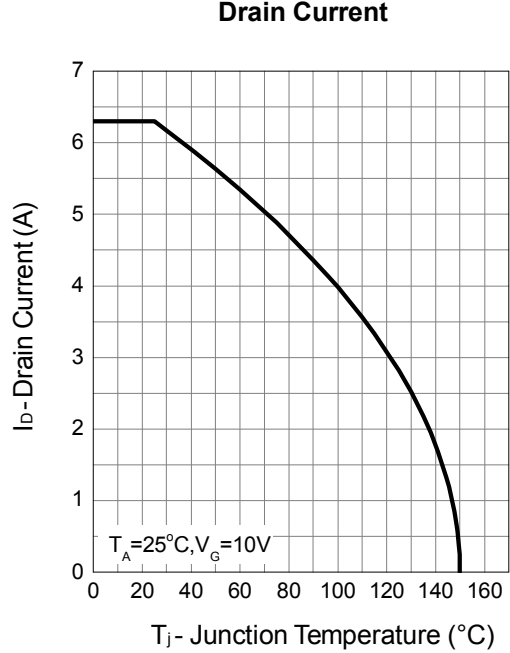
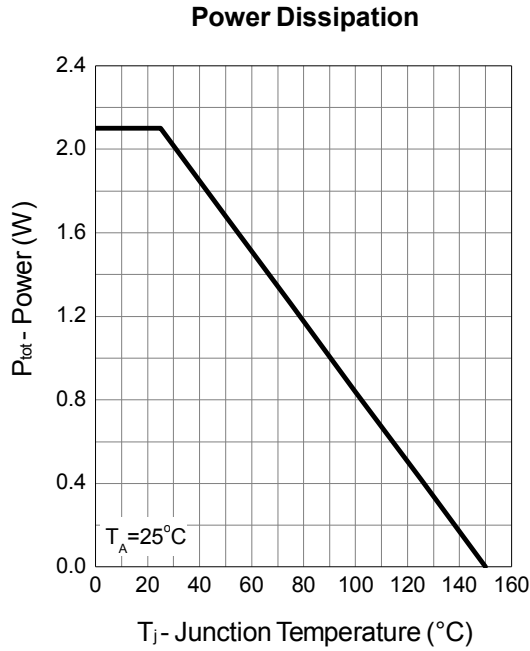
P Channel Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	P Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
		T _J =85°C	-	-	-30	mA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1.3	-1.8	-2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-6A	-	31	39	mΩ
		V _{GS} =-4.5V, I _{DS} =-3A	-	45	61	
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	-	-0.75	-1	V
t _{rr}	Reverse Recovery Time	I _{DS} =-6A, di _{SD} /dt=100A/μs	-	13	-	ns
Q _{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	3.3	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, Frequency=1.0MHz	-	580	-	pF
C _{oss}	Output Capacitance		-	105	-	
C _{rss}	Reverse Transfer Capacitance		-	72	-	
t _{d(ON)}	Turn-on Delay Time		-	8.7	15.5	ns
t _r	Turn-on Rise Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	10	18	
t _{d(OFF)}	Turn-off Delay Time		-	22	40	
t _f	Turn-off Fall Time		-	9	16	
Gate Charge Characteristics^f						
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-6A	-	13	19.5	nC
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-4.5V, I _{DS} =-6A	-	6	9	
Q _{gs}	Gate-Source Charge		-	2	-	
Q _{gd}	Gate-Drain Charge		-	3	-	

Note e : Pulse test; pulse width≤300μs, duty cycle≤2%.

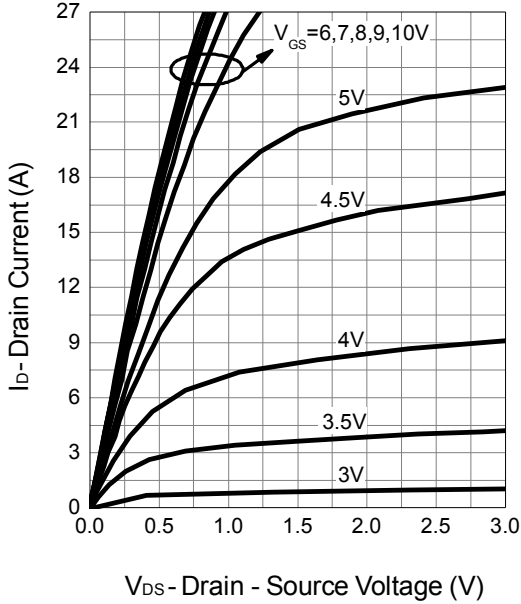
Note f : Guaranteed by design, not subject to production testing.

N Channel Typical Operating Characteristics

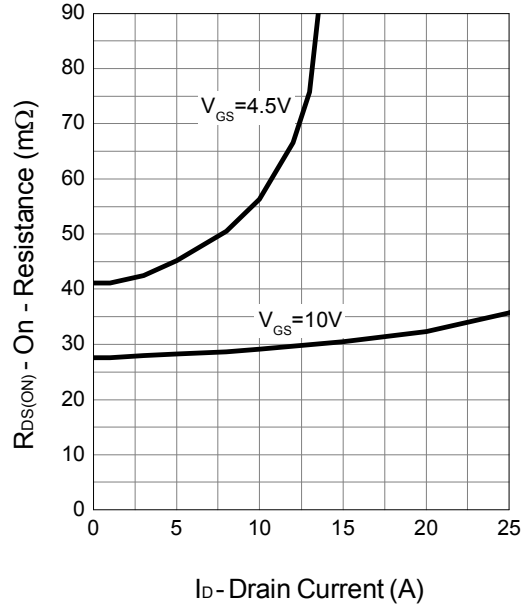


N Channel Typical Operating Characteristics (Cont.)

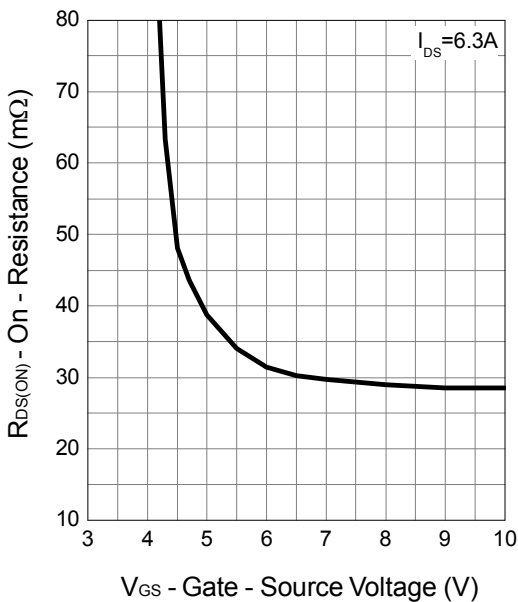
Output Characteristics



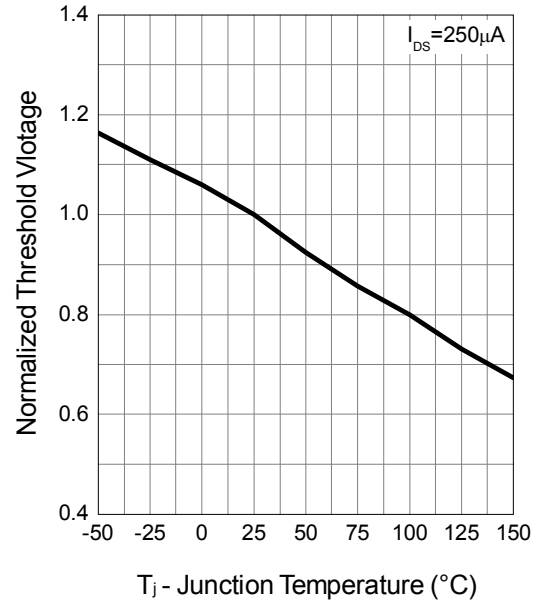
Drain-Source On Resistance



Gate-Source On Resistance

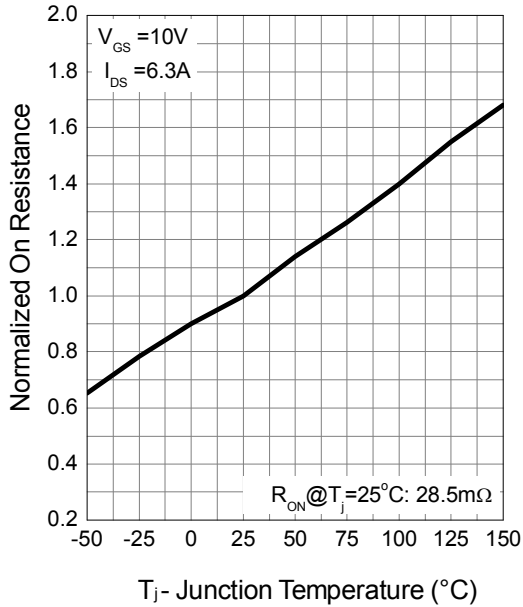


Gate Threshold Voltage

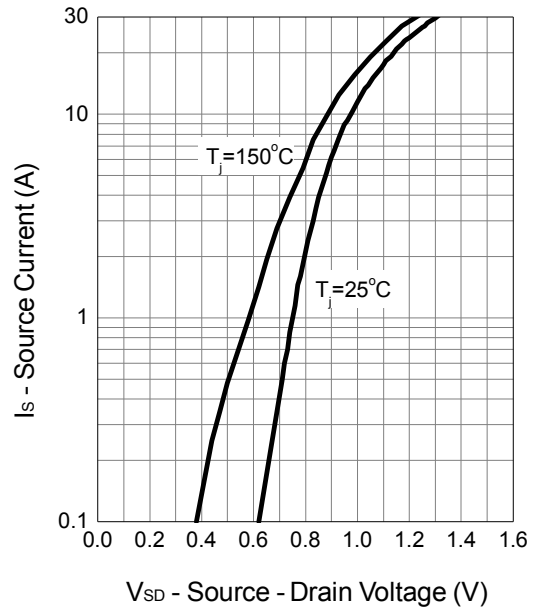


N Channel Typical Operating Characteristics (Cont.)

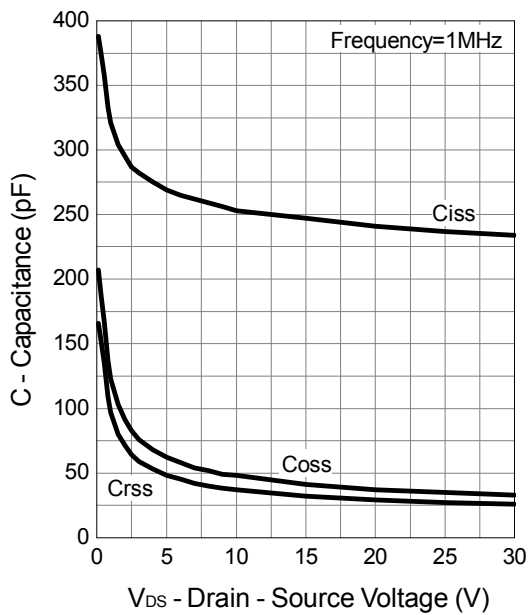
Drain-Source On Resistance



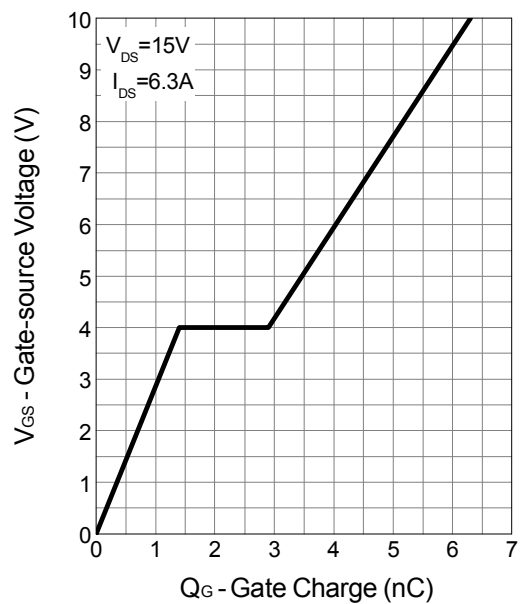
Source-Drain Diode Forward



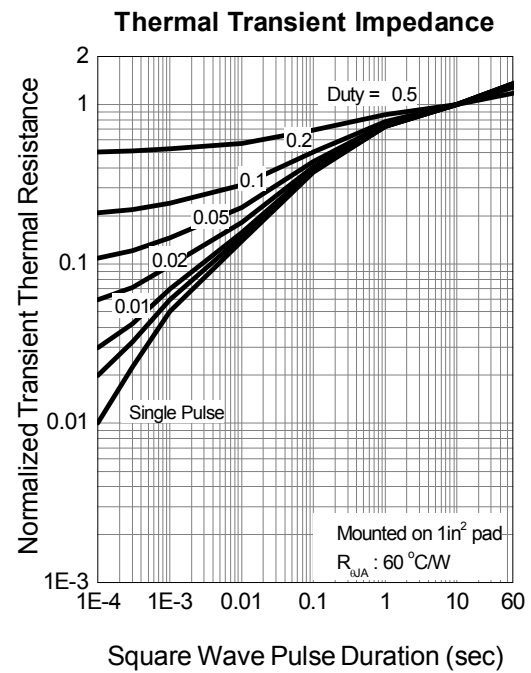
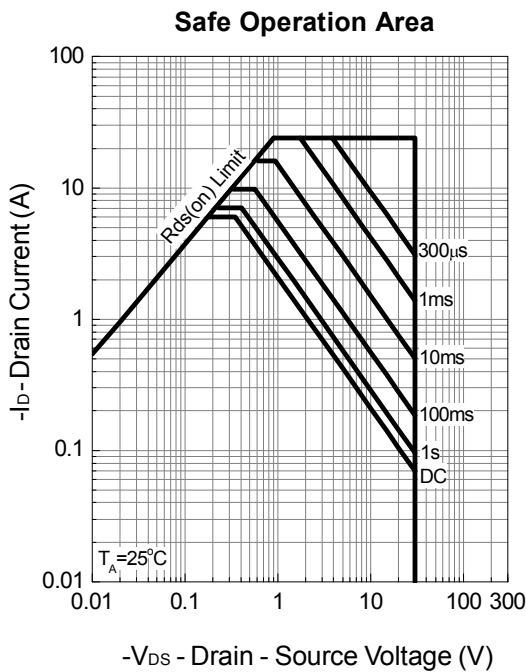
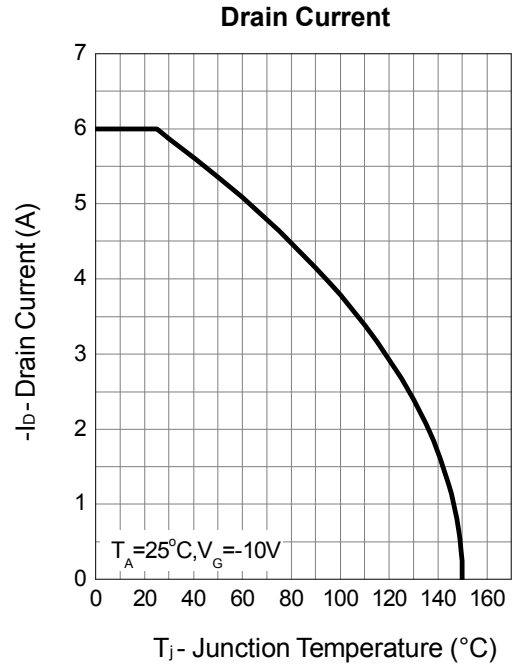
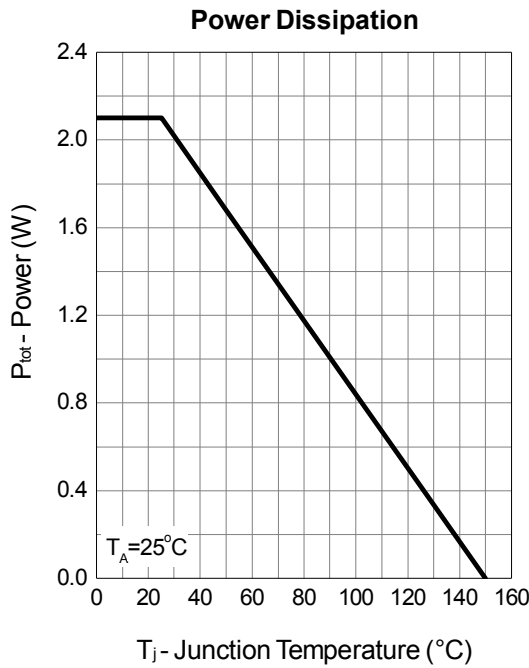
Capacitance



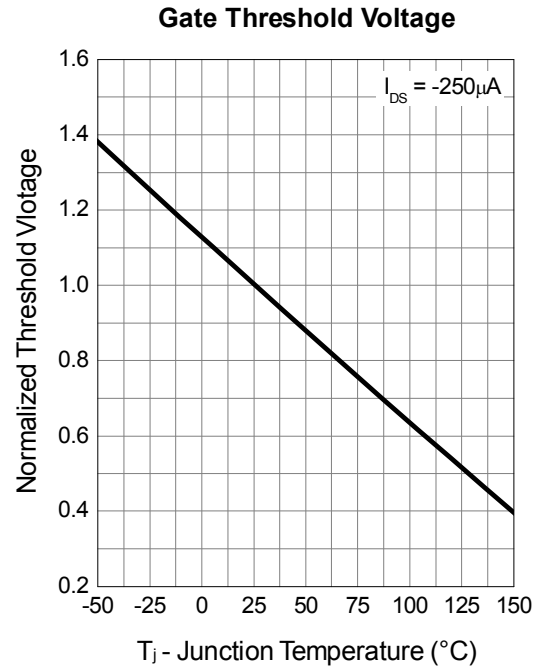
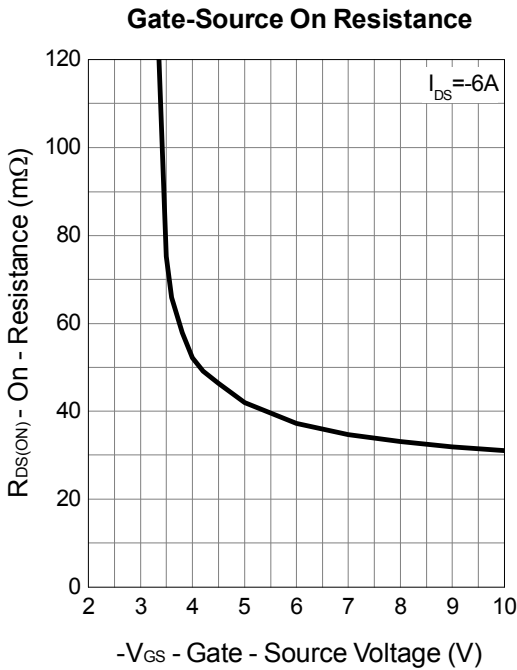
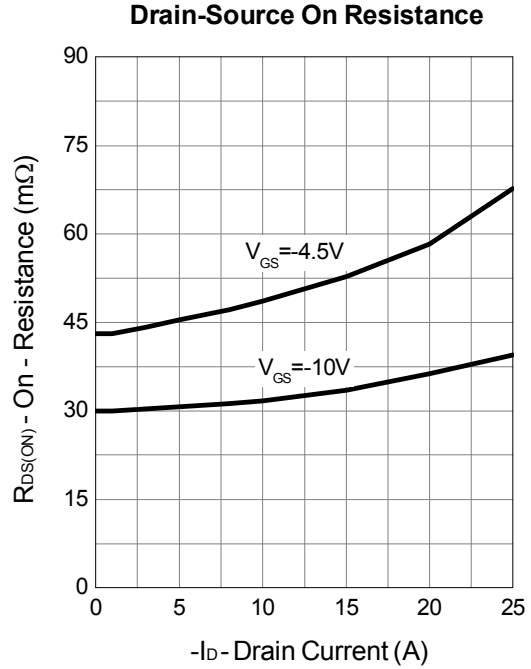
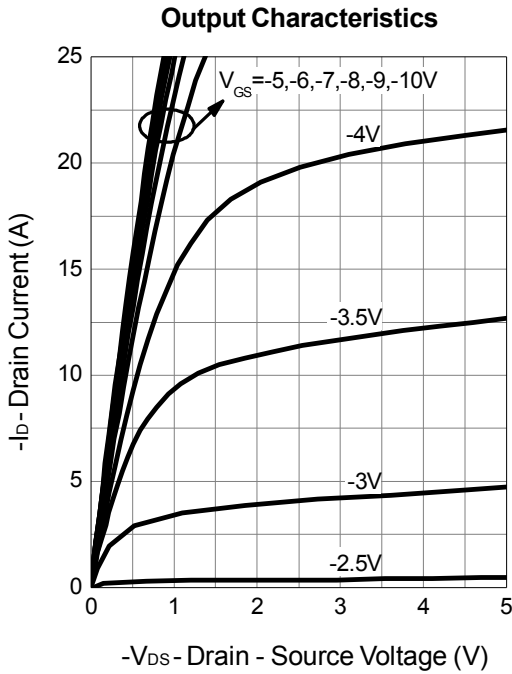
Gate Charge



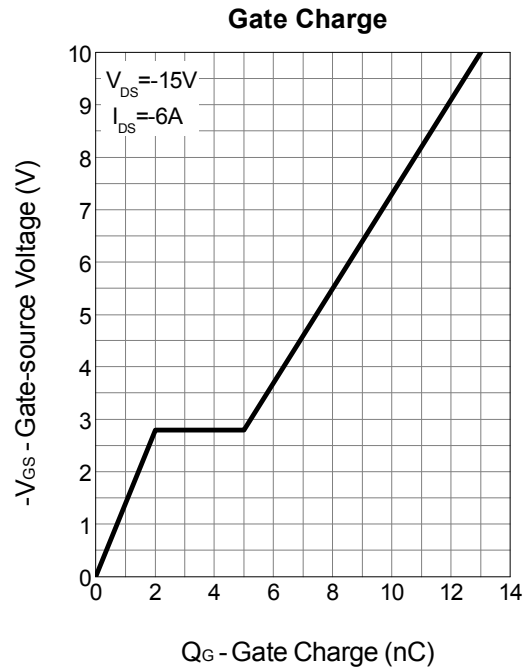
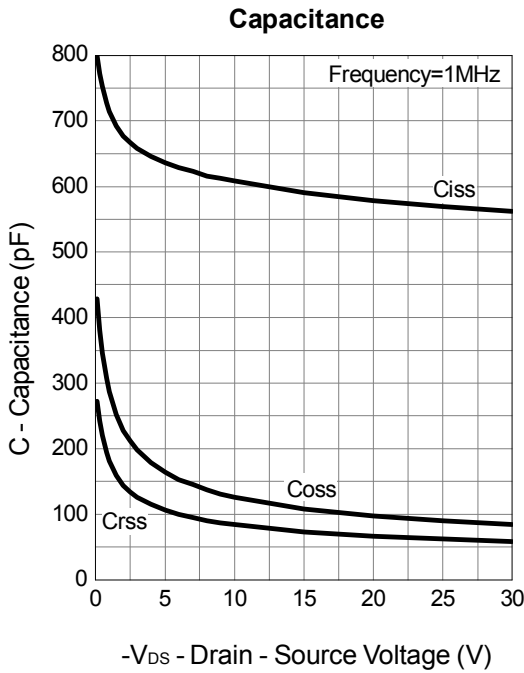
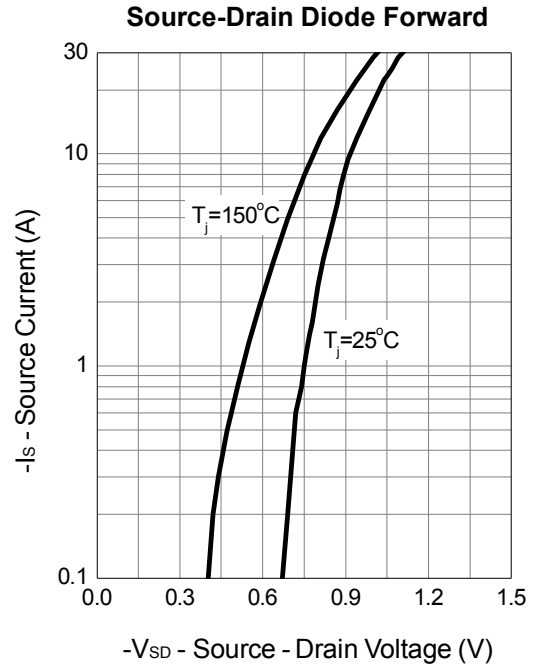
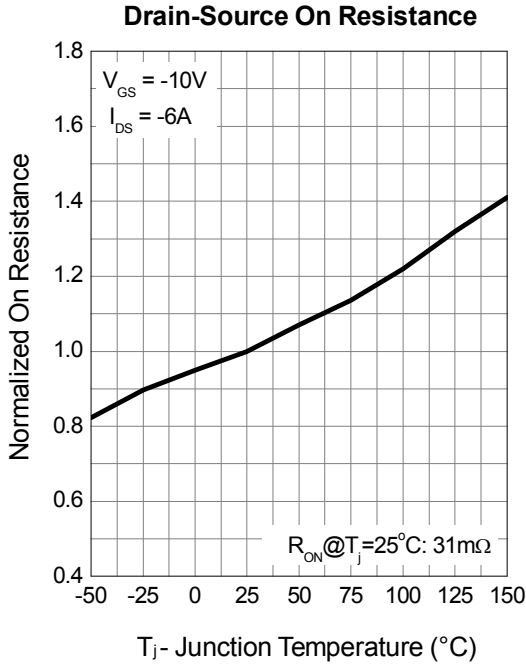
P Channel Typical Operating Characteristics



P Channel Typical Operating Characteristics (Cont.)

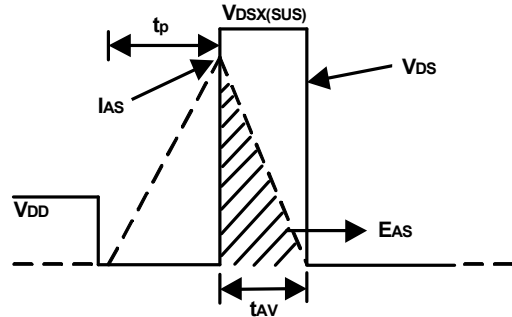
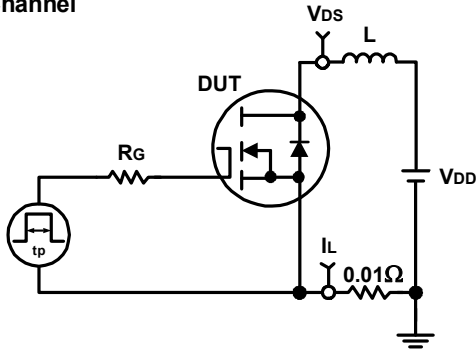


P Channel Typical Operating Characteristics (Cont.)

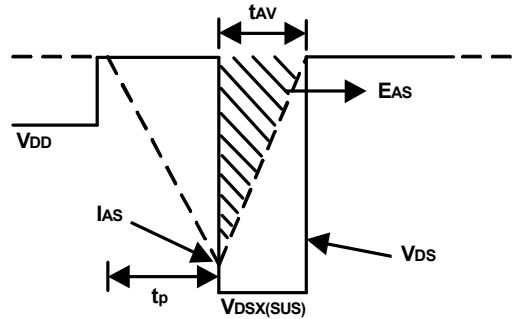
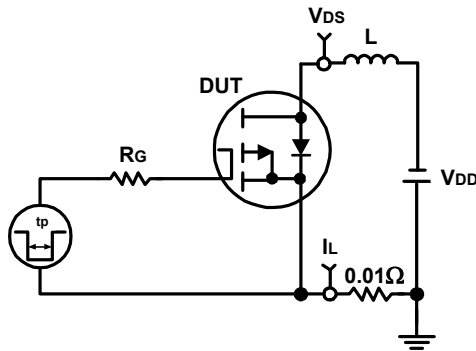


Avalanche Test Circuit and Waveforms

N Channel

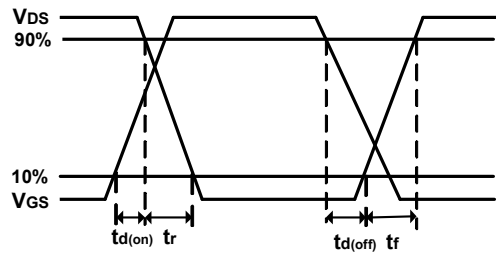
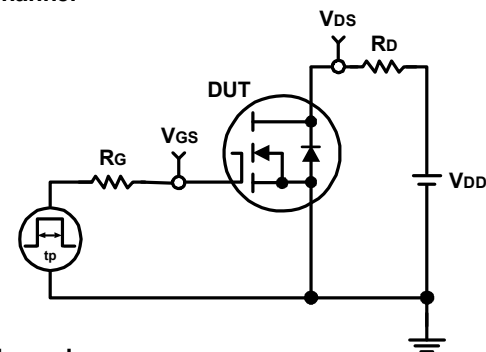


P Channel

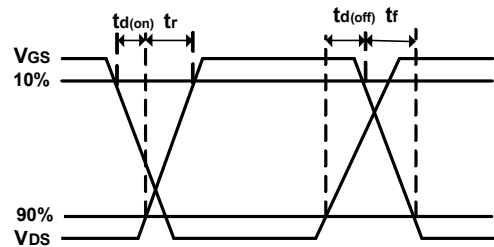
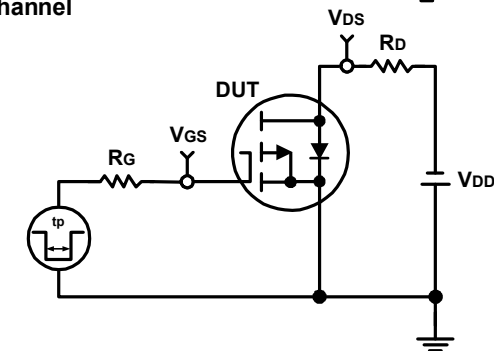


Switching Time Test Circuit and Waveforms

N Channel



P Channel



Disclaimer

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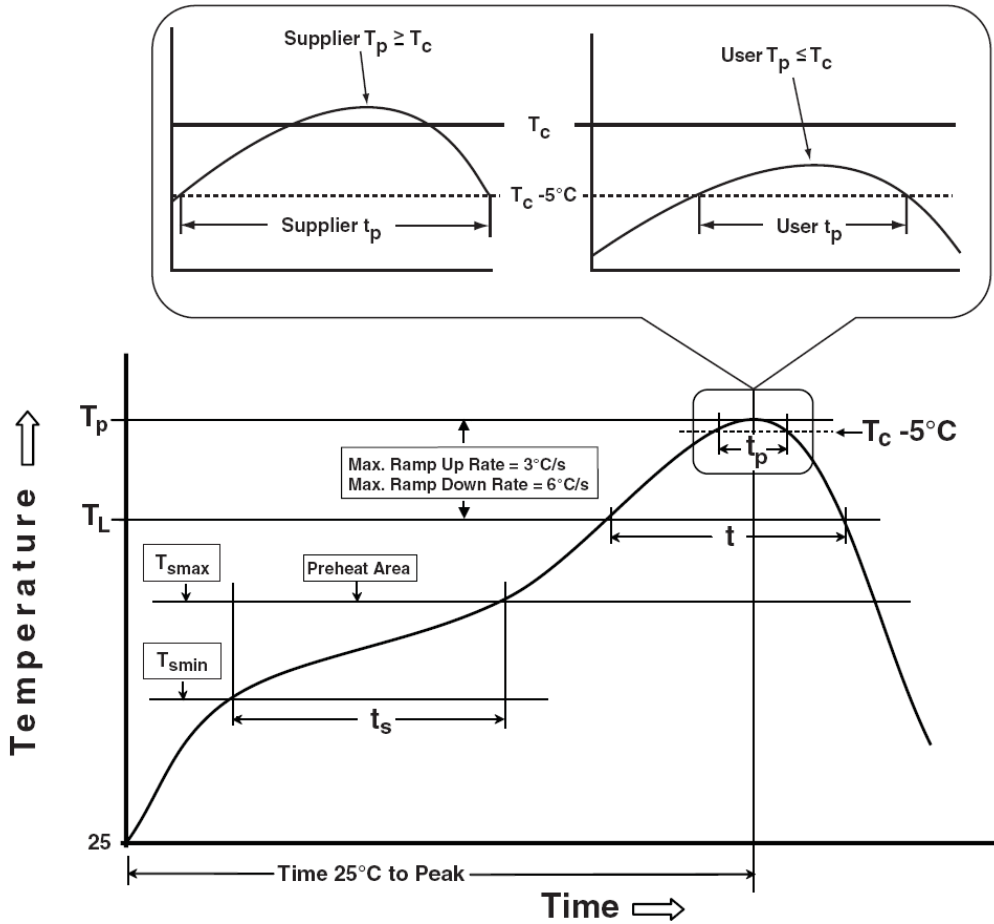
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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