

SED8830A
Dual N-Channel Enhancement Mode Field Effect Transistor

Revision:A

Features

For a single mosfet

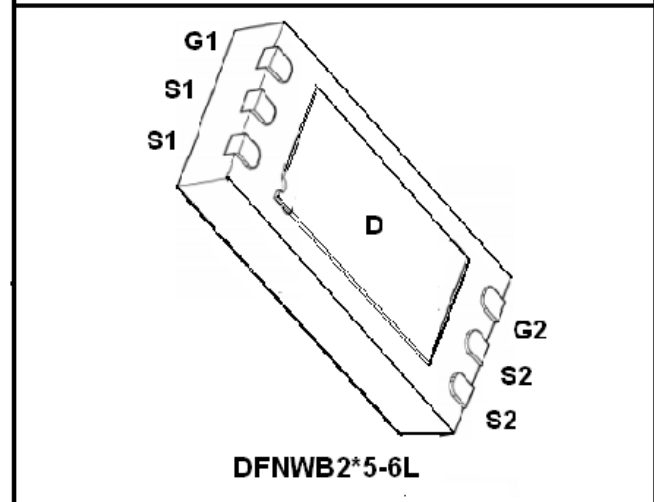
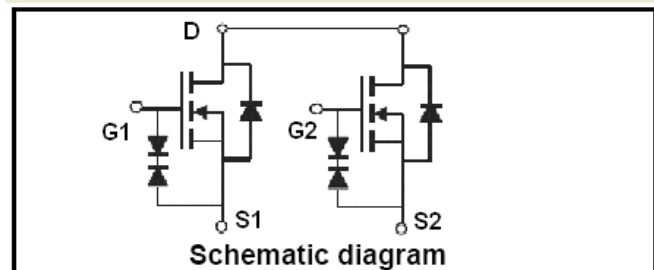
- $V_{DS} = 20\text{ V}$
- $R_{DS(ON)} = 14.5\text{m}\Omega @ V_{GS} = 4.5\text{V}$
- $R_{DS(ON)} = 22\text{m}\Omega @ V_{GS} = 2.5\text{V}$

Applications

- Battery protection
- Load switch
- Power management

Construction

- Silicon epitaxial planer



Absolute Maximum Ratings

Paramet		Symbol	Rating	Units
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 12	V
Drain Current (Note 1)	Continuous	I_D	7	A
	Pulsed	I_{DM}	28	
Drain-Source Diode Forward Current		I_S	1.7	A
Maximum Power Dissipation		P_D	1.5	W
Operating Junction Temperature Range		T_J	-55 to 150	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}		

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Electrical Characteristics (T _J =25°C unless otherwise noted)							
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit	
OFF CHARACTERISTICS							
B _{VDSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0 V	20			V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16 V, V _{GS} =0 V			1	μA	
I _{GSS}	Gate-Body leakage	V _{DS} =0 V, V _{GS} =±10 V			±10	μA	
ON CHARACTERISTICS							
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} I _D =250μA	0.5	0.8	1.3	V	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =5A	-		14.5	mΩ	
		V _{GS} =2.5V, I _D =3A	-		22		
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =5A		1		S	
DYNAMIC							
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =8V, f=1.0MHz		69		pF	
C _{oss}	Output Capacitance				18		pF
C _{rss}	Reverse Transfer Capacitance				13		pF
SWITCHING							
Q _g	Total Gate Charge	V _{GS} =4.0V V _{DS} =10V I _D =5A		11		nC	
Q _{gs}	Gate Source Charge				1.8		
Q _{gd}	Gate Drain Charge				4.9		
t _{d(on)}	Turn-On DelayTime	V _{GEN} =4.0V R _{GEN} =10Ω V _{DD} =10V I _D =1A		31		ns	
t _{d(off)}	Turn-Off DelayTime				96		
t _{d(r)}	Turn-On Rise Time				62		
t _{d(f)}	Turn-Off Fall Time				40		

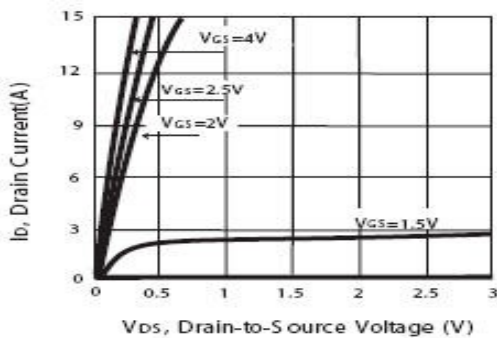


Figure 1. Output Characteristics

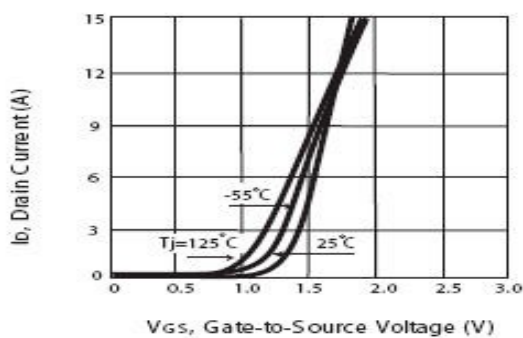


Figure 2. Transfer Characteristics

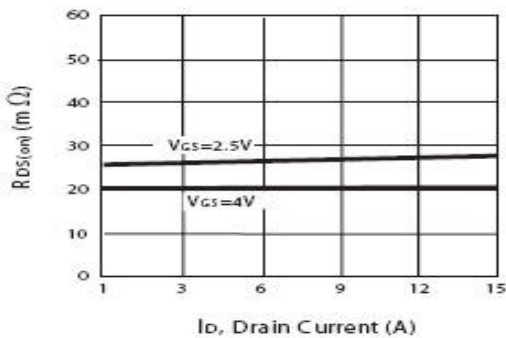


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

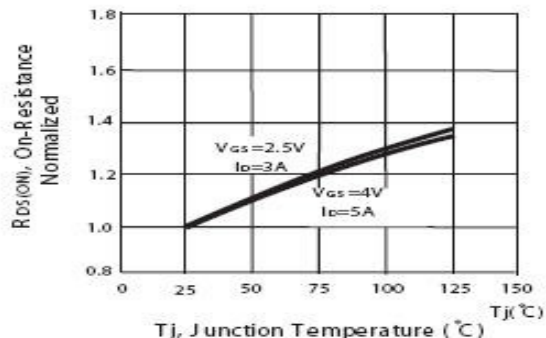


Figure 4. On-Resistance Variation with Drain Current and Temperature

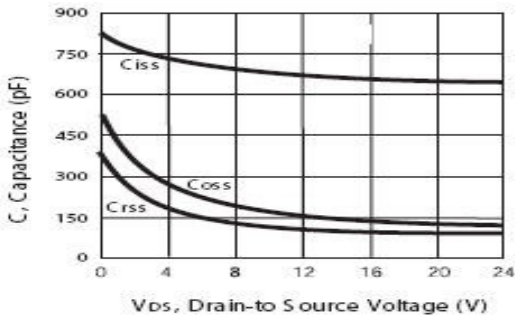


Figure 9. Capacitance

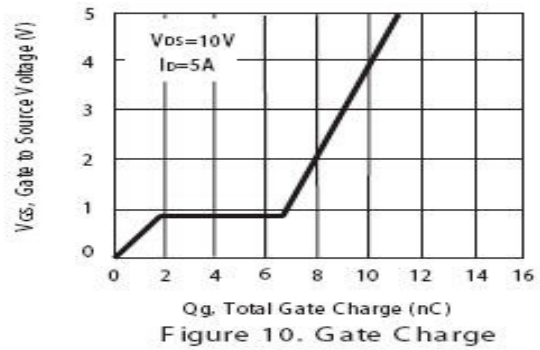


Figure 10. Gate Charge

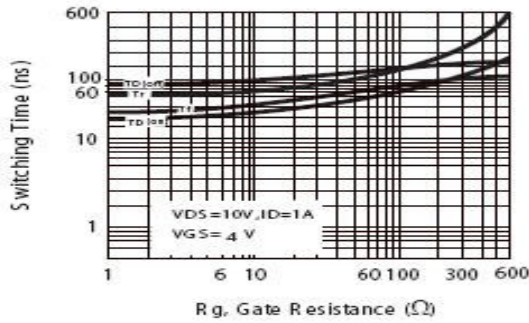


Figure 11. switching characteristics

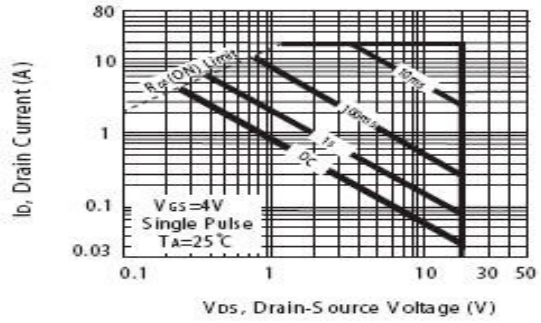


Figure 12. Maximum Safe Operating Area

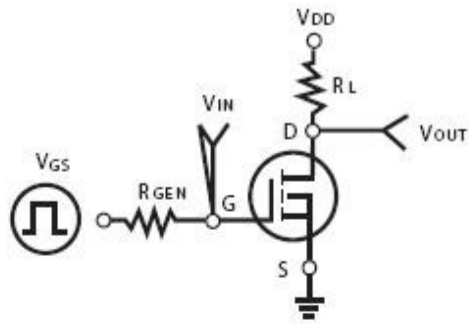


Figure 11. Switching Test Circuit

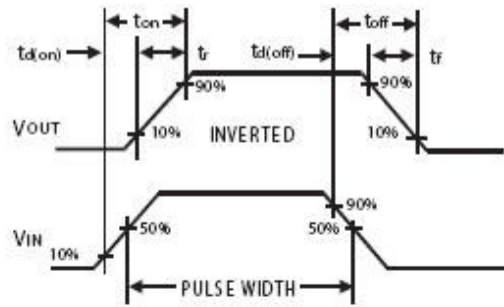
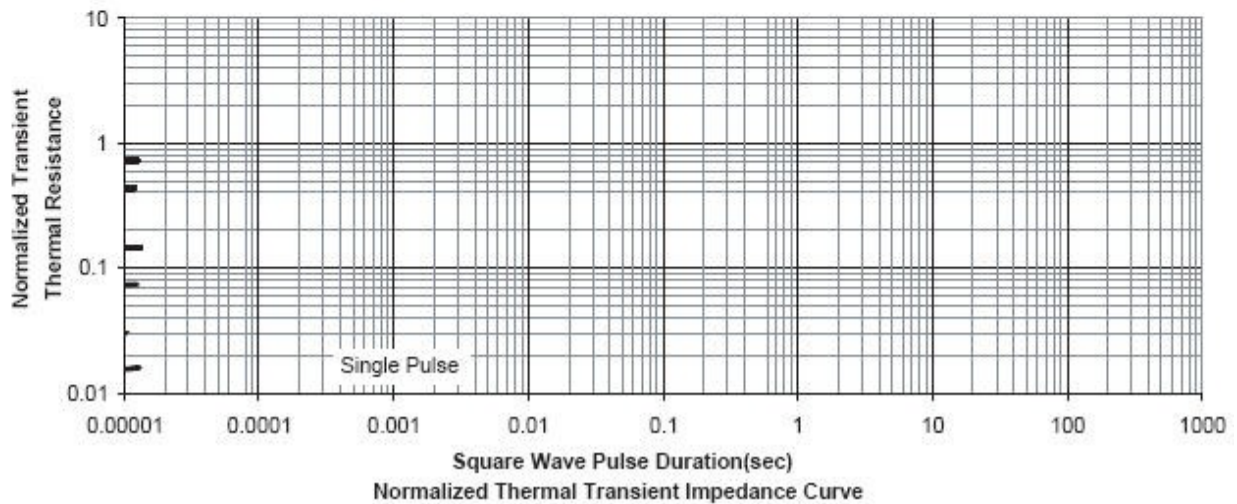


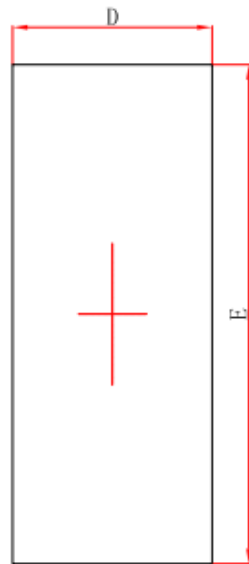
Figure 12. Switching Waveforms



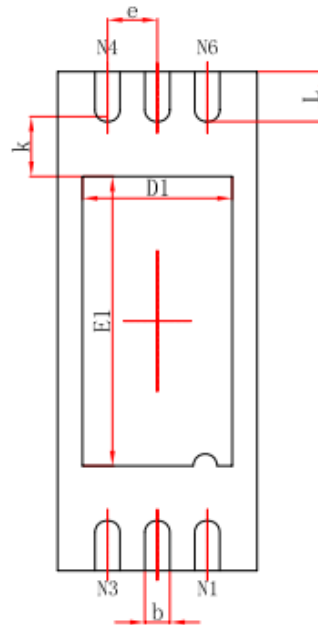
SED8830A

PACKAGE DIMENSION (Unit:mm)

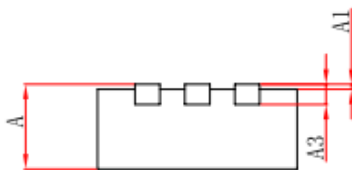
DFNWB2*5-6L



Top View



Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.924	2.076	0.076	0.082
E	4.924	5.076	0.194	0.200
D1	1.400	1.600	0.055	0.063
E1	2.800	3.000	0.110	0.118
k	0.200MIN.		0.008MIN.	
b	0.200	0.300	0.008	0.012
e	0.500TYP.		0.020TYP.	
L	0.374	0.526	0.015	0.021

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