TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7S66F, TC7S66FU

Bilateral Switch

The TC7S66 is a high Speed C²MOS Bilateral Switch fabricated with silicon gate C²MOS technology.

It consists of a high speed switch capable of controlling either digital or analog signals while maintaining the C²MOS low power dissipation.

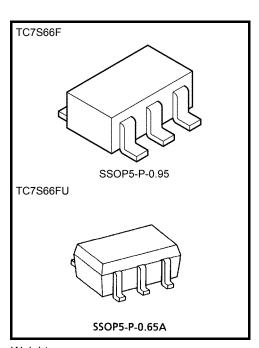
Control input (C) is provided to control the switch.

The switch turns ON while the C input is high, and the switch turns OFF while low.

Input is equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 7$ ns (typ.) @V_{CC} = 5 V
- Low power dissipation: $I_{CC} = 1 \ \mu A \ (max) \ @Ta = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Low ON resistance: $R_{ON} = 100 \Omega$ (typ.) @V_{CC} = 9 V
- Low T.H.D: THD = 0.05% (typ.) @V_{CC} = 5 V
- Pin and function compatible with TC4S66F



Weight SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

| Characteristics | Symbol | Rating | Unit | |
|------------------------------------|------------------|--------------------------|------|--|
| DC Supply voltage | V _{CC} | –0.5 to 13 | V | |
| Control input voltage | V _{IN} | -0.5 to V_{CC} + 0.5 | V | |
| Switch I/O voltage | V _{I/O} | -0.5 to V_{CC} + 0.5 | V | |
| Control diode current | ICK | ±20 | mA | |
| I/O diode current | liok | ±20 | mA | |
| Through I/O current | Ι _Τ | ±12.5 | mA | |
| DC V _{CC} /ground current | ICC | ±25 | mA | |
| Power dissipation | PD | 200 | mW | |
| Storage temperature range | T _{stg} | –65 to 150 | °C | |
| Lead temperature (10 s) | ΤL | 260 | °C | |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

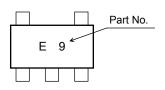
> Start of commercial production 1991-06

2014-03-01

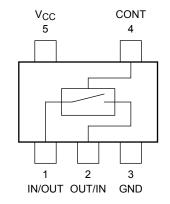
Absolute Maximum Ratings (Ta = 25°C)

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Marking



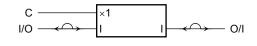
Pin Configuration (top view)



Truth Table

| Control | Switch Function |
|---------|-----------------|
| Н | ON |
| L | OFF |

Logic Diagram



Operating Ranges

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|------------------|-------------------------------------|------|
| Supply voltage | V _{CC} | 2 to 12 | V |
| Control input voltage | V _{IN} | 0 to V _{CC} | V |
| Switch I/O voltage | V _{I/O} | 0 to V _{CC} | V |
| Operating temperature range | T _{opr} | -40 to 85 | °C |
| | | 0 to 1000 (V _{CC} = 2.0 V) | |
| Input rise and fall time | tr, tf | 0 to 500 (V _{CC} = 4.5 V) | ns |
| | ւր, ւր | 0 to 400 (V _{CC} = 6.0 V) | 115 |
| | | 0 to 250 (V _{CC} = 10.0 V) | |

Electrical Characteristics

DC Electrical Characteristics

| Characteristics Symbol Test Condition | | Symbol Test Condition | | | Ta = 25°C | | Ta = -40 to 85°C | | Unit | |
|--|---------------|---|--|------|-----------|-----|---------------------|------|-------|----|
| | | $V_{CC}(V)$ | Min | Тур. | Max | Min | Max | | | |
| | | | | 2.0 | 1.5 | | _ | 1.5 | _ | |
| | High level | | | 4.5 | 3.15 | | | 3.15 | _ | |
| | riigirievei | VIHC | — | 9.0 | 6.3 | | _ | 6.3 | _ | |
| Control input | | | | 12.0 | 8.4 | _ | _ | 8.4 | — | V |
| voltage | | | | 2.0 | _ | _ | 0.5 | _ | 0.5 | v |
| | Low level | VILC | | 4.5 | _ | _ | 1.35 | _ | 1.35 | |
| | | VILC | _ | 9.0 | _ | _ | 2.7 | _ | 2.7 | |
| | | | | 12.0 | _ | _ | 3.6 | _ | 3.6 | |
| | | $R_{ON} = V_{IHC}$ $V_{I/O} = V_{CC} \text{ to GND}$ $I_{I/O} \leq 1 \text{ mA}$ $V_{IN} = V_{IHC}$ $V_{I/O} = V_{CC} \text{ or GND}$ $I_{I/O} \leq 1 \text{ mA}$ | | 4.5 | _ | 192 | 340 | _ | 400 | |
| | | | $V_{I/O} = V_{CC}$ to GND | 9.0 | | 110 | 170 | | 200 | |
| | | | 12.0 | | 90 | 160 | | 180 | | |
| ON resistance | ON resistance | | | 2.0 | | 320 | | | | Ω |
| | | | 4.5 | | 140 | 200 | | 260 | | |
| | | | | 9.0 | | 100 | 150 | | 190 | |
| | | | | 12.0 | | 90 | 140 | | 180 | |
| Input/output lea current (switch | | I _{OFF} | | 12.0 | _ | _ | ±100 | | ±1000 | nA |
| Switch input lea current (switch on, out | - | Ι _{ΙΖ} | $V_{OS} = V_{CC}$ or GND $V_{IN} = V_{IHC}$ | 12.0 | _ | _ | ±100 | _ | ±1000 | nA |
| Control input c | urrent | I _{IN} | $V_{IN} = V_{CC}$ or GND | 12.0 | _ | | ±100 | _ | ±1000 | nA |
| | | | | 6.0 | | | 1.0 | | 10.0 | |
| Quiescent devi | ce current | nt I_{CC} $V_{IN} = V_{CC}$ or GND | $V_{IN} = V_{CC}$ or GND | 9.0 | | | 4.0 | | 40.0 | μA |
| | | | | 12.0 | _ | | 8.0 | _ | 80.0 | |

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AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

| Characteristics | Symbol Test Condition | | | Ta = 25°C | | Ta = -40 to 85°C | | Unit | |
|--------------------------------|--------------------------------------|--|-------------|-----------|------|---------------------|-----|------|------|
| | , | | $V_{CC}(V)$ | Min | Тур. | Max | Min | Max | |
| | | | 2.0 | | 20 | 75 | | 100 | ns |
| Phase difference between | φI-O | — | 4.5 | | 7 | 15 | | 20 | |
| input and output | ψŀ-O | | 9.0 | | 4 | 12 | | 15 | |
| | | | 12.0 | | 4 | 11 | | 14 | |
| | | | 2.0 | | 20 | 150 | | 190 | |
| Output enable time | t _{pZL} | $R_L = 1 \ k\Omega$ | 4.5 | | 13 | 30 | | 38 | - ns |
| | t _{pZH} | | 9.0 | _ | 9 | 18 | — | 33 | |
| | | | 12.0 | _ | 8 | 18 | — | 27 | |
| | t _{pLZ} t _{pHZ} | R _L = 1 kΩ | 2.0 | _ | 40 | 170 | — | 220 | - ns |
| Output disable time | | | 4.5 | _ | 11 | 35 | — | 44 | |
| | | | 9.0 | _ | 10 | 30 | — | 38 | |
| | | | 12.0 | _ | 9 | 27 | — | 33 | |
| | | $\label{eq:RL} \begin{split} R_L &= 1 \; k\Omega \\ C_L &= 15 \; pF \\ V_{OUT} &= 1/2 \; V_{CC} \end{split}$ | 2.0 | | 30 | _ | | _ | MHz |
| Maximum control input | | | 4.5 | _ | 30 | — | — | _ | |
| frequency | | | 9.0 | | 30 | _ | _ | | |
| | | | 12.0 | | 30 | _ | _ | | |
| Control input capacitance | C _{IN} | — | | | 5 | 10 | _ | 10 | pF |
| Switch terminal capacitance | C _{I/O} | | | _ | 6 | _ | _ | _ | pF |
| Feedthrough capacitance | C _{IOS} | | | _ | 0.5 | | | | pF |
| Power dissipation capacitance | C _{PD} | | (Note) | _ | 15 | | _ | | pF |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \bullet V_{CC} \bullet f_{IN} + I_{CC}$

Analog Switch Characteristics (GND = 0 V, $Ta = 25^{\circ}C$) (Note)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|--|--------|---|---------------------|------|------|
| | _ | $f_{IN} = 1 \text{ kHz}, V_{IN} = 4 \text{ V}_{p-p} (V_{CC} = 4.5 \text{ V})$ | 4.5 | 0.05 | % |
| Total harmonic distortion (T.H.D) | | $\label{eq:RL} \begin{split} R_L &= 10 \ k\Omega, \ V_{IN} = 8 \ V_{p\text{-}p} \ (V_{CC} = 9.0 \ V) \\ C_L &= 50 \ pF \end{split}$ | 9.0 | 0.04 | |
| Maximum propagation frequency (switch on) | fMAX | Adjust f_{IN} voltage to obtain 0dBm at V_{OS} increase f_{IN} frequency until dB meter reads | 4.5 | 200 | |
| | | –3dB. $R_L = 50 \Omega$, $C_L = 10 pF$ $f_{IN} = 1 MHz$, Sine wave | 9.0 | 200 | MHz |
| Feedthrough (switch on) | _ | V_{IN} is centered at V _{CC} /2 adjust input for 0dBm R_L = 600 $\Omega,~C_L$ = 50 pF f_{IN} = 1 MHz, Sine wave | 4.5 | -60 | 10 |
| | | | 9.0 | -60 | dB |
| Crosstalk (control switch) | _ | R_L = 600 Ω, C_L = 50 pF f _{IN} = 1 MHz, Pulse (t _r = t _f = 6 ns) | 4.5 | 60 | mV |
| | | | 9.0 | 100 | IIIV |

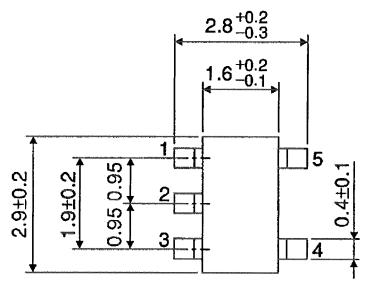
Note: These characteristics are determined by design of devices.

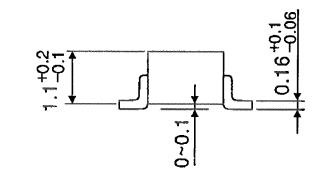
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Package Dimensions

SSOP5-P-0.95

Unit : mm

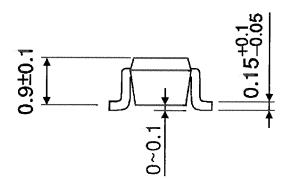




Weight: 0.016 g (typ.)

Package Dimensions

SSOP5-P-0.65A 2.1±0.1 1.25±0.1 0.65 5 1-EE 2.0 ± 0.2 1.3±0. 2-EE N o -3-EE 0.65 4



Weight: 0.006 g (typ.)

Unit : mm

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