## LM2576 Series <br> SIMPLE SWITCHER 3A Step-Down Voltage Regulator

## General Description

The LM2576 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving 3A load with excellent line and load regulation. These devices are availableinfixedoutputvoltagesof3.3V,5V,12V,andan adjustable output version.
Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensation and a fixed-frequency oscillator.
The LM2576 series offers a high-efficiency replacement for popular three-terminal linear regulators. It substantially reduces the size of the heat sink, and in some cases no heat sink is required.
A standard series of inductors optimized for use with the LM2576 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies.
Other features include a guaranteed $\pm 4 \%$ tolerance on output voltage within specified input voltages and output load conditions, and $\pm 10 \%$ on the oscillator frequency. External shutdown is included, featuring $50 \mu \mathrm{~A}$ (typical) standby current. The output switch includes cycle-by-cycle current limiting, as well as thermal shutdown for full protection under fault conditions.

## Features

■ 3.3V,5V,12V,andadjustableoutputversions

- Adjustable version output voltage range, $1.23 \mathrm{Vto} 37 \mathrm{~V} \pm 4 \%$ max over line and load conditions
- Guaranteed 3A output current
- Wideinputvoltagerange,40V
- Requires only 4 external components
- 52 kHz fixed frequency internal oscillator
- TTL shutdown capability, low power standby mode
- High efficiency
- Uses readily available standard inductors
- Thermal shutdown and current limit protection

■ P+ Product Enhancement tested

## Applications

■ Simple high-efficiency step-down (buck) regulator

- Efficient pre-regulator for linear regulators

■ On-card switching regulators

- Positive to negative converter (Buck-Boost)


FIGURE 1.

## Block Diagram



## Absolute Maximum Ratings <br> (Note 1) <br> If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Maximum Supply Voltage

| LM2576 | 45 V |
| :--- | ---: |
| $\overline{\text { ON } / \text { OFF Pin Input Voltage }}$ | $-0.3 \mathrm{~V} \leq \mathrm{V} \leq+\mathrm{V}_{\text {IN }}$ |
| Output Voltage to Ground | -1 V |
| $\quad$ (Steady State) | Internally Limited |
| Power Dissipation | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $150^{\circ} \mathrm{C}$ |


| Minimum ESD Rating |  |
| :--- | ---: |
| $\quad(\mathrm{C}=100 \mathrm{pF}, \mathrm{R}=1.5 \mathrm{k} \Omega)$ | 2 kV |
| Lead Temperature |  |
| (Soldering, 10 Seconds) | $260^{\circ} \mathrm{C}$ |

## Operating Ratings

| Temperature Range |  |
| :--- | ---: |
| LM2576 | $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+125^{\circ} \mathrm{C}$ |
| Supply Voltage |  |
| LM2576 | 40 V |

## LM2576-3.3

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2576-3.3 |  | $\begin{gathered} \text { Units } \\ \text { (Limits) } \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | $\begin{aligned} & \text { Limit } \\ & \text { (Note 2) } \end{aligned}$ |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUt }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ <br> Circuit of Figure 2 | 3.3 | $\begin{aligned} & 3.234 \\ & 3.366 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\operatorname{Min}) \\ \mathrm{V}(\operatorname{Max}) \end{gathered}$ |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2576 | $6 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.5 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}$ <br> Circuit of Figure 2 | 3.3 | $\begin{aligned} & 3.168 / 3.135 \\ & 3.432 / 3.465 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\operatorname{Min}) \\ \mathrm{V}(\operatorname{Max}) \end{gathered}$ |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 75 |  | \% |

## LM2576-5.0

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with Figure 2 boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2576-5.0 |  | $\begin{aligned} & \text { Units } \\ & \text { (Limits) } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ <br> Circuit of Figure 2 | 5.0 | $\begin{aligned} & 4.900 \\ & 5.100 \end{aligned}$ | $\begin{gathered} \hline \mathrm{V} \\ \mathrm{~V}(\operatorname{Min}) \\ \mathrm{V}(\operatorname{Max}) \end{gathered}$ |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2576 | $\begin{aligned} & 0.5 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}, \\ & 8 \mathrm{~V} \leq \mathrm{V}_{\mathrm{IN}} \leq 40 \mathrm{~V} \\ & \text { Circuit of Figure } 2 \end{aligned}$ | 5.0 | $\begin{aligned} & \text { 4.800/4.750 } \\ & 5.200 / 5.250 \end{aligned}$ | $\begin{gathered} \hline \text { V } \\ \text { V(Min) } \\ \text { V(Max) } \end{gathered}$ |
|  |  |  |  |  |  |

## LM2576-5.0

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with Figure 2 boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2576-5.0 |  | Units(Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit <br> (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 77 |  | \% |

## LM2576-12

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2576-12 |  | Units(Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=25 \mathrm{~V}, \mathrm{I}_{\mathrm{LOAD}}=0.5 \mathrm{~A}$ <br> Circuit of Figure 2 | 12 | $\begin{aligned} & 11.76 \\ & 12.24 \end{aligned}$ | V <br> V (Min) <br> V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2576 | $\begin{aligned} & 0.5 \mathrm{~A} \leq \mathrm{I}_{\mathrm{LOAD}} \leq 3 \mathrm{~A}, \\ & 15 \mathrm{~V} \leq \mathrm{V}_{\mathrm{IN}} \leq 40 \mathrm{~V} \\ & \text { Circuit of Figure } 2 \end{aligned}$ | 12 | $\begin{aligned} & 11.52 / 11.40 \\ & 12.48 / 12.60 \end{aligned}$ | $\begin{gathered} \hline \text { V } \\ \text { V(Min) } \\ \text { V(Max) } \end{gathered}$ |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=15 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}$ | 88 |  | \% |

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2576-ADJ |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Feedback Voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A} \\ & \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}, \\ & \text { Circuit of Figure } 2 \end{aligned}$ | 1.230 | $\begin{aligned} & 1.217 \\ & 1.243 \end{aligned}$ | V V(Min) V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Feedback Voltage LM2576 | $\begin{aligned} & 0.5 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 3 \mathrm{~A}, \\ & 8 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V} \\ & \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}, \text { Circuit of Figure } 2 \end{aligned}$ | 1.230 | $\begin{aligned} & 1.193 / 1.180 \\ & 1.267 / 1.280 \\ & \hline \end{aligned}$ | V V(Min) V(Max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=3 \mathrm{~A}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}$ | 77 |  | \% |

## All Output Voltage Versions Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable version, $\mathrm{V}_{\mathrm{IN}}=25 \mathrm{~V}$ for the 12 V version, and $\mathrm{V}_{\mathrm{IN}}$ $=30 \mathrm{~V}$ for the 15 V version. $\mathrm{I}_{\text {LOAD }}=500 \mathrm{~mA}$.

| Symbol | Parameter | Conditions | LM2576-XX |  | Units <br> (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| DEVICE PARAMETERS |  |  |  |  |  |
| $\mathrm{I}_{\mathrm{b}}$ | Feedback Bias Current | $\mathrm{V}_{\text {Out }}=5 \mathrm{~V}$ (Adjustable Version Only) | 50 | 100/500 | nA |
| $\mathrm{f}_{0}$ | Oscillator Frequency | (Note 11) | 52 | $\begin{aligned} & 47 / 42 \\ & 58 / 63 \end{aligned}$ | kHz <br> kHz <br> (Min) <br> kHz <br> (Max) |
| $\mathrm{V}_{\text {SAT }}$ | Saturation Voltage | lout $=3 \mathrm{~A}($ Note 4) | 1.4 | 1.8/2.0 | $\begin{gathered} \mathrm{V} \\ \mathrm{~V} \text { (Max) } \end{gathered}$ |
| DC | Max Duty Cycle (ON) | (Note 5) | 98 | 93 | $\begin{gathered} \% \\ \%(\operatorname{Min}) \end{gathered}$ |
| ICL | Current Limit | (Notes 4, 11) | 5.8 | $\begin{aligned} & 4.2 / 3.5 \\ & 6.9 / 7.5 \\ & \hline \end{aligned}$ | $\begin{gathered} A \\ A(\operatorname{Min}) \\ A(\mathrm{Max}) \\ \hline \end{gathered}$ |
| $\mathrm{I}_{\mathrm{L}}$ | Output Leakage Current | (Notes 6, 7): Output $=0 \mathrm{~V}$ $\begin{aligned} & \text { Output }=-1 \mathrm{~V} \\ & \text { Output }=-1 \mathrm{~V} \end{aligned}$ | 7.5 | $2$ <br> 30 | $\begin{gathered} \mathrm{mA}(\mathrm{Max}) \\ \mathrm{mA} \\ \mathrm{~mA}(\mathrm{Max}) \end{gathered}$ |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current | (Note 6) | 5 | 10 | mA mA(Max) |
| $\mathrm{I}_{\text {STBY }}$ | Standby Quiescent Current | $\overline{\mathrm{ON}} / \mathrm{OFF}$ Pin $=5 \mathrm{~V}$ (OFF) | 50 | 200 | $\mu \mathrm{A}$ <br> $\mu \mathrm{A}(\mathrm{Max})$ |

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable version, $\mathrm{V}_{\mathrm{IN}}=25 \mathrm{~V}$ for the 12 V version, and $\mathrm{V}_{\mathrm{IN}}$ $=30 \mathrm{~V}$ for the 15 V version. $\mathrm{I}_{\text {LOAD }}=500 \mathrm{~mA}$.

| Symbol | Parameter | Conditions | LM2576-XX |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| DEVICE PARAMETERS |  |  |  |  |  |
| $\begin{aligned} & \hline \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JC}} \\ & \theta_{\mathrm{JA}} \\ & \hline \end{aligned}$ | Thermal Resistance | T Package, Junction to Ambient (Note 8) <br> T Package, Junction to Ambient (Note 9) <br> T Package, Junction to Case <br> S Package, Junction to Ambient (Note 10) | $\begin{gathered} \hline 65 \\ 45 \\ 2 \\ 50 \end{gathered}$ |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\overline{\text { ON }}$ /OFF CONTROL Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{IH}}$ | $\overline{\mathrm{ON}} / \mathrm{OFF}$ Pin Logic Input Level | $\mathrm{V}_{\text {OUT }}=0 \mathrm{~V}$ | 1.4 | 2.2/2.4 | V (Min) |
| $\mathrm{V}_{\text {IL }}$ |  | $\mathrm{V}_{\text {OUT }}=$ Nominal Output Voltage | 1.2 | 1.0/0.8 | V(Max) |
| $\mathrm{I}_{\mathrm{IH}}$ | $\overline{\mathrm{ON}} /$ OFF Pin Input Current | $\overline{\mathrm{ON}} /$ OFF Pin $=5 \mathrm{~V}$ (OFF) | 12 | 30 | $\begin{gathered} \mu \mathrm{A} \\ \mu \mathrm{~A}(\mathrm{Max}) \end{gathered}$ |
| IIL |  | $\overline{\text { ON }} /$ OFF Pin $=0 V(O N)$ | 0 | 10 | $\mu \mathrm{A}$ $\mu \mathrm{A}$ (Max) |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.
Note 2: All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are $100 \%$ production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods.
Note 3: External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance. When the LM2576 isusedasshowninthe Figure 2 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.
Note 4: Output pin sourcing current. No diode, inductor or capacitor connected to output.
Note 5: Feedback pin removed from output and connected to OV .
Note 6: Feedback pin removed from output and connected to +12 V for the Adjustable, 3.3 V , and 5 V versions, and +25 V for the 12 V and 15 V versions, to force the output transistor OFF.
Note 7: $\mathrm{V}_{\mathrm{IN}}=40 \mathrm{~V}$ ( 60 V for high voltage version).
Note 8: Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with $1 / 2$ inch leads in a socket, or on a PC board with minimum copper area.
Note 9: Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with $1 / 4$ inch leads soldered to a PC board containing approximately 4 square inches of copper area surrounding the leads.
Note 10: If the TO-263 package is used, the thermal resistance can be reduced by increasing the PC board copper area thermally connected to the package. Using 0.5 square inches of copper area, $\theta_{\mathrm{JA}}$ is $50^{\circ} \mathrm{C} / \mathrm{W}$, with 1 square inch of copper area, $\theta_{\mathrm{JA}}$ is $37^{\circ} \mathrm{C} / \mathrm{W}$, and with 1.6 or more square inches of copper area, $\theta_{\mathrm{JA}}$ is $32^{\circ} \mathrm{C} / \mathrm{W}$. Note 11: The oscillator frequency reduces to approximately 11 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately $40 \%$ from the nominal output voltage. This self protection feature lowers the average power dissipation of the IC by lowering the minimum duty cycle from $5 \%$ down to approximately $2 \%$.

## Typical Performance Characteristics

(Circuit of Figure 2)



Typical Performance Characteristics (Circuit of Figure 2) (Continued)


01147629



Current Limit


Standby Quiescent Current


Switch Saturation
Voltage


01147634

Typical Performance Characteristics (Circuit of Figure 2) (Continued)



01147637





## LM2576 Series Buck Regulator Design Procedure (Continued)

inductor value selection guides (For Continuous Mode Operation)


FIGURE 3. LM2576(HV)-3.3


FIGURE 5. LM2576(HV)-12


DS011476-10
FIGURE 4. LM2576(HV)-5.0


FIGURE 7. LM2576(HV)-ADJ

## Test Circuit and Layout Guidelines

As in any switching regulator, layout is very important. Rapidly switching currents associated with wiring inductance generate voltage transients which can cause problems. For minimal inductance and ground loops, the length of the leads indicated by heavy lines should be kept as short as possible.

Single-point grounding (as indicated) or ground plane construction should be used for best results. When using the Adjustable version, physically locate the programming resistors near the regulator, to keep the sensitive feedback wiring short.

$\mathrm{C}_{\mathrm{IN}}-100 \mu \mathrm{~F}, 75 \mathrm{~V}$, Aluminum Electrolytic
Cout - $1000 \mu \mathrm{~F}, 25 \mathrm{~V}$, Aluminum Electrolytic
$\mathrm{D}_{1}$ - Schottky, MBR360
$L_{1}-100 \mu H$, Pulse Eng. PE-92108
$R_{1}-2 k, 0.1 \%$
$R_{2}-6.12 k, 0.1 \%$
Adjustable Output Voltage Version


$$
\begin{aligned}
V_{\text {OUT }} & =V_{\text {REF }}\left(1+\frac{R_{2}}{R_{1}}\right) \\
R_{2} & =R_{1}\left(\frac{V_{\text {OUT }}}{V_{\text {REF }}}-1\right)
\end{aligned}
$$

where $\mathrm{V}_{\text {REF }}=1.23 \mathrm{~V}$, R1 between 1 k and 5 k .

FIGURE 2.

## LM2576 Series Buck Regulator Design Procedure

## PROCEDURE (Adjustable Output Voltage Versions)

2. Inductor Selection (L1) A. Calculate the inductor Volt - microsecond constant, E•T (V• H ) , from the following formula:

$$
\begin{aligned}
& \mathrm{E} \bullet \mathrm{~T}=\left(\mathrm{V}_{\text {IN }}-\mathrm{V}_{\mathrm{OUT}}\right) \frac{\mathrm{V}_{\mathrm{OUT}}}{\mathrm{~V}_{\text {IN }}} \bullet \frac{1000}{\mathrm{~F}(\text { in } k H z)}(\mathrm{V} \bullet \mu \mathrm{~S}) \\
& \text { B. Use the } \mathrm{E} \cdot \mathrm{~T} \text { value from the previous formula and }
\end{aligned}
$$ match it with the $E \cdot T$ number on the vertical axis of the Inductor Value Selection Guide shown in Figure 7. C. On the horizontal axis, select the maximum load current. D. Identify the inductance region intersected by the E-T value and the maximum load current value, and note the inductor code for that region. E. Identify the inductor value from the inductor code, and select an appropriate inductor from the table shown in Figure 9. Part numbers are listed for three inductor manufacturers. The inductor chosen must be rated for operation at the LM2576 switching frequency ( 52 kHz ) and for a current rating of $1.15 \times \mathrm{I}_{\text {LOAD }}$. For additional inductor information, see the inductor section in the application hints section of this data sheet.

3. Output Capacitor Selection ( $\mathbf{C}_{\text {out }}$ ) A. The value of the output capacitor together with the inductor defines the dominate pole-pair of the switching regulator loop. For stable operation, the capacitor must satisfy the following requirement:

$$
\mathrm{C}_{\text {OUT }} \geq 13,300 \frac{\mathrm{~V}_{\text {IN }}(\text { Max })}{\mathrm{V}_{\text {OUT }} \cdot \mathrm{L}(\mu \mathrm{H})}(\mu \mathrm{F})
$$

The above formula yields capacitor values between $10 \mu \mathrm{~F}$ and $2200 \mu \mathrm{~F}$ that will satisfy the loop requirements for stable operation. But to achieve an acceptable output ripple voltage, (approximately $1 \%$ of the output voltage) and transient response, the output capacitor may need to be several times larger than the above formula yields. B. The capacitor's voltage rating should be at last 1.5 times greater than the output voltage. For a 10 V regulator, a rating of at least 15 V or more is recommended. Higher voltage electrolytic capacitors generally have lower ESR numbers, and for this reason it may be necessary to select a capacitor rate for a higher voltage than would normally be needed.
4. Catch Diode Selection (D1) A. The catch-diode current rating must be at least 1.2 times greater than the maximum load current. Also, if the power supply design must withstand a continuous output short, the diode should have a current rating equal to the maximum current limit of the LM2576. The most stressful condition for this diode is an overload or shorted output. See diode selection guide in Figure 8. B. The reverse voltage rating of the diode should be at least 1.25 times the maximum input voltage.
5. Input Capacitor ( $\mathbf{C}_{\text {IN }}$ ) An aluminum or tantalum electrolytic bypass capacitor located close to the regulator is needed for stable operation.

To further simplify the buck regulator design procedure, National Semiconductor is making available computer design software to be used with the SIMPLE SWITCHER line of

EXAMPLE (Adjustable Output Voltage Versions)
2. Inductor Selection (L1) A. Calculate E•T (V• Ls )

$$
E \cdot T=(25-10) \cdot \frac{10}{25} \cdot \frac{1000}{52}=115 \mathrm{~V} \cdot \mu \mathrm{~S}
$$

B. $E \cdot T=115 \mathrm{~V} \cdot \mu \mathrm{C}$ C. I LOAD (Max) $=3 \mathrm{~A} D$. Inductance Region $=\mathrm{H} 150 \mathrm{E}$. Inductor Value $=150 \mu \mathrm{H}$ Choose from AIE part \#415-0936 Pulse Engineering part \#PE-531115, or Renco part \#RL2445.
3. Output Capacitor Selection ( $\mathrm{C}_{\text {out }}$ )
$\mathrm{C}_{\text {OUT }}>13,300 \frac{25}{10 \cdot 150}=22.2 \mu \mathrm{~F}$
However, for acceptable output ripple voltage select $\mathrm{C}_{\text {Out }}$ $\geq 680 \mu \mathrm{~F} \mathrm{C}_{\text {OUT }}=680 \mu \mathrm{~F}$ electrolytic capacitor
4. Catch Diode Selection (D1) A. For this example, a 3.3A current rating is adequate. B. Use a 30V 31DQ03 Schottky diode, or any of the suggested fast-recovery diodes in Figure 8.
5. Input Capacitor ( $\mathbf{C}_{\mathrm{IN}}$ ) A $100 \mu \mathrm{~F}$ aluminum electrolytic capacitor located near the input and ground pins provides sufficient bypassing.
switching regulators. Switchers Made Simple (Version 3.3) is available on a ( $3^{1 / 2} 2^{\prime \prime}$ ) diskette for IBM compatible computers from a National Semiconductor sales office in your area.

## Additional Applications

The switch currents in this buck-boost configuration are higher than in the standard buck-mode design, thus lowering the available output current. Also, the start-up input current of the buck-boost converter is higher than the standard buck-mode regulator, and this may overload an input power source with a current limit less than 5A. Using a delayed turn-on or an undervoltage lockout circuit (described in the next section) would allow the input voltage to rise to a high enough level before the switcher would be allowed to turn on.
Because of the structural differences between the buck and the buck-boost regulator topologies, the buck regulator design procedure section can not be used to to select the inductor or the output capacitor. The recommended range of inductor values for the buck-boost design is between $68 \mu \mathrm{H}$ and $220 \mu \mathrm{H}$, and the output capacitor values must be larger than what is normally required for buck designs. Low input voltages or high output currents require a large value output capacitor (in the thousands of micro Farads).
The peak inductor current, which is the same as the peak switch current, can be calculated from the following formula:

$$
I_{p} \approx \frac{I_{\text {LOAD }}\left(V_{I N}+\left|V_{O}\right|\right)}{V_{I N}}+\frac{V_{I N}\left|V_{O}\right|}{V_{I N}+\left|V_{O}\right|} \times \frac{1}{2 L_{1} f_{\text {OSc }}}
$$

Where $f_{\text {osc }}=52 \mathrm{kHz}$. Under normal continuous inductor current operating conditions, the minimum $\mathrm{V}_{\text {IN }}$ represents the worst case. Select an inductor that is rated for the peak current anticipated.


FIGURE 10. Inverting Buck-Boost Develops -12V
Also, the maximum voltage appearing across the regulator is the absolute sum of the input and output voltage. For a -12 V output, the maximum input voltage for the LM2576 is +28 V , or +48 V for the LM2576HV.
The Switchers Made Simple (version 3.0) design software can be used to determine the feasibility of regulator designs using different topologies, different input-output parameters, different components, etc.

## NEGATIVE BOOST REGULATOR

Another variation on the buck-boost topology is the negative boost configuration. The circuit in Figure 11 accepts an input voltage ranging from -5 V to -12 V and provides a regulated -12 V output. Input voltages greater than -12 V will cause the output to rise above -12 V , but will not damage the regulator.


Typical Load Current
400 mA for $\mathrm{V}_{\mathrm{IN}}=-5.2 \mathrm{~V}$
750 mA for $\mathrm{V}_{\mathrm{IN}}=-7 \mathrm{~V}$
Note: Heat sink may be required.

## FIGURE 11. Negative Boost

Because of the boosting function of this type of regulator, the switch current is relatively high, especially at low input voltages. Output load current limitations are a result of the maximum current rating of the switch. Also, boost regulators can not provide current limiting load protection in the event of a shorted load, so some other means (such as a fuse) may be necessary.

## UNDERVOLTAGE LOCKOUT

In some applications it is desirable to keep the regulator off until the input voltage reaches a certain threshold. An undervoltage lockout circuit which accomplishes this task is shown in Figure 12, while Figure 13 shows the same circuit applied to a buck-boost configuration. These circuits keep the regulator off until the input voltage reaches a predetermined level.
$\mathrm{V}_{\mathrm{TH}} \approx \mathrm{V}_{\mathrm{Z} 1}+2 \mathrm{~V}_{\mathrm{BE}}(\mathrm{Q} 1)$


Note: Complete circuit not shown.

FIGURE 12. Undervoltage Lockout for Buck Circuit

## Additional Applications



Note: Complete circuit not shown (see Figure 10).

FIGURE 13. Undervoltage Lockout for Buck-Boost Circuit

## DELAYED STARTUP

The $\overline{\mathrm{ON}}$ /OFF pin can be used to provide a delayed startup feature as shown in Figure 14. With an input voltage of 20 V and for the part values shown, the circuit provides approximately 10 ms of delay time before the circuit begins switch-
ing. Increasing the RC time constant can provide longer delay times. But excessively large RC time constants can cause problems with input voltages that are high in 60 Hz or 120 Hz ripple, by coupling the ripple into the $\overline{\mathrm{ON}}$ /OFF pin.

## ADJUSTABLE OUTPUT, LOW-RIPPLE POWER SUPPLY

A 3 A power supply that features an adjustable output voltage is shown in Figure 15. An additional L-C filter that reduces the output ripple by a factor of 10 or more is included in this circuit.


Note: Complete circuit not shown

FIGURE 14. Delayed Startup


FIGURE15.1.2Vto37V Adjustable3APowerSupplywithLowOutputRipple

## Definition of Terms

## buck regulator

A switching regulator topology in which a higher voltage is converted to a lower voltage. Also known as a step-down switching regulator.

## BUCK-BOOST REGULATOR

A switching regulator topology in which a positive voltage is converted to a negative voltage without a transformer.

## DUTY CYCLE (D)

Ratio of the output switch's on-time to the oscillator period.

$$
\begin{aligned}
\text { for buck regulator } & D & =\frac{t_{O N}}{T}=\frac{V_{O U T}}{V_{I N}} \\
\text { for buck-boost regulator } & D & =\frac{t_{O N}}{T}=\frac{\left|V_{O}\right|}{\left|V_{O}\right|+V_{I N}}
\end{aligned}
$$

## CATCH DIODE OR CURRENT STEERING DIODE

The diode which provides a return path for the load current when the LM2576 switch is OFF.

## EFFICIENCY ( $\eta$ )

The proportion of input power actually delivered to the load.

$$
\eta=\frac{\mathrm{P}_{\mathrm{OUT}}}{\mathrm{P}_{\mathrm{IN}}}=\frac{\mathrm{P}_{\mathrm{OUT}}}{\mathrm{P}_{\mathrm{OUT}}+\mathrm{P}_{\mathrm{LOSS}}}
$$

## Definition of Terms

## CAPACITOR EQUIVALENT SERIES RESISTANCE (ESR)

The purely resistive component of a real capacitor's impedance (see Figure 16). It causes power loss resulting in capacitor heating, which directly affects the capacitor's operating lifetime. When used as a switching regulator output filter, higher ESR values result in higher output ripple voltages.


FIGURE 16. Simple Model of a Real Capacitor
Most standard aluminum electrolytic capacitors in the $100 \mu \mathrm{~F}-1000 \mu \mathrm{~F}$ range have $0.5 \Omega$ to $0.1 \Omega \mathrm{ESR}$. Highergrade capacitors ("low-ESR", "high-frequency", or "lowinductance") in the $100 \mu \mathrm{~F}-1000 \mu \mathrm{~F}$ range generally have ESR of less than $0.15 \Omega$.

## EQUIVALENT SERIES INDUCTANCE (ESL)

The pure inductance component of a capacitor (see Figure 16). The amount of inductance is determined to a large extent on the capacitor's construction. In a buck regulator, this unwanted inductance causes voltage spikes to appear on the output.

## OUTPUT RIPPLE VOLTAGE

The AC component of the switching regulator's output voltage. It is usually dominated by the output capacitor's ESR multiplied by the inductor's ripple current ( $\Delta \mathrm{I}_{\mathrm{IND}}$ ). The peak-to-peak value of this sawtooth ripple current can be determined by reading the Inductor Ripple Current section of the Application hints.

## CAPACITOR RIPPLE CURRENT

RMS value of the maximum allowable alternating current at which a capacitor can be operated continuously at a specified temperature.

## STANDBY QUIESCENT CURRENT ( $\mathbf{I}_{\text {Stby }}$ )

Supply current required by the LM2576 when in the standby mode ( $\overline{\mathrm{ON}} / \mathrm{OFF}$ pin is driven to TTL-high voltage, thus turning the output switch OFF).

## INDUCTOR RIPPLE CURRENT ( $\Delta \mathrm{I}_{\text {IND }}$ )

The peak-to-peak value of the inductor current waveform, typically a sawtooth waveform when the regulator is operating in the continuous mode (vs. discontinuous mode).

## OPERATING VOLT MICROSECOND CONSTANT ( $\mathrm{E} \cdot \mathrm{T}_{\mathrm{op}}$ )

The product (in Volt• $\mu \mathrm{s}$ ) of the voltage applied to the inductor and the time the voltage is applied. This $\mathrm{E} \bullet \mathrm{T}_{\mathrm{op}}$ constant is a measure of the energy handling capability of an inductor and is dependent upon the type of core, the core area, the number of turns, and the duty cycle.

## Connection Diagrams

Straight Leads
5-Lead TO-220 (T)
Top View


NS Package Number T05A

TO-263 (S)
5-Lead Surface-Mount Package Top View


LM2576S-XX
01147625
NS Package Number TS5B
LM2576SX-XX
NS Package Number TS5B, Tape and Reel

Bent, Staggered Leads
5-Lead TO-220 (T)
Top View


Side View


01147622

## LM2576T-XX Flow LB03

NS Package Number T05D

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