

SGM8740 45ns, Low-Power, 3V/5V, Rail-to-Rail Input Single-Supply Comparator

GENERAL DESCRIPTION

The SGM8740 is a single high-speed comparator optimized for systems powered from a 3V or 5V supply. The device features high-speed response, low-power consumption, and rail-to-rail input range. Propagation delay is 45ns, while supply current is only 155µA.

The input common mode range of the SGM8740 extends beyond both power supply rails. The output pulls to within 0.1V of either supply rail without external pull-up circuitry, making the device ideal for interface with both CMOS and TTL logics. All input and output pins can tolerate a continuous short-circuit fault condition to either rail. Internal hysteresis ensures clean output switching, even with slow-moving input signals.

The SGM8740 is available in Green SOT-23-5 and SC70-5 packages. It is rated over the -40 $^{\circ}$ C to +85 $^{\circ}$ C temperature range.

FEATURES

- Fast, 45ns Propagation Delay (10mV Overdrive)
- Low Power Consumption:
 155µA (TYP) at V_s = 3V
- Wide Supply Voltage Range: 2.7V to 5.5V
- Optimized for 3V and 5V Applications
- Rail-to-Rail Input Voltage Range
- Low Offset Voltage: 0.9mV (TYP)
- Internal Hysteresis for Clean Switching
- Output Swing to within 200mV from Rails with 4mA Output Current
- CMOS/TTL-Compatible Output
- -40°C to +85°C Operating Temperature Range
- Available in Green SOT-23-5 and SC70-5 Packages

APPLICATIONS

Line Receivers

Battery-Powered Systems

Threshold Detectors/Discriminators

3V/5V Systems

Zero-Crossing Detectors

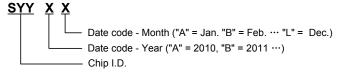
Sampling Circuits

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION	
00110740	SOT-23-5	-40℃ to +85℃	SGM8740YN5G/TR	SL5XX	Tape and Reel, 3000	
SGM8740	SC70-5	-40℃ to +85℃	SGM8740YC5G/TR	SK7XX	Tape and Reel, 3000	

NOTE: XX = Date Code.

MARKING INFORMATION



For example: SL5AA (2010, January)

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, +V _S to -V _S	6V
V _{IN} Differential	±2.5V
Voltage at Input/Output pins(-V _S) - 0.3V	to $(+V_S) + 0.3V$
Operating Temperature Range	40°C to +85°C
Junction Temperature	150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	260°C
ESD Susceptibility	
HBM	6000V
MM	400V

NOTE:

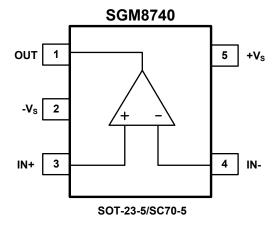
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATIONS (TOP VIEW)



ELECTRICAL CHARACTERISTICS

(V_S = 5V, V_{CM} = 0V, C_L = 15pF, T_A = +25°C, unless otherwise noted.)

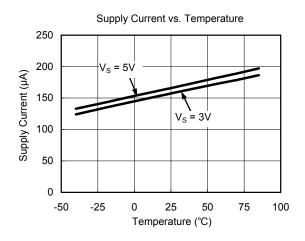
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS		
Operating Supply Voltage (1)	Vs		2.7		5.5	V		
Input Common Mode Voltage Range (2)	V _{CM}		-0.1		V _S + 0.1	V		
(3)		V _S = 5V, V _{CM} = 0V	0.9		5	>/		
Input Offset Voltage (3)	Vos	-40°C ≤ T _A ≤ +85°C			5.8	mV		
Input Hysteresis (4)	V _{HYST}	V _S = 5V, V _{CM} = 0V		2.8		mV		
	1	V_S = 5V, Out to $V_S/2$	21	33				
Output Short Circuit Current	SOURCE	-40°C ≤ T _A ≤ +85°C	17					
Output Short-Circuit Current	1	V_S = 5V, Out to $V_S/2$		-32	-20	mA		
	I _{SINK}	-40°C ≤ T _A ≤ +85°C			-15			
Common Mada Dairatica Datia (5)	CMDD	$V_S = 5V$, $V_{CM} = 0V$ to $5V$	60	78		- dB		
Common Mode Rejection Ratio (5)	CMRR	-40°C ≤ T _A ≤ +85°C	54					
Bernard Belantin Batta	DODD	$V_{CM} = 0V, V_S = 2.7V \text{ to } 5.5V$	59	77		10		
Power Supply Rejection Ratio	PSRR	-40°C ≤ T _A ≤ +85°C	55			dB		
	V _{OH}	V _S = 5V, I _O = 4mA		198	450	mV		
O to 1)/ellers O to form Bell		-40°C ≤ T _A ≤ +85°C			480			
Output Voltage Swing from Rail	V _{OL}	$V_S = 5V$, $I_O = -4mA$		180	231			
		-40°C ≤ T _A ≤ +85°C			258			
	Is	V _S = 3V, I _O = 0		155	215			
0		-40°C ≤ T _A ≤ +85°C			250	μΑ		
Supply Current		V _S = 5V, I _O = 0		164	230			
		-40°C ≤ T _A ≤ +85°C			270			
December 2015 (1915) (1915)		V _S = 3V, Overdrive = 10mV	45					
Propagation Delay (High to Low)		V _S = 3V, Overdrive = 100mV		20		ns		
Decreasing Delay (Leaves High)		V _S = 3V, Overdrive = 10mV		35				
Propagation Delay (Low to High)		V _S = 3V, Overdrive = 100mV		25		ns		
Disc Time	t _{RISE}	V _S = 3V, Overdrive = 10mV	9					
Rise Time		V _S = 3V, Overdrive = 100mV		8		ns		
Fall Time	1	V _S = 3V, Overdrive = 10mV 8						
Fall Time	t _{FALL}	V _S = 3V, Overdrive = 100mV 5				ns		

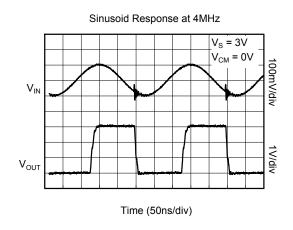
NOTES:

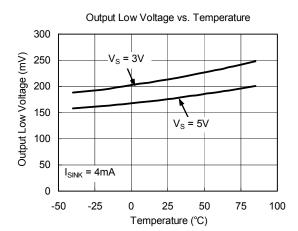
- 1. Inferred from PSRR test.
- 2. Inferred from PD test. Note also that either or both inputs can be driven to the absolute maximum limit (0.1V beyond either supply rail) without damage or false output inversion.
- 3. V_{OS} is defined as the center of the input-referred hysteresis zone. See Figure 1.
- 4. The input-referred trip points are the extremities of the differential input voltage required to make the comparator output change state. The difference between the upper and lower trip points is equal to the width of the input-referred hysteresis zone. See Figure 1.
- 5. Specified over the full input common mode voltage range (V_{CM}).

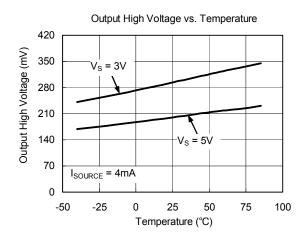


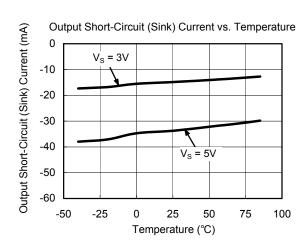
TYPICAL PERFORMANCE CHARACTERISTICS

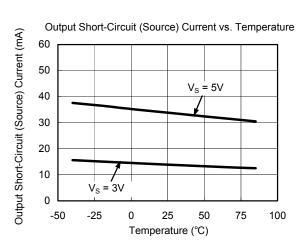




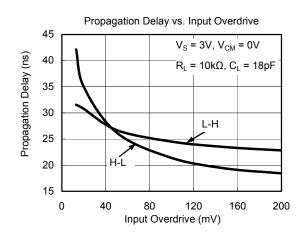


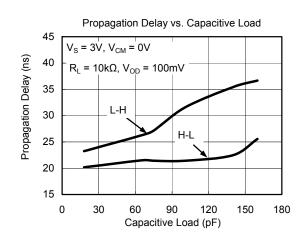


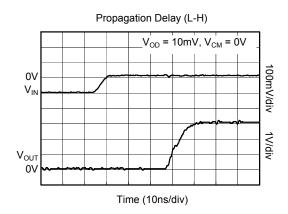


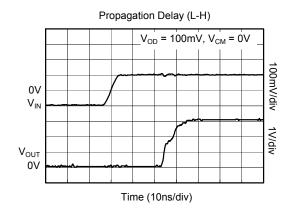


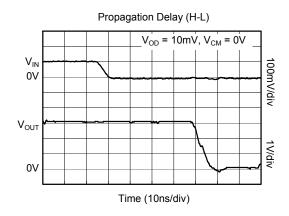
TYPICAL PERFORMANCE CHARACTERISTICS

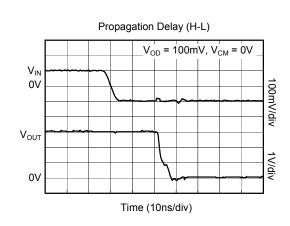




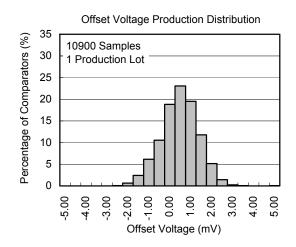








TYPICAL PERFORMANCE CHARACTERISTICS



DETAILED DESCRIPTION

The SGM8740 is a single-supply comparator that features internal hysteresis, high speed, and low power. With 4mA output current, its output is pulled to within 200mV of either supply rail without external pull-up or pull-down circuitry. Rail-to-rail input voltage range and low-voltage single-supply operation make the device ideal for portable equipments. The SGM8740 interfaces directly to CMOS and TTL logics.

Most high-speed comparators oscillate in the linear region because of noise or undesired parasitic feedback. This tends to occur when the voltage on one input is at or equal to the voltage on the other input. To counter the parasitic effects and noise, the SGM8740 has an internal hysteresis of 2.8mV.

The hysteresis in a comparator creates two trip points: one for the rising input voltage and one for the falling input voltage (Figure 1). The difference between the trip points is the hysteresis. The average of the trip points is the offset voltage. When the comparator's input voltages are equal, the hysteresis effectively causes one comparator input voltage to move quickly past the other, thus taking the input out of the region where oscillation occurs. Standard comparators require hysteresis to be added with external resistors. The SGM8740's fixed internal hysteresis eliminates these resistors. To increase hysteresis and noise margin even more, add positive feedback with two resistors as a voltage divider from the output to the non-inverting input.

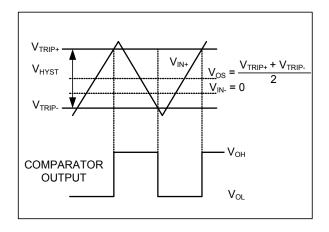


Figure 1. Input and Output Waveform, Non-inverting Input Varied

Figure 1 illustrates the case where IN- is fixed and IN+ is varied. If the inputs were reversed, the figure would look the same, except the output would be inverted.

Output Stage Circuitry

The SGM8740 contains a current-driven output stage as shown in Figure 2. During an output transition, I_{SOURCE} or I_{SINK} is pushed or pulled to the output pin. The output source or sink current is high during the transition, creating a rapid slew rate. Once the output voltage reaches V_{OH} or V_{OL} , the source or sink current decreases to a small value, capable of maintaining the V_{OH} or V_{OL} static condition. This significant decrease in current conserves power after an output transition has occurred.

One consequence of a current-driven output stage is a linear dependence between the slew rate and the load capacitance. A heavy capacitive load will slow down a voltage output transition. This can be useful in noise-sensitive applications where fast edges may cause interference.

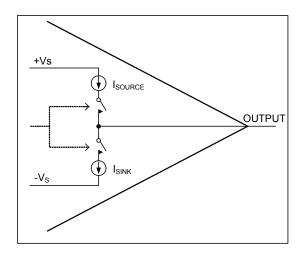


Figure 2. Output Stage Circuitry

APPLICATION INFORMATION

Circuit Layout and Bypassing

The high gain-bandwidth product of the SGM8740 requires design precautions to realize the full high-speed capabilities of the comparator. The recommended precautions are:

- 1) Use a PCB with a good, unbroken, low-inductance ground plane.
- 2) Place a decoupling capacitor (a $0.1\mu F$ ceramic capacitor is a good choice) as close to $+V_S$ as possible.
- 3) Pay close attention to the decoupling capacitor's bandwidth, keeping leads short.
- 4) On the inputs and output, keep lead lengths short to avoid unwanted parasitic feedback around the comparator.
- 5) Solder the device directly to the PCB instead of using a socket.

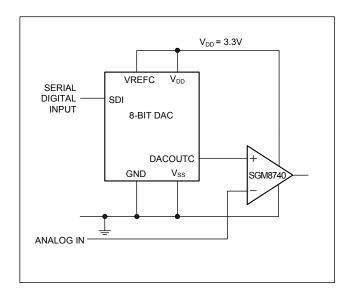


Figure 3. 3.3V Digitally Controlled Threshold Detector

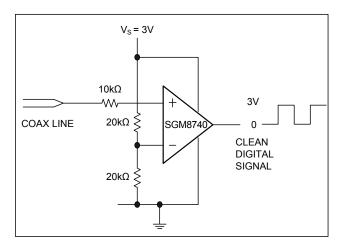
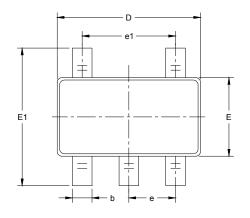
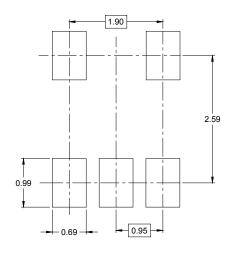


Figure 4. Line Receiver Application

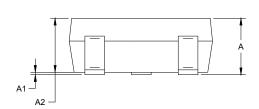
PACKAGE OUTLINE DIMENSIONS

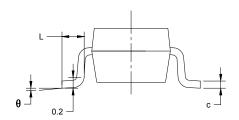
SOT-23-5





RECOMMENDED LAND PATTERN (Unit: mm)

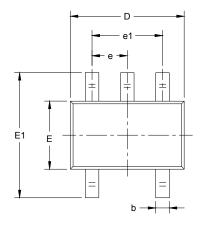


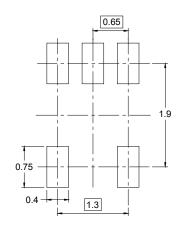


Symbol	_	nsions imeters	Dimensions In Inches		
	MIN MAX		MIN	MAX	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
Е	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	BSC	0.037 BSC		
e1	1.900 BSC		0.075	BSC	
L	0.300	0.600	0.012	0.024	
θ	0° 8°		0°	8°	

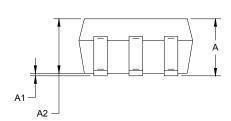
PACKAGE OUTLINE DIMENSIONS

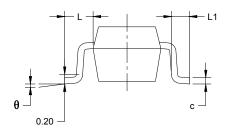
SC70-5





RECOMMENDED LAND PATTERN (Unit: mm)

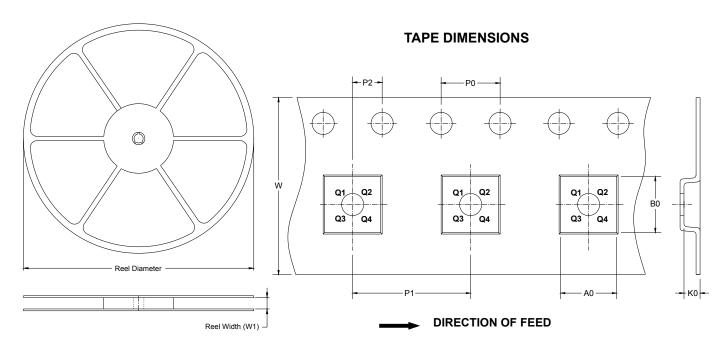




Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.080	0.150	0.003	0.006	
D	2.000	2.200	0.079	0.087	
Е	1.150	1.350	0.045	0.053	
E1	2.150	2.450	0.085 0.096		
е	0.65 TYP		0.026 TYP		
e1	1.300 BSC		0.051 BSC		
L	0.525 REF		0.021	REF	
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

TAPE AND REEL INFORMATION

REEL DIMENSIONS

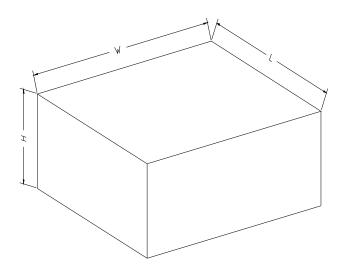


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.2	3.2	1.4	4.0	4.0	2.0	8.0	Q3
SC70-5	7"	9.5	2.25	2.55	1.20	4.0	4.0	2.0	8.0	Q3

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

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MAX984CPE MAX9062EBSTG45 MAX9041AEUTT MAX9022ASAT RT2902YDT M38510/11201B2A NTE911 5962-8751601DA
5962-8751601CA MAX961EUA+T MAX9065EBS+TG45 NCV2202SN2T1G MAX919ESA+T LT6700HS6-2#TRMPBF MAX19005CCS+
LM339EDR2G LT6700HS6-2#TRM NTE919 NTE922 TS883IQ2T LT6700HVCS6-3#TRMPBF LT6700HVHS6-3#TRMPBF
MAX978EEE+T MAX975ESA+T MAX9602EUG+T MAX997EUA+T MIC841NYC5-T5 LM393WDT MCP6564T-E/STVAO
MAX983ESA+T NCX2200GMAZ LTC1540CMS8#PBF MC10E1651FNG