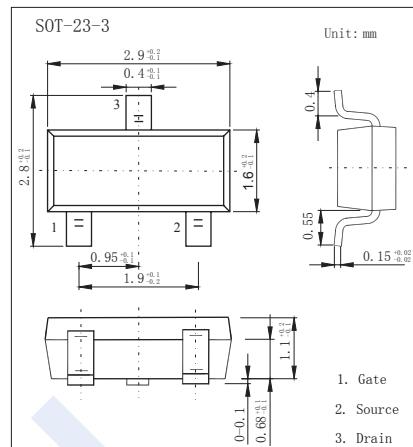
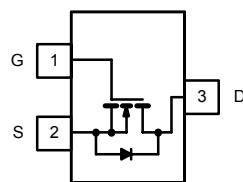


## N-Channel Enhancement MOSFET

SI2302 (KI2302)

## ■ Features

- $V_{DS}=20V$
- $R_{DS(on)}= 85m\Omega @ V_{GS}=4.5V, I_D=3.6A$
- $R_{DS(on)}= 115m\Omega @ V_{GS}=2.5V, I_D=3.1A$

■ Absolute Maximum Ratings  $T_a = 25^\circ C$ 

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current $T_J=150^\circ C$ *1	$I_D$	2.8	A
$T_a=70^\circ C$		2.2	
Pulsed Drain Current	$I_{DM}$	10	
Power Dissipation	$P_D$	1.25	W
$T_a=70^\circ C$		0.8	
Thermal Resistance.Junction- to-Ambient *1 *2	$R_{thJA}$	100	$^\circ C/W$
		166	
Junction Temperature	$T_J$	150	
Storage Temperature Range	$T_{stg}$	-55 to 150	$^\circ C$

Notes:

\*1.Surface Mounted on FR4 Board,  $t \leq 5$  sec.

\*2.Surface Mounted on FR4 Board.

## N-Channel Enhancement MOSFET

## SI2302 (KI2302)

■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	20			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$		1		$\mu\text{A}$
		$V_{DS}=20\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			10	
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	0.6		1.0	V
Static Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=4.5\text{V}, I_D=3.6\text{A}$		45	85	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=3.1\text{A}$		70	115	
Forward Transconductance *	$g_{fs}$	$V_{DS}=5\text{V}, I_D=3.6\text{A}$		8		S
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}, V_{DS}=10\text{V}, f=1\text{MHz}$		300		pF
Output Capacitance	$C_{oss}$			120		
Reverse Transfer Capacitance	$C_{rss}$			80		
Total Gate Charge	$Q_g$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=3.6\text{A}$		4	10	nC
Gate-Source Charge	$Q_{gs}$			0.65		
Gate-Drain Charge	$Q_{gd}$			1.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=4.5\text{V}, V_{DS}=10\text{V}, R_L=5.5 \Omega, R_{GEN}=6 \Omega$ $I_D=3.6\text{A}$		7	15	ns
Turn-On Rise Time	$t_r$			55	80	
Turn-Off Delay Time	$t_{d(off)}$			16	60	
Turn-Off Fall Time	$t_f$			10	25	
Continuous Source Current (Diode Conduction)	$I_s$			1.6		A
Diode Forward Voltage	$V_{SD}$	$I_s=1.6 \text{ A}, V_{GS}=0\text{V}$		0.76	1.2	V

\* Pulse test: PW  $\leq 300\text{us}$  duty cycle  $\leq 2\%$ 

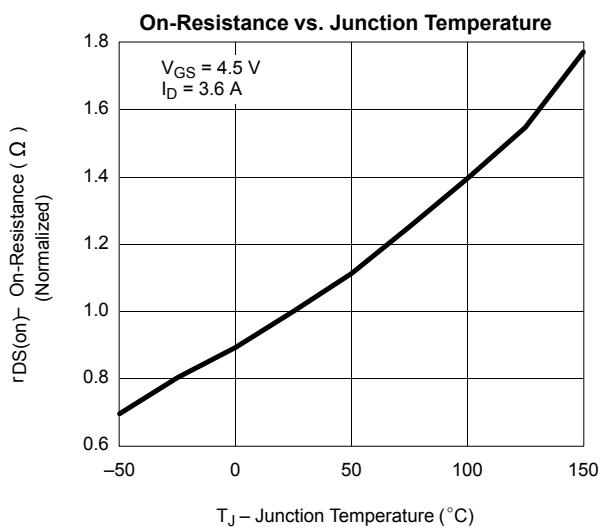
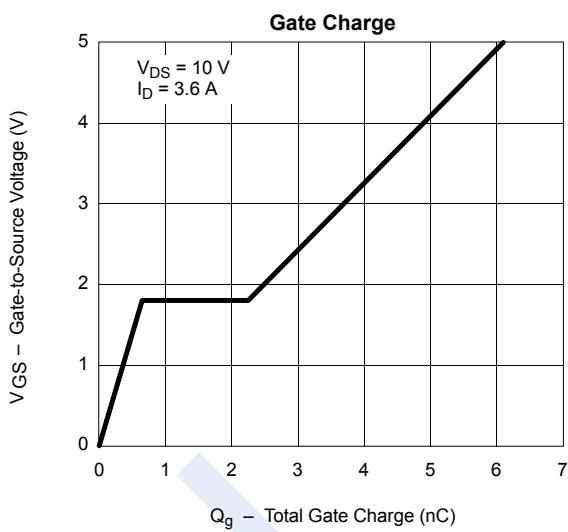
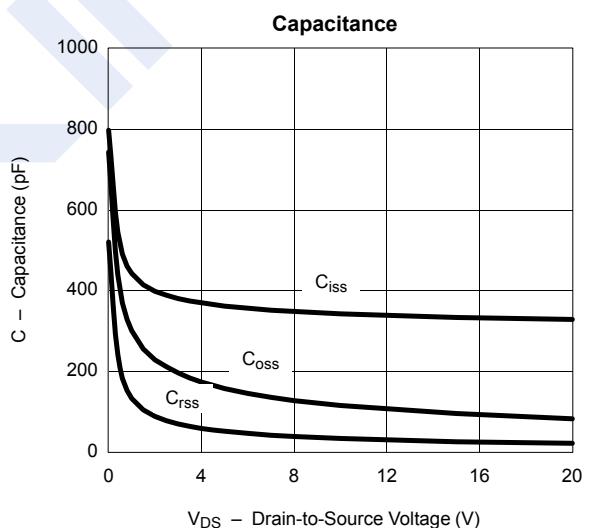
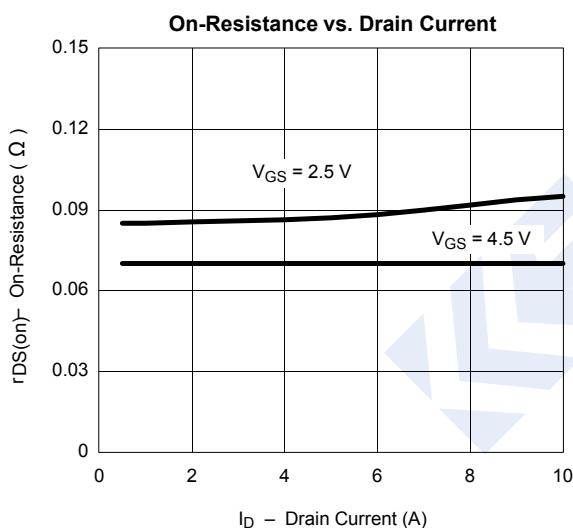
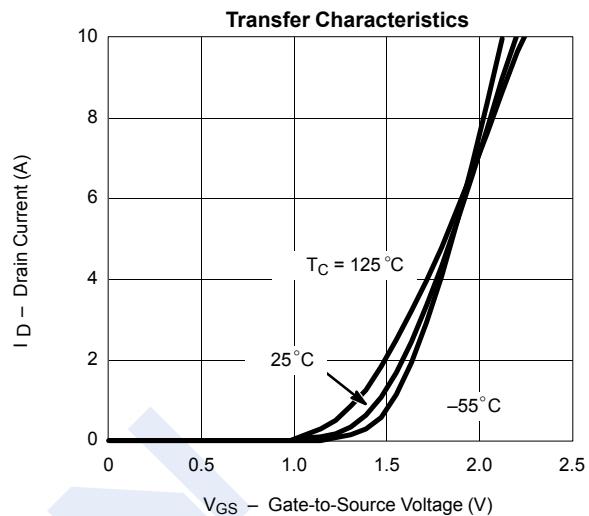
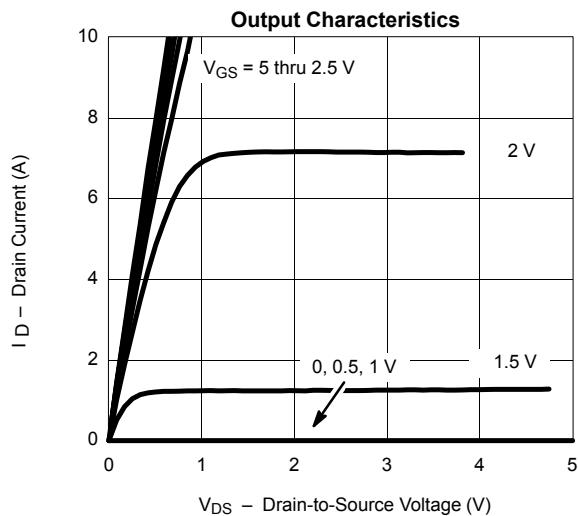
## ■ Marking

Marking	A2SHB
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## N-Channel Enhancement MOSFET

SI2302 (KI2302)

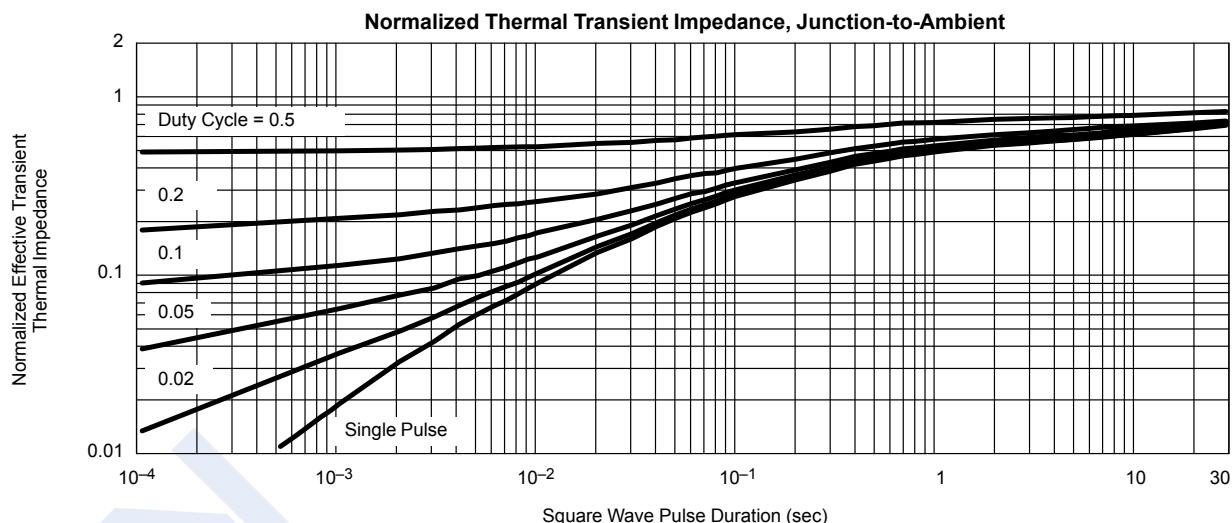
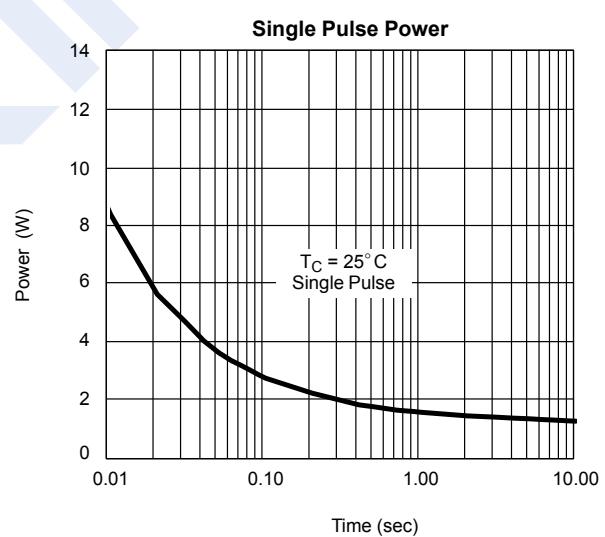
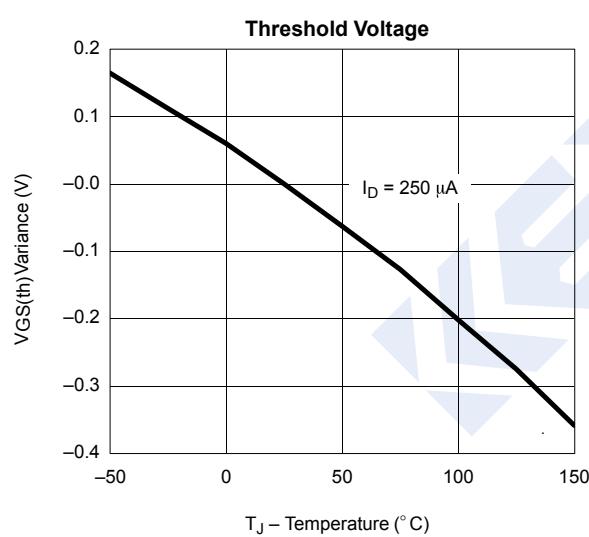
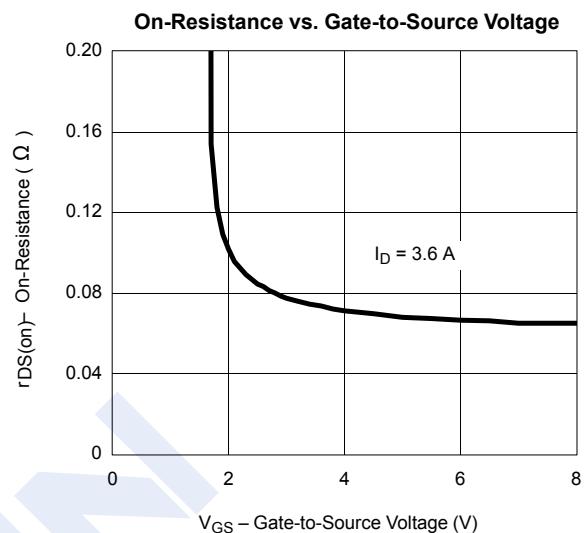
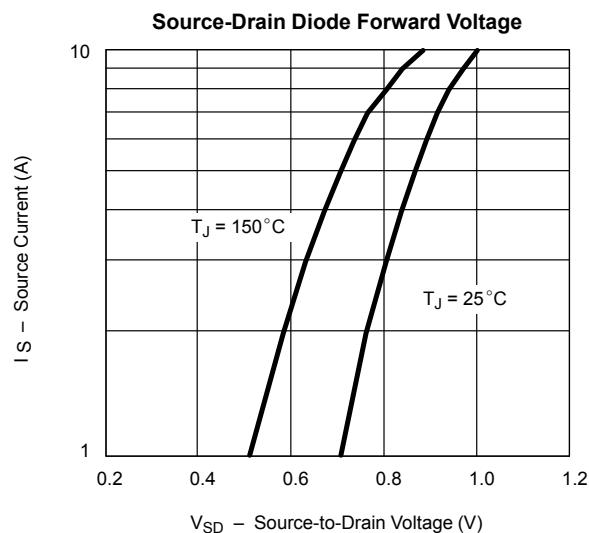
## ■ Typical Characteristics



## N-Channel Enhancement MOSFET

SI2302 (KI2302)

## ■ Typical Characteristics



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