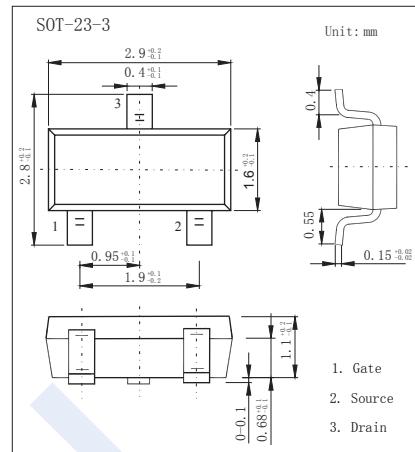
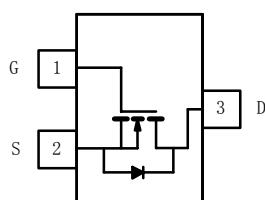


N-Channel Enhancement MOSFET

SI2312 (KI2312)

■ Features

- V_{DS} (V) = 20V
- I_D = 4.9 A (V_{GS} = 4.5V)
- $R_{DS(ON)} < 33m\Omega$ (V_{GS} = 4.5V)
- $R_{DS(ON)} < 40m\Omega$ (V_{GS} = 2.5V)
- $R_{DS(ON)} < 51m\Omega$ (V_{GS} = 1.8V)

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	5 sec	Steady State	Unit
Drain-Source Voltage	V_{DS}	20	3.77	V
Gate-Source Voltage	V_{GS}			
Continuous Drain Current $T_J=150^\circ C$ *1	I_D	4.9	3.77	A
		3.9	3.0	
Pulsed Drain Current *2	I_{DM}	15		A
Avalanche Current *2	I_{AS}	15		
Single Avalanche Energy	E_{AS}	11.25		mJ
Power Dissipation *1	P_D	1.25	0.75	W
		0.8	0.48	
Thermal Resistance.Junction- to-Ambient *1 $t \leq 5$ sec	R_{thJA}	100		$^\circ C/W$
Steady State		166		
Thermal Resistance.Junction-to-Foot	R_{thJF}	50		
Junction Temperature	T_J	150		$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150		

*1 Surface Mounted on 1" x 1" FR4 Board.

*2 Pulse width limited by maximum junction temperature

N-Channel Enhancement MOSFET

SI2312 (KI2312)

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=20\text{V}, V_{GS}=0\text{V}, T_a=70^\circ\text{C}$			75	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	0.45	0.65	0.85	V
On-State Drain Current *1	$I_{D(on)}$	$V_{DS} \geq 10\text{V}, V_{GS} = 4.5\text{V}$	15			A
Static Drain-Source On-Resistance *1	$R_{DS(on)}$	$V_{GS}=4.5\text{V}, I_D=5.0\text{A}$		27	33	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=4.5\text{A}$		33	40	
		$V_{GS}=1.8\text{V}, I_D=4.0\text{A}$		42	51	
Forward Transconductance *1	g_{FS}	$V_{DS}=15\text{V}, I_D=5.0\text{A}$		40		S
Total Gate Charge	Q_g	$V_{GS}=4.5\text{V}, V_{DS}=10\text{V}, I_D=5.0\text{A}$		11.2	14	nC
Gate Source Charge	Q_{gs}			1.4		
Gate Drain Charge	Q_{gd}			2.2		
Turn-On DelayTime	$t_{d(on)}$	$I_D=1.0\text{A}, V_{DS}=10\text{V}, V_{GEN}=4.5\text{V}$ $R_L=10\Omega, R_G=6\Omega$		15	25	ns
Turn-On Rise Time	t_r			40	60	
Turn-Off DelayTime	$t_{d(off)}$			48	70	
Turn-Off Fall Time	t_f			31	45	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 1.0\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		13	25	
Maximum Body-Diode Continuous Current	I_S				1.0	A
Diode Forward Voltage	V_{SD}	$I_S=1.0\text{A}, V_{GS}=0\text{V}$		0.8	1.2	V

*1 Pulse test: $PW \leq 300\text{us}$ duty cycle $\leq 2\%$.

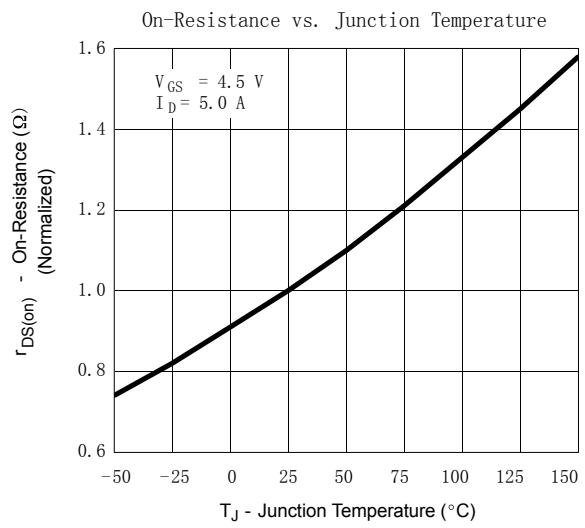
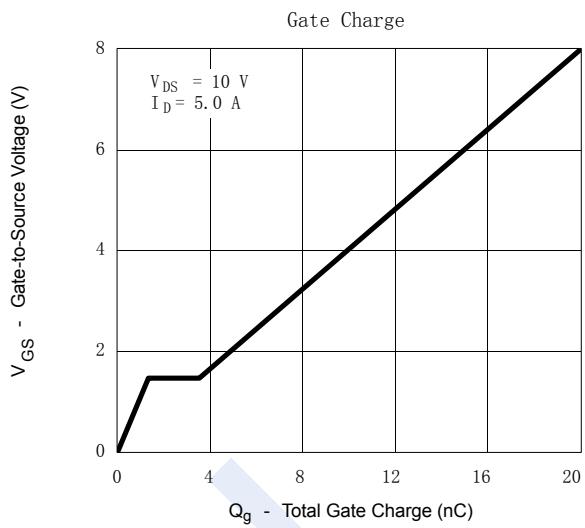
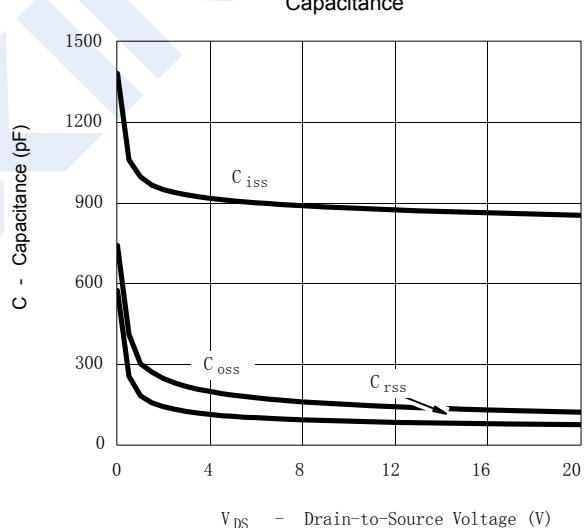
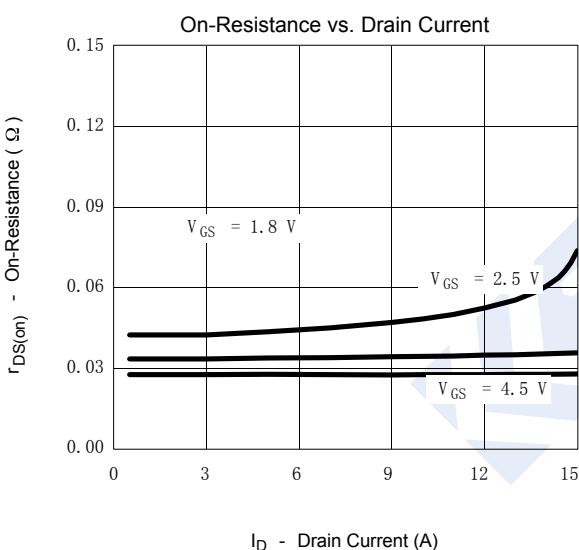
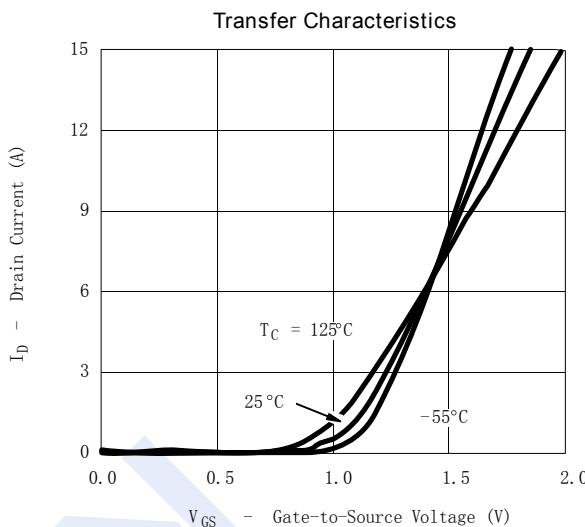
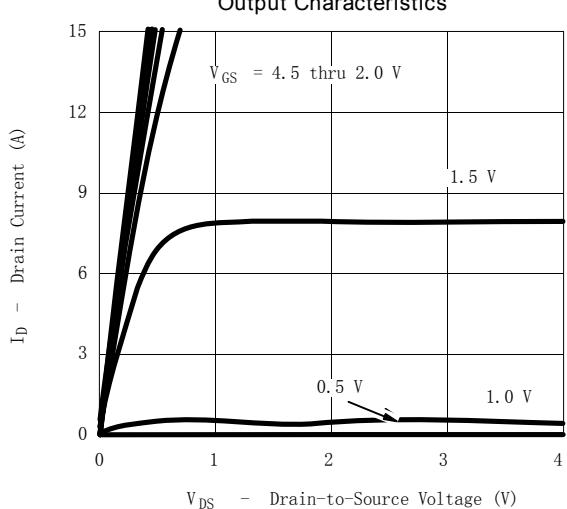
■ Marking

Marking	AE9T
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N-Channel Enhancement MOSFET

SI2312 (KI2312)

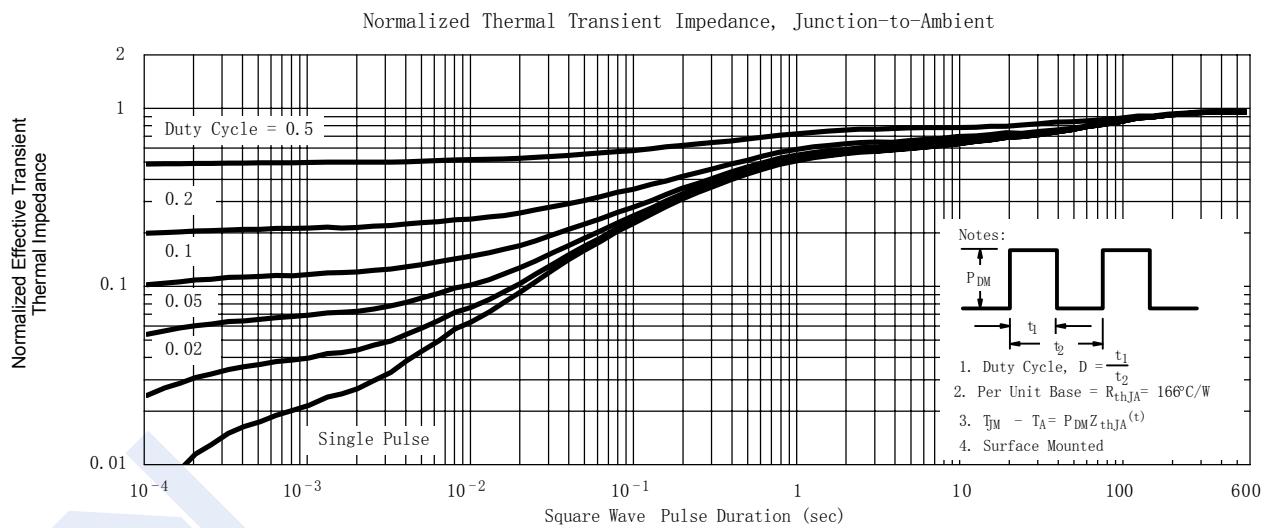
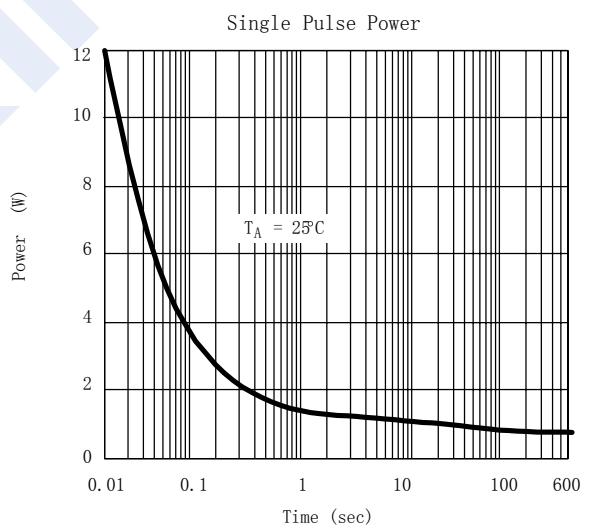
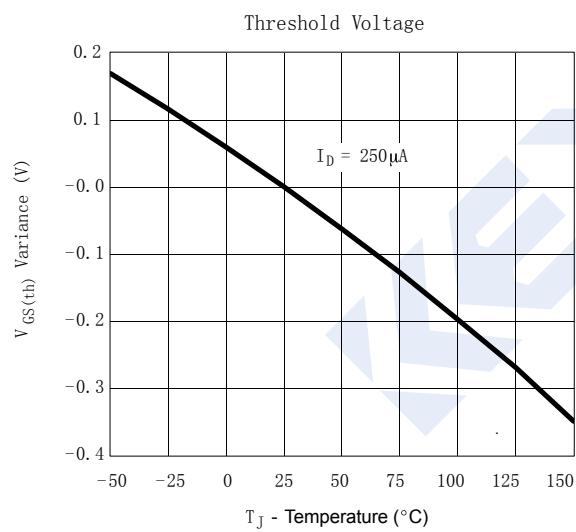
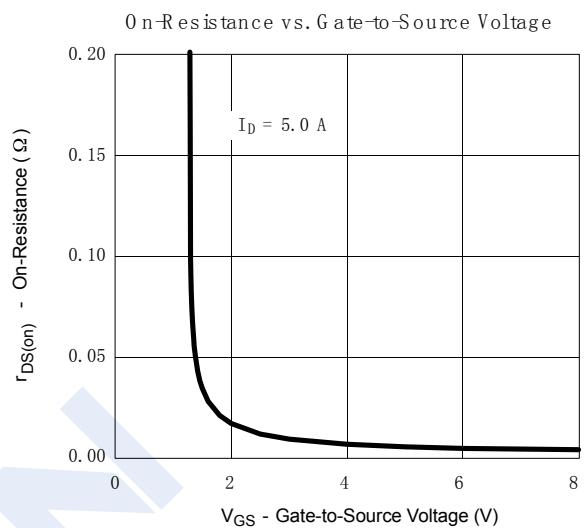
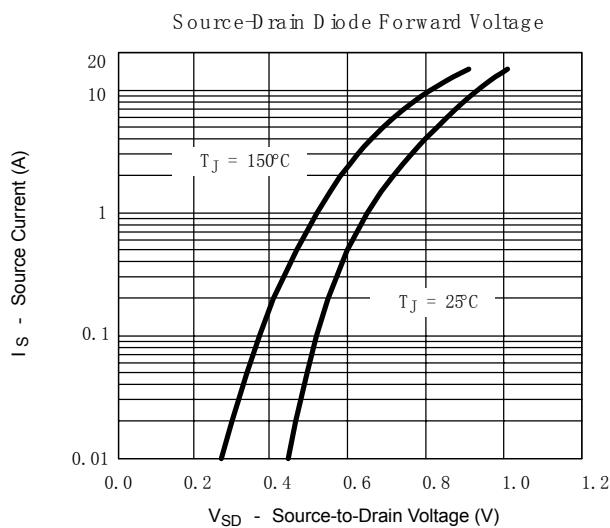
■ Typical Characteristics



N-Channel Enhancement MOSFET

SI2312 (KI2312)

■ Typical Characteristics



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