

General Description

KEC Field Stop Trench IGBTs offer low switching losses, high energy efficiency and short circuit ruggedness.

It is designed for applications such as motor control, uninterrupted power supplies(UPS), general inverters.

FEATURES

- High speed switching
- High ruggedness, temperature stable behavior
- Short Circuit Withstand Times 10us
- Extremely enhanced avalanche capability

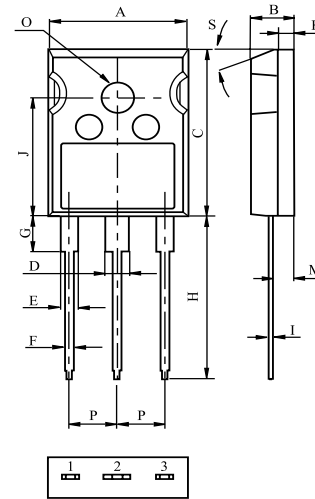
MAXIMUM RATING (Ta=25 °C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Collector-Emitter Voltage		V_{CES}	600	V
Gate-Emitter Voltage		V_{GES}	± 20	V
Collector Current	@Tc=25	I_C	100	A
	@Tc=100		50	A
Pulsed Collector Current		I_{CM}^*	150	A
Diode Continuous Forward Current	@Tc=100	I_F	50	A
Diode Maximum Forward Current		I_{FM}	100	A
Maximum Power Dissipation	@Tc=25	P_D	277	W
	@Tc=100		111	W
Maximum Junction Temperature		T_j	150	
Storage Temperature Range		T_{stg}	-55 to + 150	

*Repetitive rating : Pulse width limited by max. junction temperature

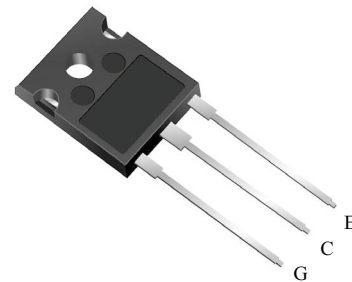
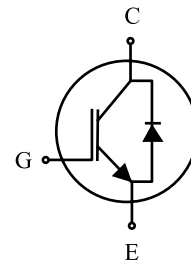
THERMAL CHARACTERISTIC

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Junction to Case (IGBT)	R_{thJC}	0.45	/W
Thermal Resistance, Junction to Case (DIODE)	R_{thJC}	1.0	/W
Thermal Resistance, Junction to Ambient	R_{thJA}	40	/W



DIM	MILLIMETERS
A	15.90 ± 0.30
B	5.00 ± 0.20
C	20.85 ± 0.30
D	3.00 ± 0.20
E	2.00 ± 0.20
F	1.20 ± 0.20
G	Max. 4.50
H	20.10 ± 0.70
I	0.60 ± 0.02
J	14.70 ± 0.20
K	2.00 ± 0.10
M	2.40 ± 0.20
O	φ 3.60 ± 0.30
P	5.45 ± 0.30
Q	φ 3.60 ± 0.20
R	φ 7.19 ± 0.10
S	20°

TO-247



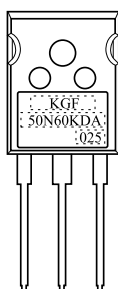
KGF50N60KDA

ELECTRICAL CHARACTERISTICS (Ta=25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Static							
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE}=0V, I_C=250\mu A$	600	-	-	V	
Collector Cut-off Current	I_{CES}	$V_{GE}=0V, V_{CE}=600V$	-	-	250	μA	
Gate Leakage Current	I_{GES}	$V_{CE}=0V, V_{GE}=\pm 20V$	-	-	± 100	nA	
Gate Threshold Voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}, I_C=5mA$	4.5	5.5	7	V	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE}=15V, I_C=50A$	-	1.65	2.1	V	
		$V_{GE}=15V, I_C=100A$	-	2.25	-	V	
		$V_{GE}=15V, I_C=50A, T_C = 125$	-	1.9	-	V	
Dynamic							
Total Gate Charge	Q_g	$V_{CC}=300V, V_{GE}=15V, I_C= 50A$	-	200	-	nC	
Gate-Emitter Charge	Q_{ge}		-	30	-	nC	
Gate-Collector Charge	Q_{gc}		-	100	-	nC	
Turn-On Delay Time	$t_{d(on)}$	$V_{CC}=300V, I_C=50A, V_{GE}=15V, R_G=10$ Inductive Load, $T_C = 25$ (Note 1)	-	60	-	ns	
Rise Time	t_r		-	45	-	ns	
Turn-Off Delay Time	$t_{d(off)}$		-	250	-	ns	
Fall Time	t_f		-	40	-	ns	
Turn-On Switching Loss	E_{on}		-	1.25	1.65	mJ	
Turn-Off Switching Loss	E_{off}		-	0.95	1.25	mJ	
Total Switching Loss	E_{ts}		-	2.2	2.9	mJ	
Turn-On Delay Time	$t_{d(on)}$		$V_{CC}=300V, I_C=50A, V_{GE}=15V, R_G=10$ Inductive Load, $T_C = 125$ (Note 1)	-	60	-	ns
Rise Time	t_r			-	50	-	ns
Turn-Off Delay Time	$t_{d(off)}$			-	260	-	ns
Fall Time	t_f	-		50	-	ns	
Turn-On Switching Loss	E_{on}	-		1.25	-	mJ	
Turn-Off Switching Loss	E_{off}	-		1.15	-	mJ	
Total Switching Loss	E_{ts}	-		2.4	-	mJ	
Input Capacitance	C_{ies}	$V_{CE}=30V, V_{GE}=0V, f=1MHz$	-	4000	5200	pF	
Output Capacitance	C_{oes}		-	250	-	pF	
Reverse Transfer Capacitance	C_{res}		-	150	-	pF	
Short Circuit Withstand Time	t_{sc}		$V_{CC}=300V, V_{GE}=15V, T_C=100$	10	-	-	μs

Note 1 : Energy loss include tail current and diode reverse recovery.

Marking



- ① Device Mark 1
- ② Device Mark 2
- ③ Lot No

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ELECTRICAL CHARACTERISTIC OF DIODE

CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Diode Forward Voltage	V_F	$I_F = 50A$	$T_C = 25$	-	1.8	2.5	V
			$T_C = 125$	-	1.9	-	
Diode Reverse Recovery Time	t_{rr}		$T_C = 25$	-	100	-	ns
			$T_C = 125$	-	175	-	
Diode Peak Reverse Recovery Current	I_{rr}	$V_{CC} = 300V, I_F = 50A$ $di/dt = 600A/\mu s$	$T_C = 25$	-	19	-	A
			$T_C = 125$	-	22	-	
Diode Reverse Recovery Charge	Q_{rr}		$T_C = 25$	-	1.1	-	μC
			$T_C = 125$	-	2.3	-	

Fig 1. Saturation Voltage Characteristics

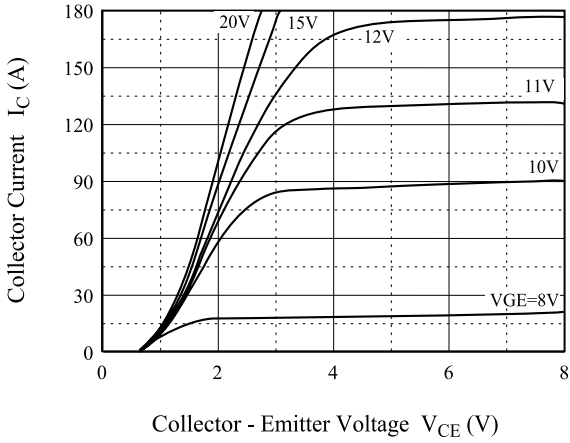


Fig 2. Saturation Voltage Characteristics

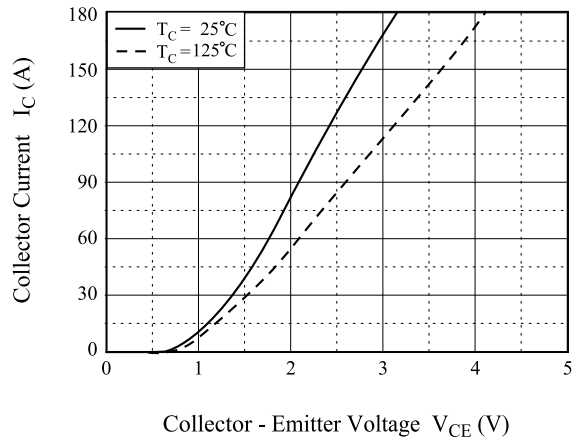


Fig 3. Saturation Voltage vs. Case Temperature

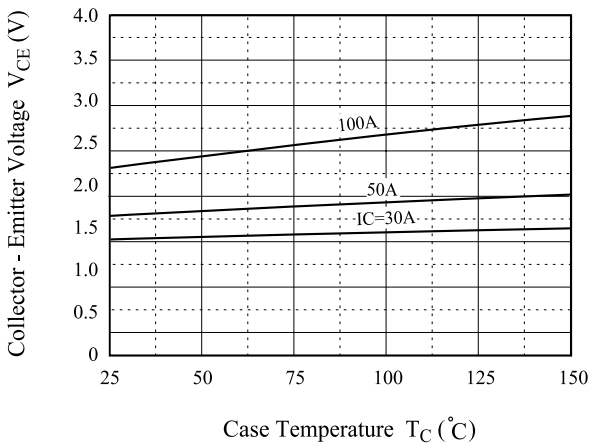


Fig 4. Saturation Voltage vs. V_{GE}

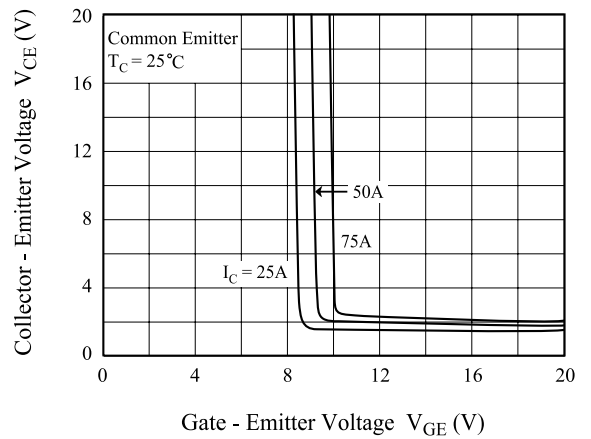


Fig 5. Saturation Voltage vs. V_{GE}

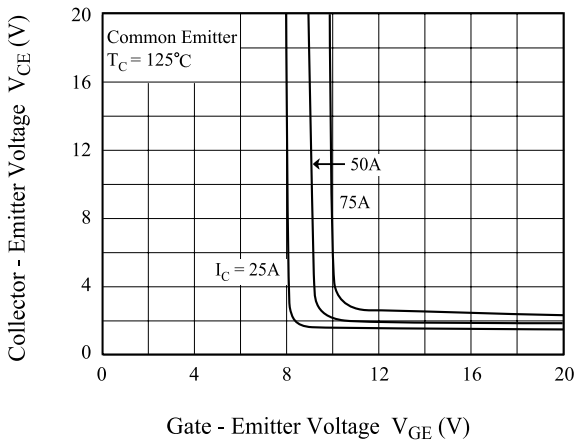
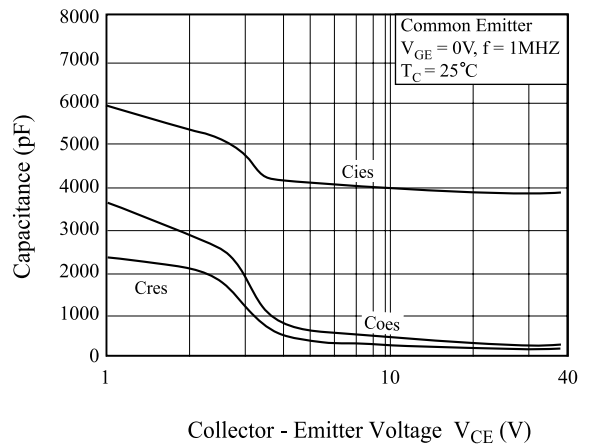


Fig 6. Capacitance Characteristics



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Fig 7. Turn-On Characteristics vs. Gate Resistance

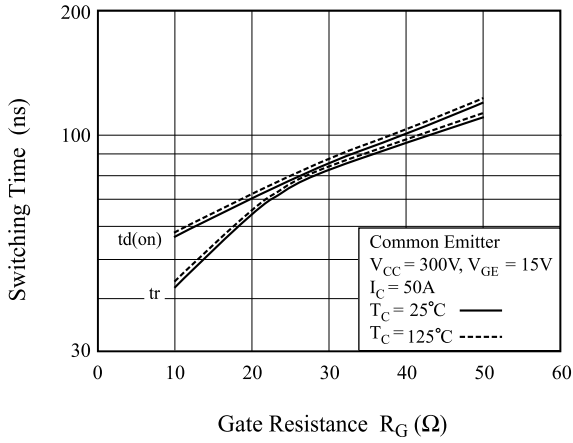


Fig 8. Turn-Off Characteristics vs. Gate Resistance

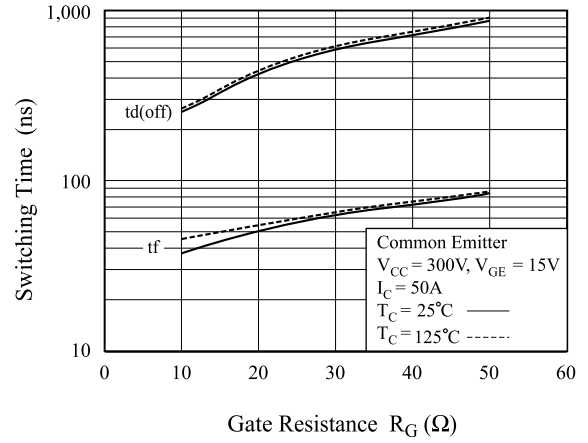


Fig 9. Switching Loss vs. Gate Resistance

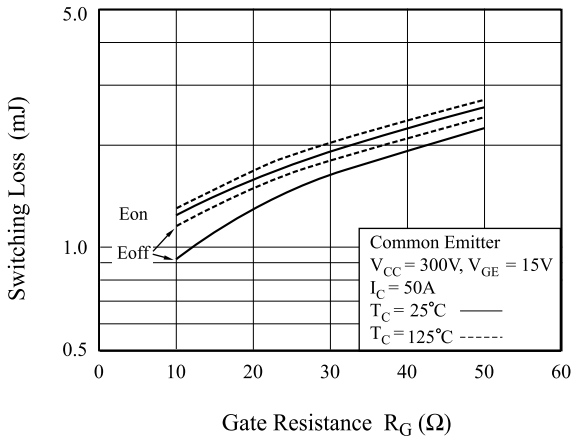


Fig 10. Turn-On Characteristics vs. Collector Current

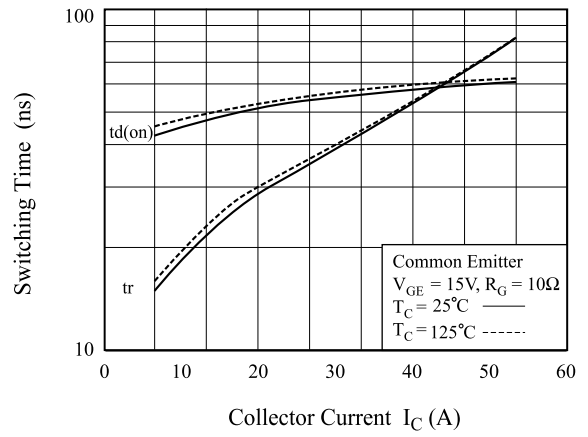


Fig 11. Turn-Off Characteristics vs. Collector Current

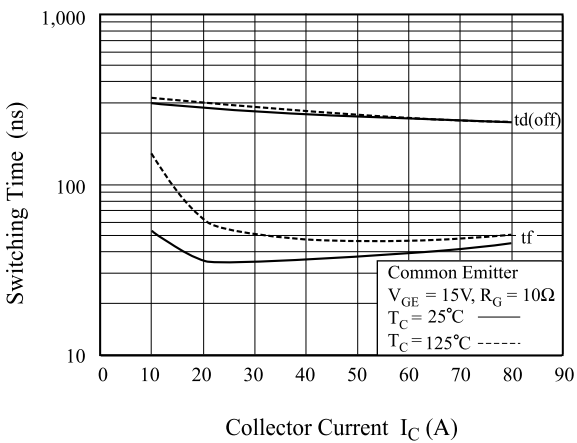
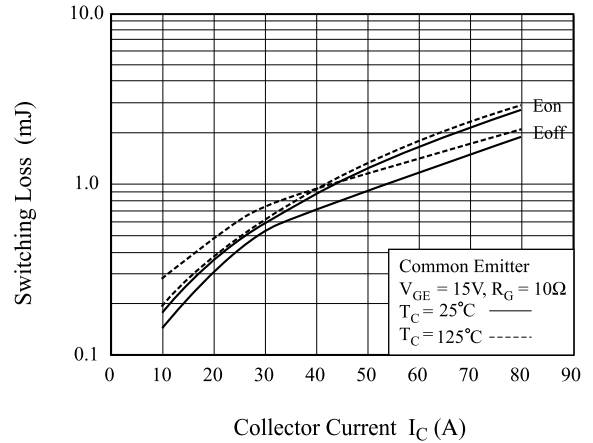


Fig 12. Switching Loss vs. Collector Current



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Fig 13. Gate Charge Characteristics

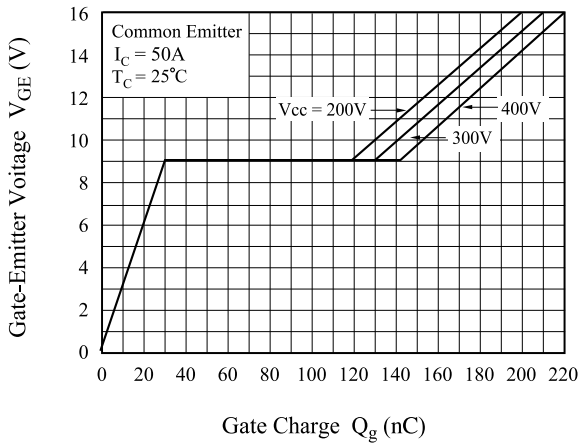


Fig 14. SOA Characteristics

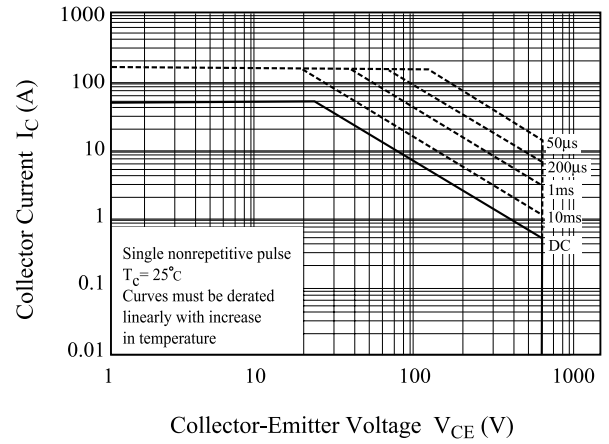


Fig 15. Turn-Off SOA

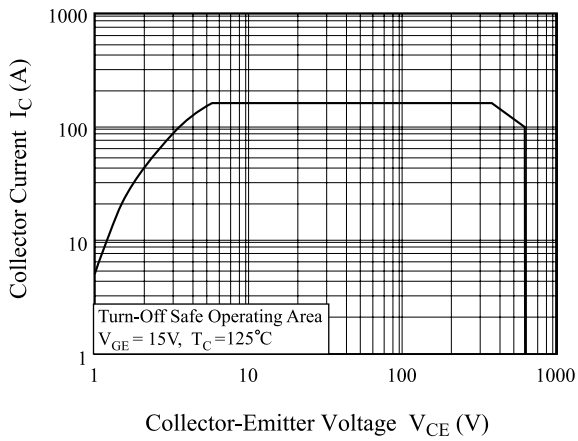
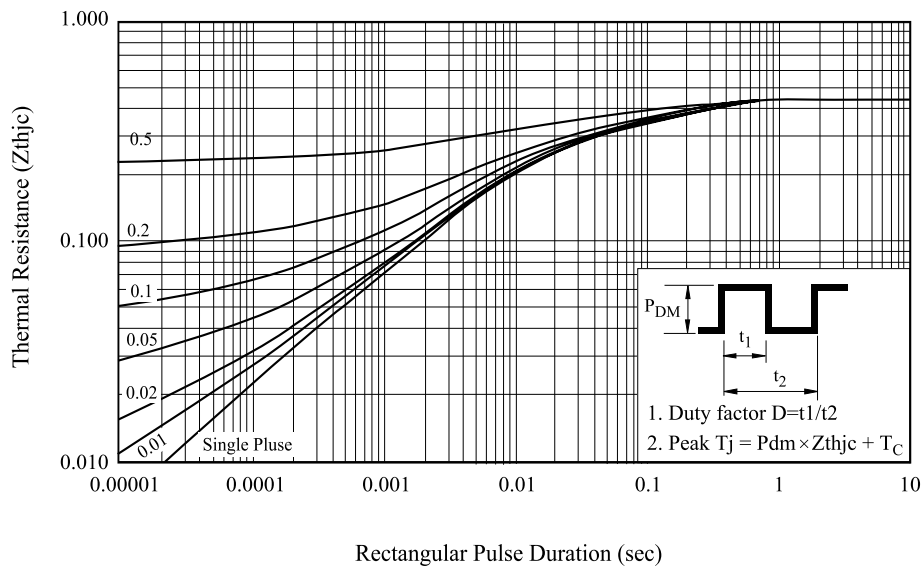


Fig 16. Transient Thermal Impedance of IGBT



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Fig 18. Forward Characteristics

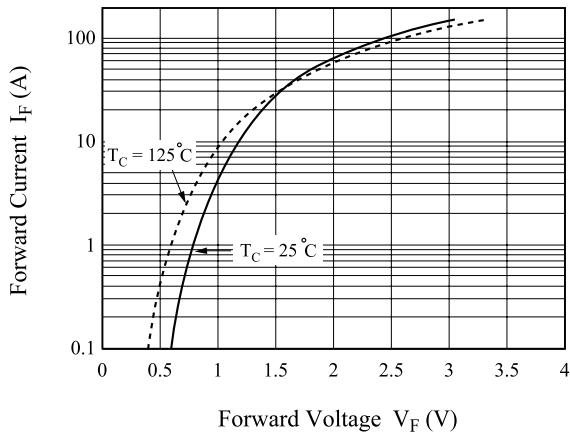


Fig 19. Reverse Recovery Current

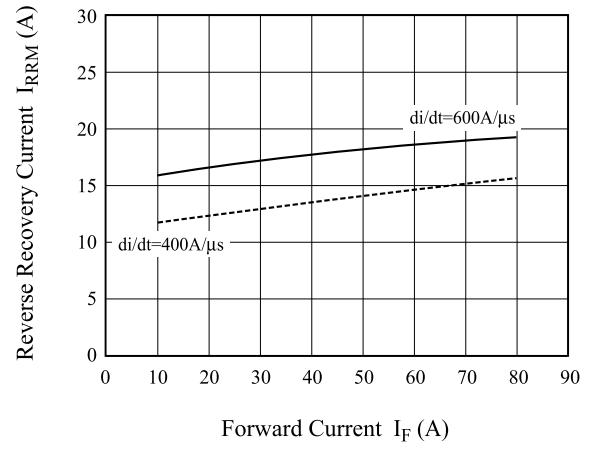


Fig 20. Reverse Recovery Time

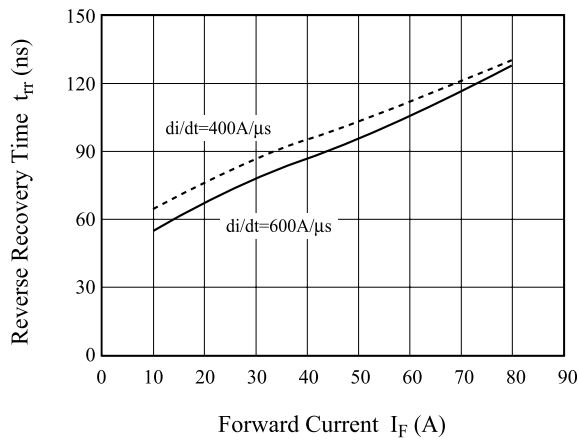


Fig 21. Switching Test Circuit

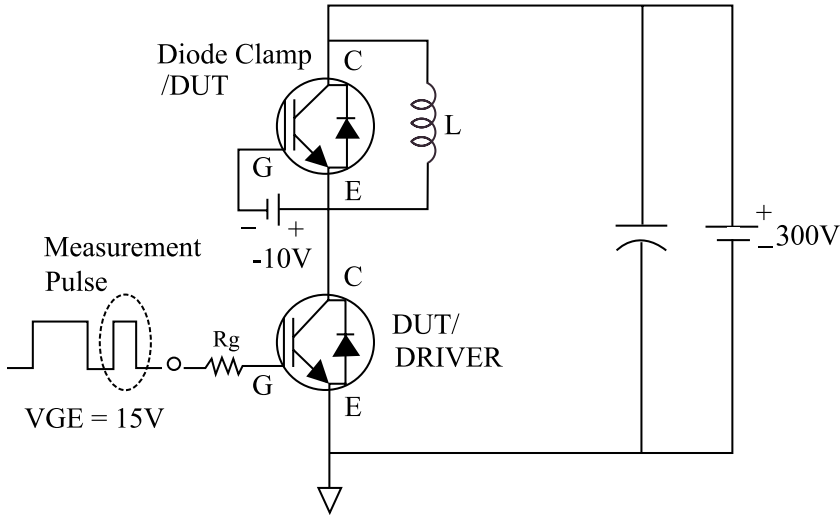


Fig 22. Definition Switching Time & Loss

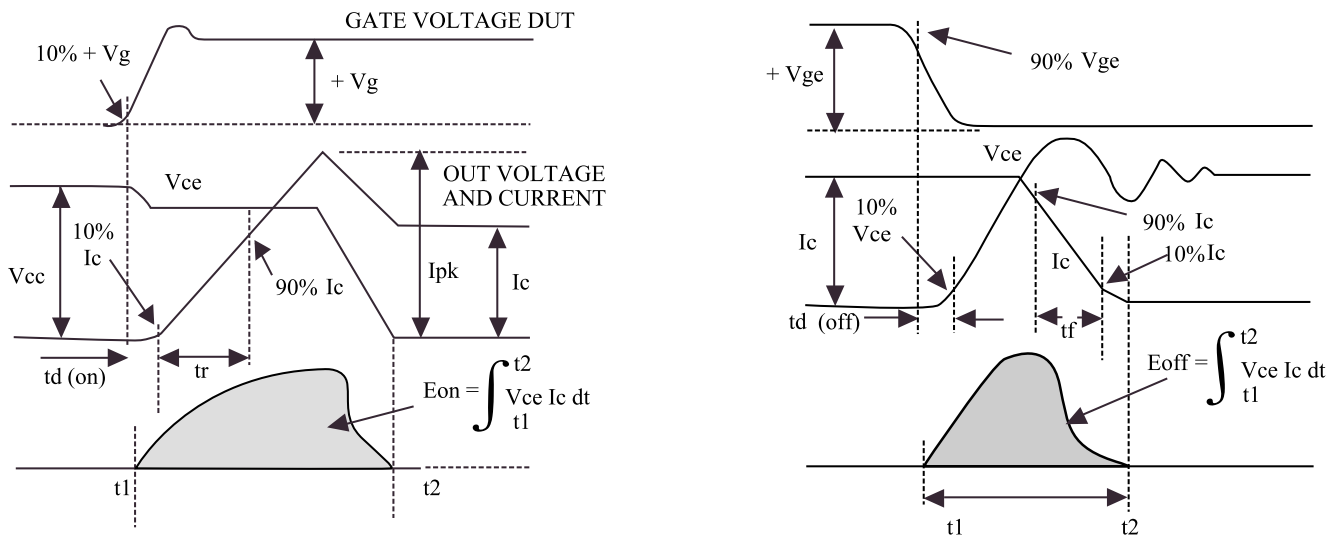
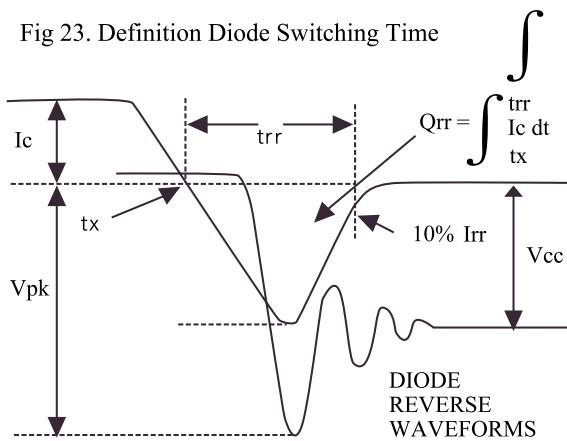


Fig 23. Definition Diode Switching Time



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[IDW40E65D2](#) [NGTB50N60L2WG](#) [STGB10H60DF](#) [STGB20V60F](#) [STGB40V60F](#) [STGFW80V60F](#) [IGW40N120H3FKSA1](#)
[RJH60D7BDPQ-E0#T2](#) [APT40GR120B](#)