

# Dual General Purpose Transistors

These transistors are designed for general purpose amplifier applications. They are housed in the SOT–363/SC–88 which is designed for low power surface mount applications.

We declare that the material of product compliance with RoHS requirements.

S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

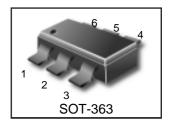
#### • Device Marking:

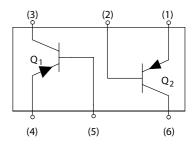
(S-)LBC856ADW1T1G= 3A (S-)LBC856BDW1T1G= 3B (S-)LBC857BDW1T1G= 3F (S-)LBC857CDW1T1G= 3G (S-)LBC858BDW1T1G= 3K (S-)LBC858CDW1T1G= 3L

#### **MAXIMUM RATINGS**

Rating	Symbol	BC856	BC857	BC858	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	-65	-45	-30	V
Collector–Base Voltage	V <sub>CBO</sub>	-80	-50	-30	V
Emitter–Base Voltage	V <sub>EBO</sub>	-5.0	-5.0	-5.0	V
Collector Current – Continuous	Ι <sub>C</sub>	-100	-100	-100	mAdc

## LBC85\*\* DW1T1G S-LBC85\*\* DW1T1G





#### THERMAL CHARACTERISTICS

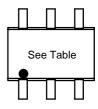
Characteristic	Symbol	Мах	Unit
Total Device Dissipation Per Device FR–5 Board (Note 1.) $T_A = 25^{\circ}C$ Derate Above 25°C	P <sub>D</sub>	380 250 3.0	mW mW/°C
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	328	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1. FR-5 = 1.0 x 0.75 x 0.062 in

#### **ORDERING INFORMATION**

Device	Shipping			
LBC85*BDW1T1G	3000/Tape & Reel			
LBC85*BDW1T3G	10000/Tape & Reel			

#### **DEVICE MARKING**





### LBC856ADW1T1G,LBC856BDW1T1G,LBC857BDW1T1G,LBC857CDW1T1G, LBC858BDW1T1G, LBC858CDW1T1G S-LBC856ADW1T1G,S-LBC856BDW1T1G,S-LBC857BDW1T1G, S-LBC857CDW1T1G, S-LBC858BDW1T1G, S-LBC858CDW1T1G

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = $25^{\circ}$ C unless otherwise noted)

Charac	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS				•		•
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –10 mA)	LBC856 Series LBC857 Series LBC858 Series	V <sub>(BR)CEO</sub>	65 45 30	_ _ _		V
Collector–Emitter Breakdown Voltage ( $I_C = -10 \ \mu A$ , $V_{EB} = 0$ )	LBC856 Series LBC857 Series LBC858 Series	V <sub>(BR)CES</sub>	80 50 30	_ _ _	- - -	V
Collector–Base Breakdown Voltage $(I_C = -10 \ \mu A)$	LBC856 Series LBC857 Series LBC858 Series	V <sub>(BR)CBO</sub>	80 50 30	- - -	- - -	V
Emitter–Base Breakdown Voltage ( $I_E = -1.0 \ \mu A$ )	LBC856 Series LBC857 Series LBC858 Series	V <sub>(BR)EBO</sub>	5.0 5.0 5.0	_ _ _	- - -	V
Collector Cutoff Current (V <sub>CB</sub> = $-30$ V) (V <sub>CB</sub> = $-30$ V, T <sub>A</sub> = $150^{\circ}$ C)		I <sub>CBO</sub>			-15 -4.0	nA μA
ON CHARACTERISTICS						
$(I_{C} = -10 \ \mu\text{A}, V_{CE} = -5.0 \ \text{V})$ LBC LBC $(I_{C} = -2.0 \ \text{mA}, V_{CE} = -5.0 \ \text{V})$ LB	2856A 2856B, LBC857B, LBC858B 2857C, LBC858C C856A 2856B, LBC857B, LBC858B	h <sub>FE</sub>	- - 125 220	90 150 270 180 290	- - 250 475	_
$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	V <sub>CE(sat)</sub>	<u>420</u> _ _	520 - -	800 -0.3 -0.65	V	
Base–Emitter Saturation Voltage ( $I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$ ) ( $I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$ )		V <sub>BE(sat)</sub>	-	-0.7 -0.9		V
Base–Emitter On Voltage ( $I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$ ) ( $I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$ )		V <sub>BE(on)</sub>	-0.6 -		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERIS	TICS					1
Current–Gain – Bandwidth Product ( $I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 1	00 MHz)	f <sub>T</sub>	100	-	-	MHz
Output Capacitance (V <sub>CB</sub> = -10 V, f = 1.0 MHz)		C <sub>ob</sub>	-	-	4.5	pF
Noise Figure ( $I_{C} = -0.2 \text{ mA}, V_{CE} = -5.0 \text{ Vdc}, R_{S} = f = 1.0 \text{ kHz}, BW = 200 \text{ Hz}$ )	= 2.0 kΩ,	NF	-	_	10	dB



#### LBC856ADW1T1G,LBC856BDW1T1G,LBC857BDW1T1G,LBC857CDW1T1G, LBC858BDW1T1G, LBC858CDW1T1G S-LBC856ADW1T1G,S-LBC856BDW1T1G,S-LBC857BDW1T1G,S-LBC857CDW1T1G, S-LBC858BDW1T1G, S-LBC858CDW1T1G

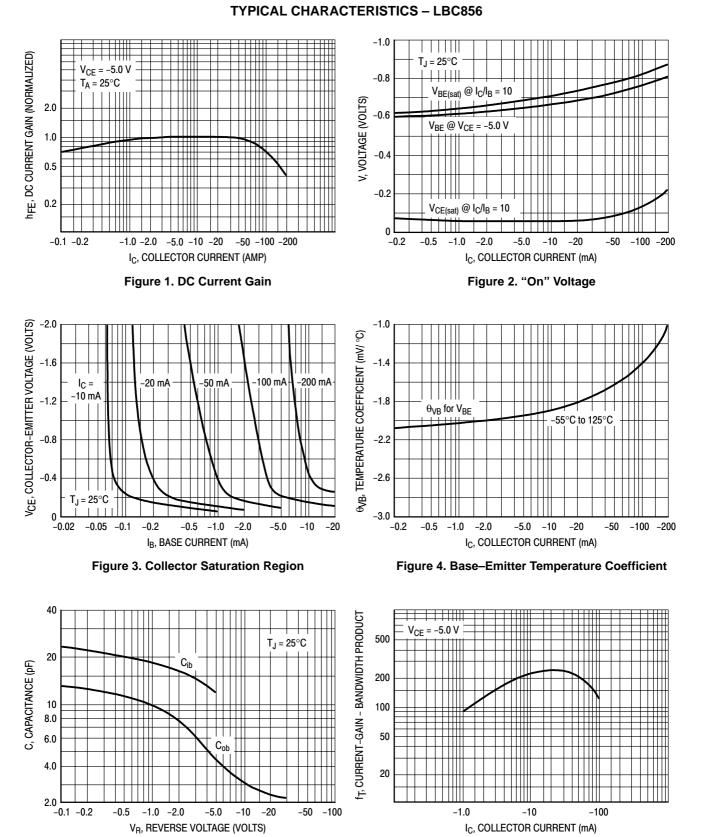
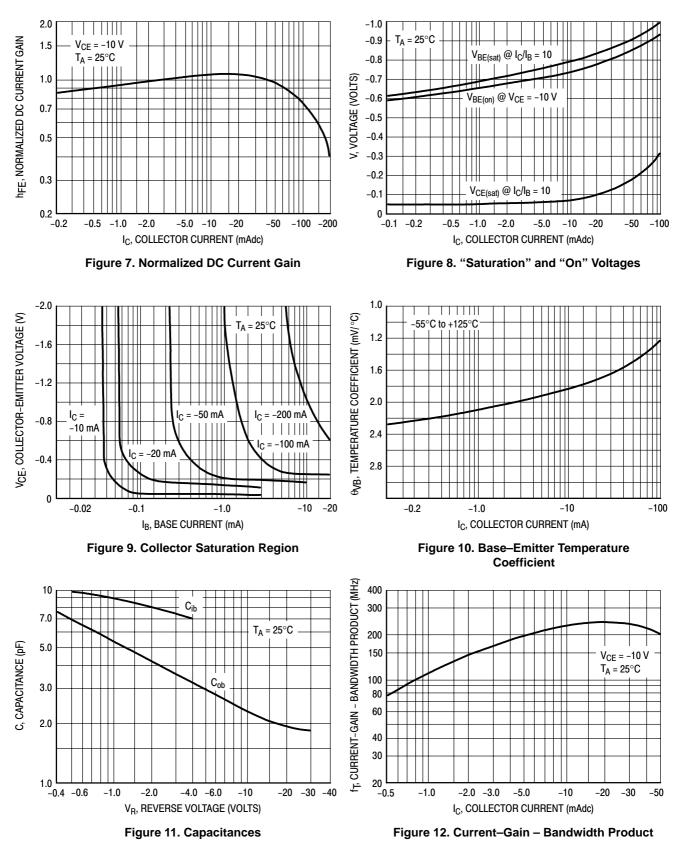


Figure 5. Capacitance

Figure 6. Current–Gain – Bandwidth Product



#### LBC856ADW1T1G,LBC856BDW1T1G,LBC857BDW1T1G,LBC857CDW1T1G, LBC858BDW1T1G, LBC858CDW1T1G S-LBC856ADW1T1G,S-LBC856BDW1T1G,S-LBC857BDW1T1G,S-LBC857CDW1T1G, S-LBC858BDW1T1G, S-LBC858CDW1T1G





### LBC856ADW1T1G,LBC856BDW1T1G,LBC857BDW1T1G,LBC857CDW1T1G, LBC858BDW1T1G, LBC858CDW1T1G S-LBC856ADW1T1G,S-LBC856BDW1T1G,S-LBC857BDW1T1G, S-LBC857CDW1T1G, S-LBC858BDW1T1G, S-LBC858CDW1T1G

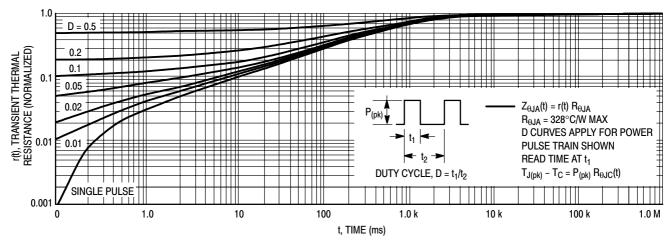


Figure 13. Thermal Response

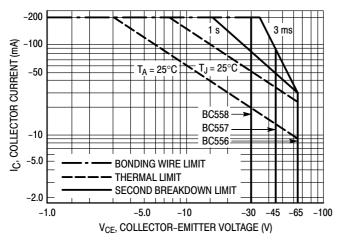


Figure 14. Active Region Safe Operating Area

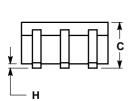
The safe operating area curves indicate  $I_C-V_{CE}$  limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

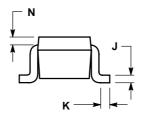
The data of Figure 14 is based upon  $T_{J(pk)} = 150^{\circ}C$ ;  $T_C$  or  $T_A$  is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided  $T_{J(pk)} \le 150^{\circ}C$ .  $T_{J(pk)}$  may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.



### LBC856ADW1T1G,LBC856BDW1T1G,LBC857BDW1T1G,LBC857CDW1T1G, LBC858BDW1T1G, LBC858CDW1T1G S-LBC856ADW1T1G,S-LBC856BDW1T1G,S-LBC857BDW1T1G, S-LBC857CDW1T1G, S-LBC858BDW1T1G, S-LBC858CDW1T1G

## SC-88/SOT-363



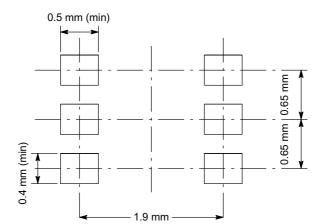


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS		
2	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026 BSC		0.65 BSC		
н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
к	0.004	0.012	0.10	0.30	
N	0.008 REF		0 .20 REF		
S	0.079	0.087	2.00	2.20	

PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2



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