

40 V, 5 A low VF MEGA Schottky barrier rectifier 26 July 2016 Pro

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 5 A
- Reverse voltage: $V_R \le 40 \text{ V}$
- Extremely low forward voltage
- High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Quic Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	square wave; δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 158 °C		-	-	5	A
V _R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	$I_F = 5 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02 ;$ $T_j = 25 \text{ °C}$		-	475	520	mV
I _R	reverse current	V_R = 10 V; T _j = 25 °C; pulsed	[1]	-	6	52	μA
		V_R = 40 V; T _j = 25 °C; pulsed	[1]	-	32	120	μA

[1] Very short test pulse to prevent junction self heating

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5. Pinning information

Table 2. F	Pinning inf	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	А	anode		
3	К	cathode	(2) CFP15 (SOT1289)	

6. Ordering information

Table 3. Ordering infor	mation		
Type number	Package		
	Name	Description	Version
PMEG040V050EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG040V050EPD	040V U05E

40 V, 5 A low VF MEGA Schottky barrier rectifier

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
l _F	forward current	T _{sp} ≤ 154 °C; δ = 1		-	7	А
I _{F(AV)}	average forward current	square wave; δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 158 °C		-	5	A
I _{FSM}	non-repetitive peak forward current	square wave; t_p = 8 ms; $T_{j(init)}$ = 25 °C		-	120	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	-	[1][2]	-	-	90	K/W
			[1][3]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

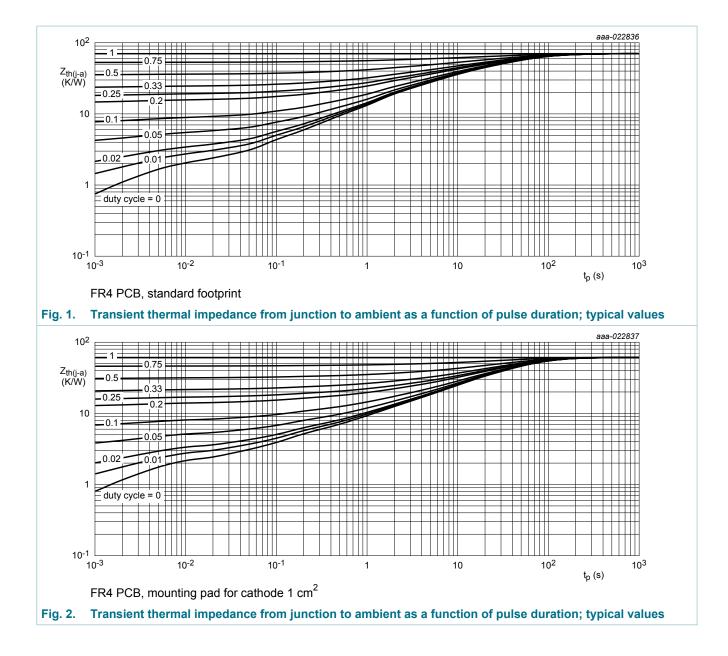
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

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40 V, 5 A low VF MEGA Schottky barrier rectifier



40 V, 5 A low VF MEGA Schottky barrier rectifier

10. Characteristics

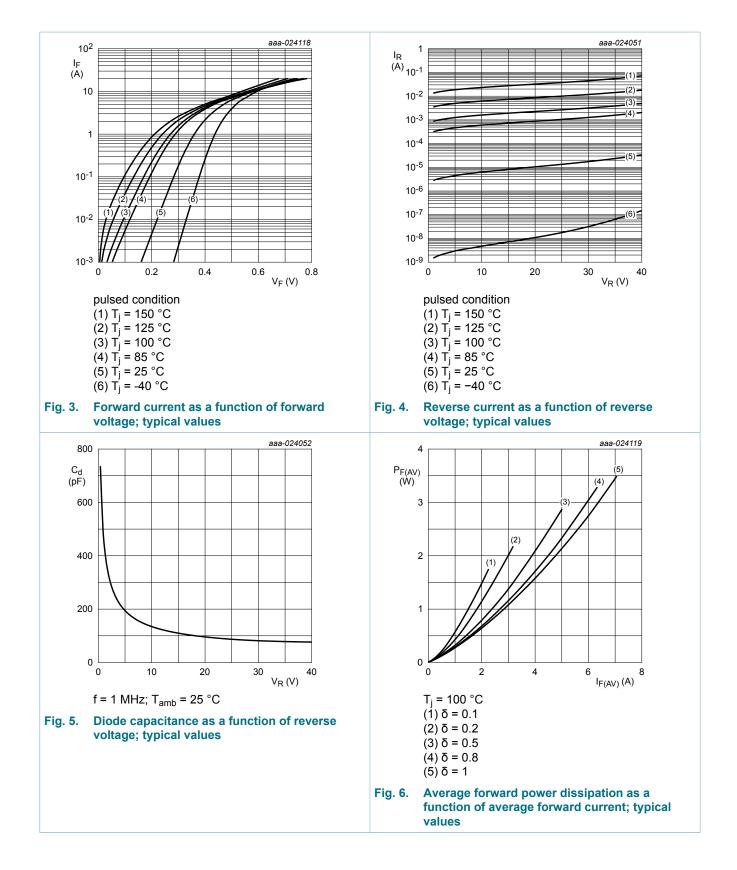
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_R = 3 mA; T_j = 25 °C; pulsed	[1]	40	-	-	V
V _F	forward voltage	$\begin{array}{l} I_{\text{F}} = 0.1 \; \text{A}; t_{p} \leq \; 300 \; \mu\text{s}; \delta \leq \; 0.02 \; \; ; \\ T_{j} = 25 \; ^{\circ}\text{C} \end{array}$		-	285	320	mV
		I_{F} = 1 A; t_{p} $\leq~$ 300 $\mu\text{s};$ δ $\leq~$ 0.02 $;$ T_{j} = 25 $^{\circ}\text{C}$		-	360	420	mV
		$\label{eq:IF} \begin{array}{l} I_{\text{F}} = 1.5 \; \text{A}; t_{p} \leq \; 300 \; \mu\text{s}; \bar{\delta} \leq \; 0.02 \; \; ; \\ T_{j} = 25 \; ^{\circ}\text{C} \end{array}$		-	380	435	mV
		I_{F} = 2 A; t_{p} $\leq~$ 300 $\mu\text{s};$ δ $\leq~$ 0.02 $;$ T_{j} = 25 $^{\circ}\text{C}$		-	395	-	mV
		I_{F} = 3 A; t_{p} \leq 300 $\mu \text{s}; \delta \leq$ 0.02 ; T_{j} = 25 °C		-	425	490	mV
		I_{F} = 5 A; t_{p} $\leq~$ 300 $\mu\text{s};$ δ $\leq~$ 0.02 $;$ T_{j} = 25 $^{\circ}\text{C}$		-	475	520	mV
		I_{F} = 5 A; t_{p} $\leq~$ 300 $\mu\text{s};$ δ $\leq~$ 0.02 $;$ T_{j} = -40 $^{\circ}\text{C}$		-	515	-	mV
		I_{F} = 5 A; $t_{\text{p}} \leq 300 \ \mu\text{s}; \ \delta \leq 0.02$; T_{j} = 125 °C		-	415	-	mV
I _R	reverse current	V_{R} = 10 V; T _j = 25 °C; pulsed	[1]	-	6	52	μA
		V_{R} = 30 V; T _j = 25 °C; pulsed	[1]	-	17	-	μA
		V_{R} = 40 V; T _j = 25 °C; pulsed	[1]	-	32	120	μA
		V_{R} = 40 V; T _j = 125 °C; pulsed	[1]	-	18	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	395	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C		-	210	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	130	-	pF
rr	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	13	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; T _j = 25 °C; I _F = 6 A; V _R = 26 V		-	11	-	ns

[1] Very short test pulse to prevent junction self heating

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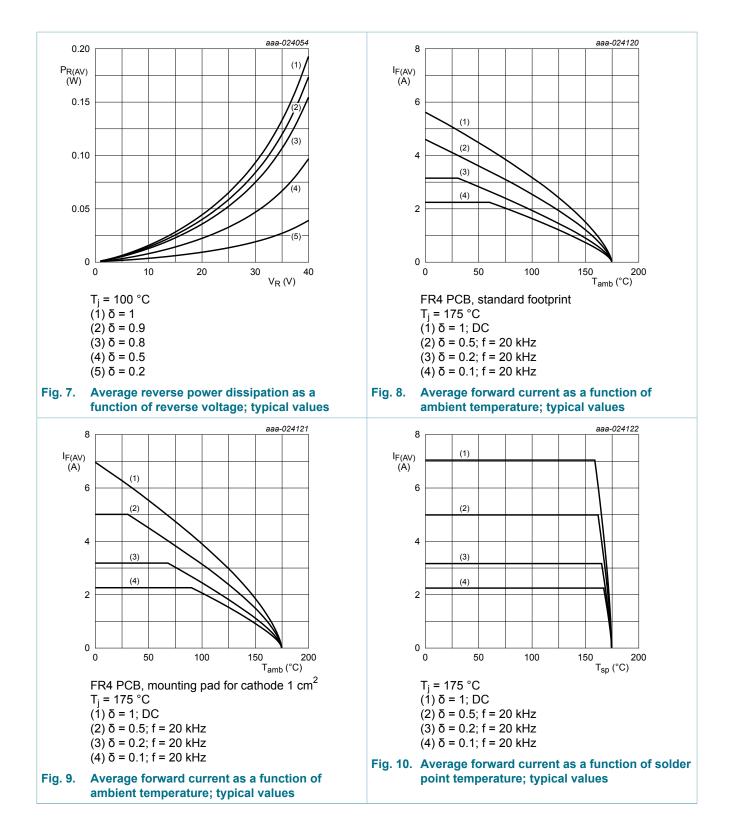
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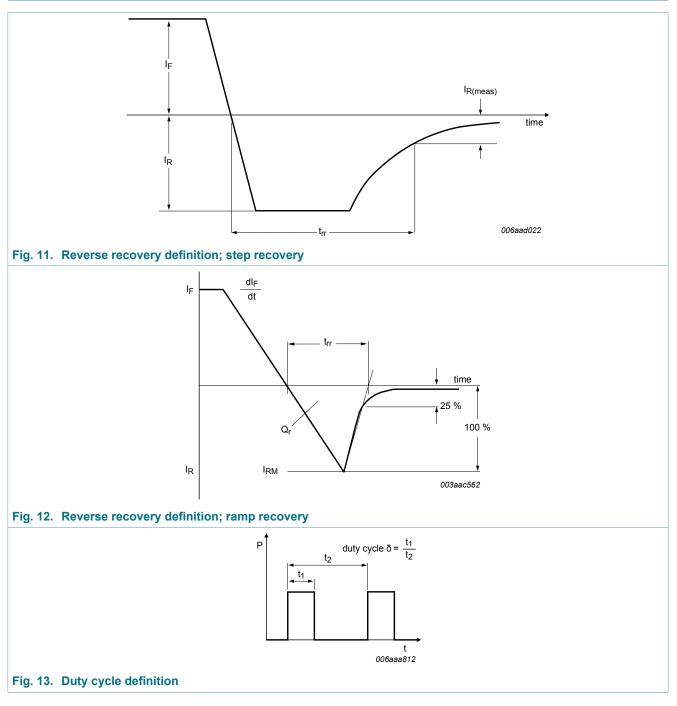
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11. Test information



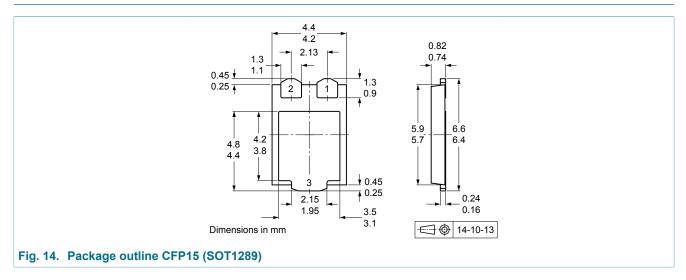
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

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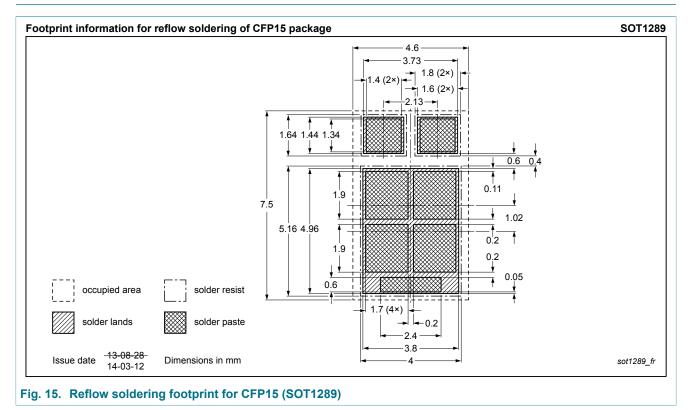
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





40 V, 5 A low VF MEGA Schottky barrier rectifier

14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG040V050EPD v.1	20160726	Product data sheet	-	-

PMEG040V050EPD

40 V, 5 A low VF MEGA Schottky barrier rectifier

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMEG040V050EPD

40 V, 5 A low VF MEGA Schottky barrier rectifier

16. Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	3
10.	Characteristics	5
11.	Test information	8
12.	Package outline	9
13.	Soldering	9
14.	Revision history	.10
15.	Legal information	11

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