1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER AZ34063C

General Description

The AZ34063C is a monolithic switching regulator control circuit which contains the primary functions required for DC-DC converters. This device consists of internal temperature compensated reference, voltage comparator, controlled duty cycle oscillator with active current limit circuit, driver and high current output switch.

The AZ34063C is specifically designed as a general DC-DC converter to be used in Step-Down, Step-Up and Voltage-Inverting applications with a minimum number of external components.

The AZ34063C is available in 2 packages: SOIC-8 and DIP-8.

Features

- Operation from 3.0V to 36V Input
- Low Standby Current
- Current Limiting
- Output Switch Current to1A
- Output Voltage Adjustable
- Operation Frequency up to 180kHz (C_T=100pF)
- Precision 2% Reference

Applications

- Battery Chargers
- ADSL Modems
- Hubs
- Negative Voltage Power Supplies

SOIC-8   DIP-8
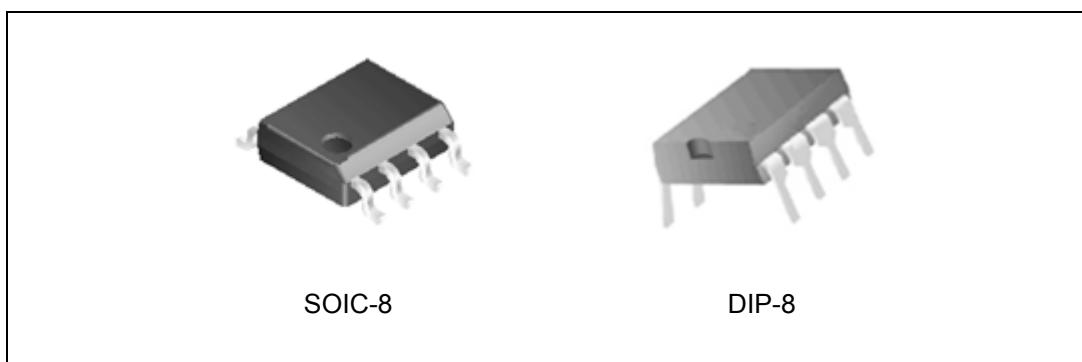
Figure 1. Package Types of AZ34063C

1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER   **AZ34063C**

Pin Configuration

M Package
(SOIC-8)

P Package
(DIP-8)

|			
Switch Collector	1	8	Driver Collector
Switch Emitter	2	7	I_{PK} Sense
Timing Capacitor	3	6	V_{CC}
GND	4	5	Comparator Inverting Input
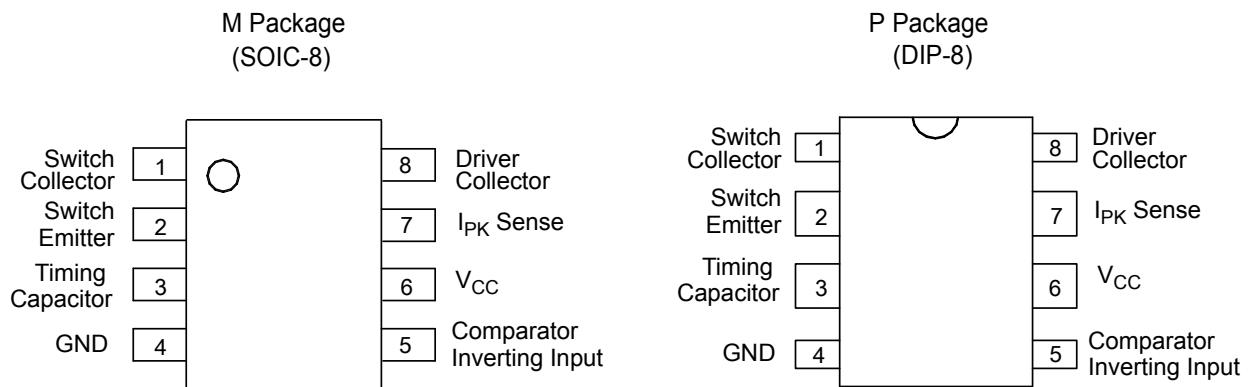
Figure 2. Pin Configuration of AZ34063C (Top View)

Functional Block Diagram
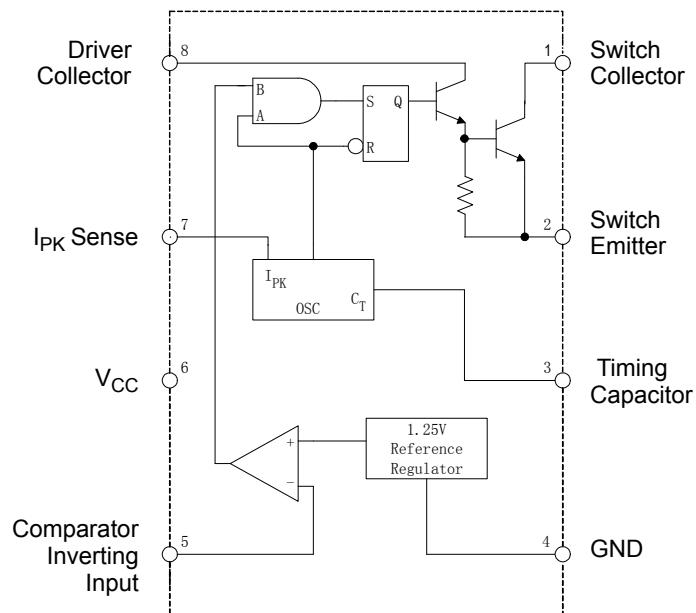
Figure 3. Functional Block Diagram of AZ34063C

**1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER**   **AZ34063C**

Pin Description

Pin Number	Pin Name	Function
1	Switch Collector	Internal switch transistor collector
2	Switch Emitter	Internal switch transistor emitter
3	Timing Capacitor	Timing Capacitor to control the switching frequency
4	GND	Ground pin for all internal circuits
5	Comparator Inverting Input	Inverting input pin for internal comparator
6	V_{CC}	Voltage supply
7	I_{PK} Sense	Peak Current Sense Input by monitoring the voltage drop across an external current sense resistor to limit the peak current through the switch
8	Driver Collector	Voltage driver collector

Ordering Information

AZ34063C ☐ ☐ - ☐

Circuit Type

Package
M: SOIC-8
P: DIP-8

E1: Lead Free
G1: Green

TR: Tape and Reel
Blank: Tube
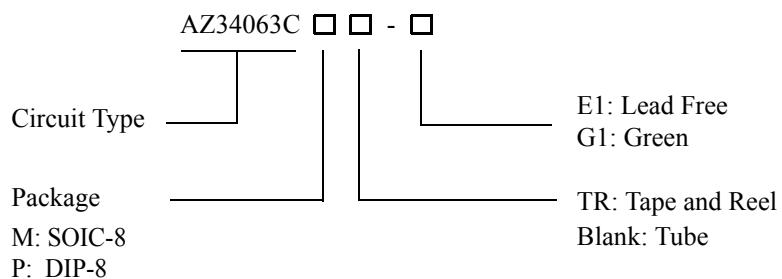
Package	Temperature Range	Part Number		Marking ID		Packing Type
		Lead Free	**Green**	**Lead Free**	**Green**	
SOIC-8	-40 to 85ºC	AZ34063CM-E1	AZ34063CM-G1	34063CM-E1	34063CM-G1	Tube
		AZ34063CMTR-E1	AZ34063CMTR-G1	34063CM-E1	34063CM-G1	Tape & Reel
DIP-8	-40 to 85ºC	AZ34063CP-E1	AZ34063CP-G1	AZ34063CP-E1	AZ34063CP-G1	Tube

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant. Products with "G1" suffix are available in green packages.

1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER **AZ34063C**

Absolute Maximum Ratings (Note 1)

Parameter		Symbol	Value	Unit
Power Supply Voltage		V_{CC}	40	V
Comparator Input Voltage Range		V_{IR}	-0.3 to 40	V
Switch Collector Voltage		V_C(switch)	40	V
Switch Emitter Voltage ($V_{PIN\ 1}$=40V)		V_E(switch)	40	V
Switch Collector to Emitter Voltage		V_{CE}(switch)	40	V
Driver Collector Voltage		V_C(driver)	40	V
Driver Collector Current (Note 2)		I_C(driver)	100	mA
Switch Current		I_{SW}	1	A
Power Dissipation (T_A=25 ℃)	DIP-8	P_D	1.25	W
	SOIC-8		780	mW
Thermal Resistance	DIP-8	$R_{\theta JA}$	100	℃ /W
	SOIC-8		160	
Operating Junction Temperature		T_J	150	℃
Lead Temperature (Soldering, 10s)		T_{LEAD}	260	℃
Storage Temperature Range		T_{STG}	-65 to 150	℃
ESD (Human body model)			2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 2: Maximum package power dissipation limits must be observed.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V_{CC}	3	36	V
Ambient Temperature	T_A	-40	85	℃

1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER · AZ34063C

Electrical Characteristics

(V_{CC}=5.0 V, T_A=-40 to 85°C, unless otherwise specified.)

Parameter	Symbol	Conditions		Min	Typ	Max	Unit
OSCILLATOR							
Frequency	f_{OSC}	V_{PIN5}=0V, T_A=25°C	C_T=1.0nF	30	38	45	KHz
			C_T=330pF	75	88	100	KHz
Charge Current	I_{CHG}	V_{CC}=5.0V to 36 V, T_A=25°C		30	38	45	μA
Discharge Current	I_{DISCHG}	V_{CC}=5.0V to 36V, T_A=25°C		180	240	290	μA
Discharge to Charge Current Ratio	I_{DISCHG}/I_{CHG}	Pin 7 to V_{CC}, T_A=25°C		5.2	6.5	7.5	
Current Limit Sense Voltage	V_{IPK}(sense)	I_{CHG}=I_{DISCHG}, T_A=25°C		250	300	350	mV
OUTPUT SWITCH (Note 3)							
Saturation Voltage, Dalington Connection	V_{CE}(sat)	I_{SW}=1A, Pins 1, 8 connected, Common Emitter			1.0	1.3	V
Saturation Voltage (Note 4)	V_{CE}(sat)	I_{SW}=1A, R_{PIN8}=82 Ω to V_{CC}, Forced β=20, Common Emitter			0.45	0.8	V
DC Current Gain	h_{FE}	I_{SW}=1A, V_{CE}=5.0V, T_A=25°C		50	75		
Collector Off-State Current	I_C(off)	V_{CE}=36V			0.01	100	μA
COMPARATOR							
Threshold Voltage	V_{TH}	T_A=25°C		1.225	1.250	1.275	V
		T_A=-40 to 85°C		1.21	1.250	1.29	
Threshold Voltage Line Regulation	R_{EGLINE}	V_{CC}=3.0V to 36V			1.4	5	mV
Input Bias Current	I_{IB}	V_{IN}=0V			-20	-400	nA
TOTAL DEVICE							
Supply Current	I_{CC}	V_{CC}=5.0V to 36V, C_T=1.0 nF, V_{PIN7}=V_{CC}, V_{PIN5} > V_{TH}, V_{PIN2}=GND, other pins open				4	mA

Note 3: Low duty cycle pulse technique are used during test to maintain junction temperature as close to ambient temperature as possible.

Note 4: If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents (≤ 300mA) and high driver currents (≥ 30mA), it may take up to 2.0us for it to come out of saturation. This condition will shorten the off time at frequencies 30KHz, and is magnified at high temperatures. This condition does not occur with a Darlington configuration, since the output switch cannot saturate. If a non-Darlington configuration is used, the following output drive condition is recommended:

Electrical Characteristics (Continued)

Forced β of output switch: $\dfrac{I_C \text{ output}}{I_C \text{ driver - 7.0mA*}} \geq 10$

* The 100Ω resistor in the emitter of the driver device requires about 7.0 mA before the output switch conducts.

Typical Performance Characteristics
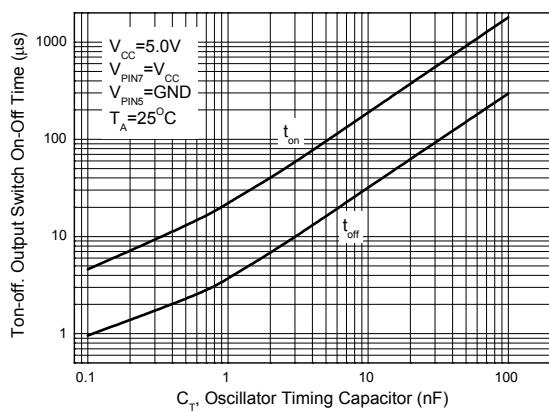
Figure 4. Output Switch On-Off Time vs.
Oscillator Timing Capacitor
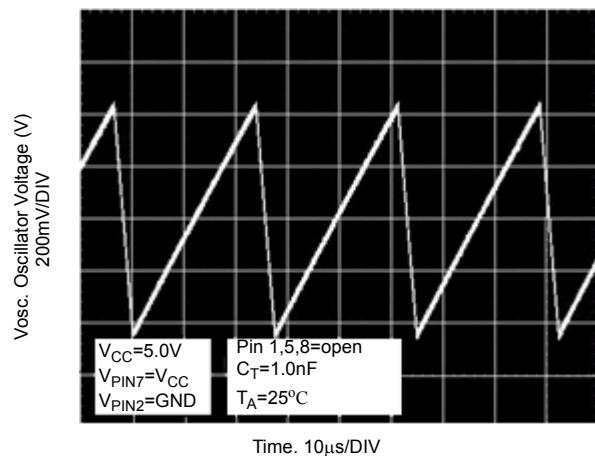
Figure 5. Timing Capacitor Waveform
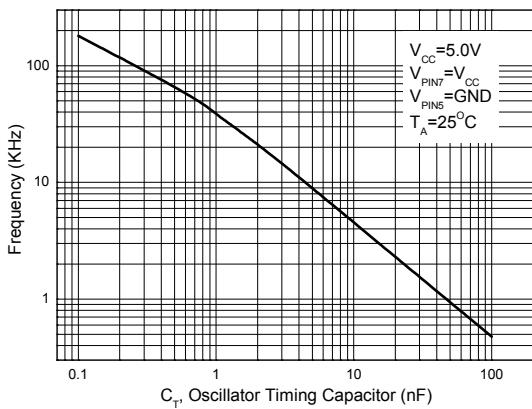

Figure 6. Oscillator Frequency vs.Timing Capacitor
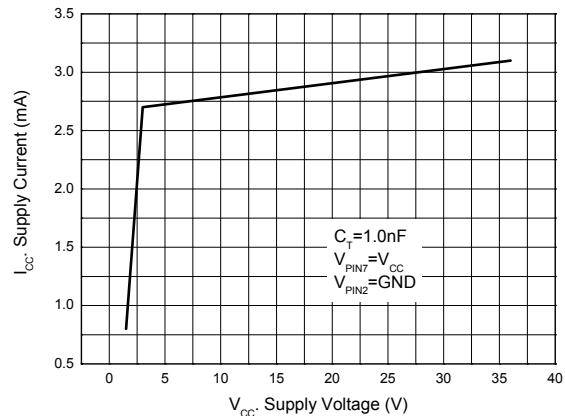
Figure 7. Standby Supply Current vs. Supply Voltage

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Typical Performance Characteristics (Continued)

V_{CC}=5.0V
$V_{PIN1,7,8}$=V_{CC}
$V_{PIN3,5}$=GND
T_A=25°C
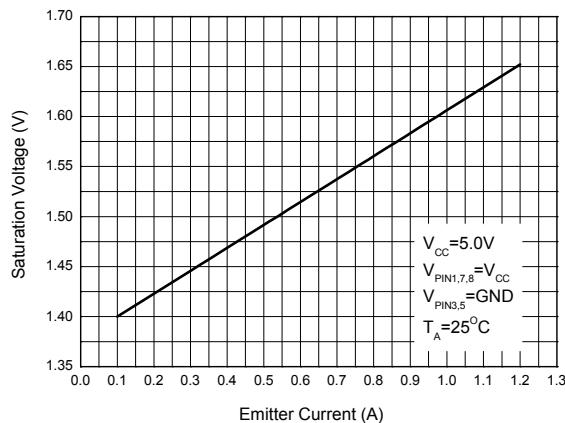
Figure 8. Emitter Follower Configuration Output Saturation Voltage vs. Emitter Current

Darlington Connection

Forced β=20

V_{CC}=5.0V
V_{PIN7}=V_{CC}
$V_{PIN2,3,5}$=GND
T_A=25°C
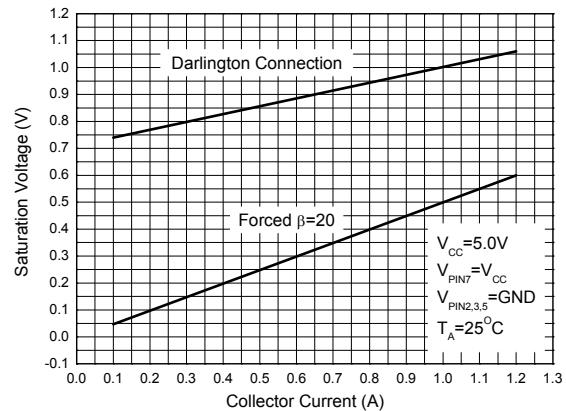
Figure 9. Common Emitter Configuration Output Switch Saturation Voltage vs. Collector Current
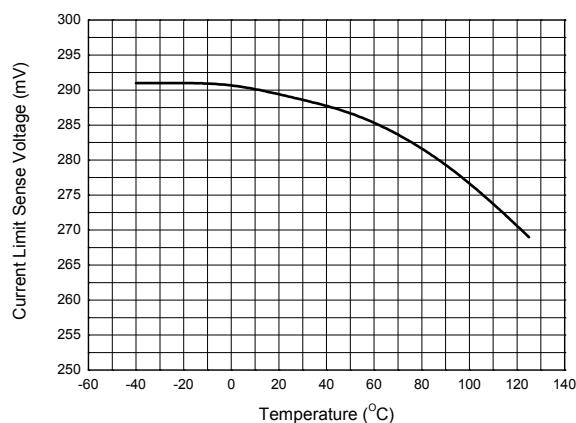
Figure 10. Current Limit Sense Voltage vs. Temperature

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Typical Applications
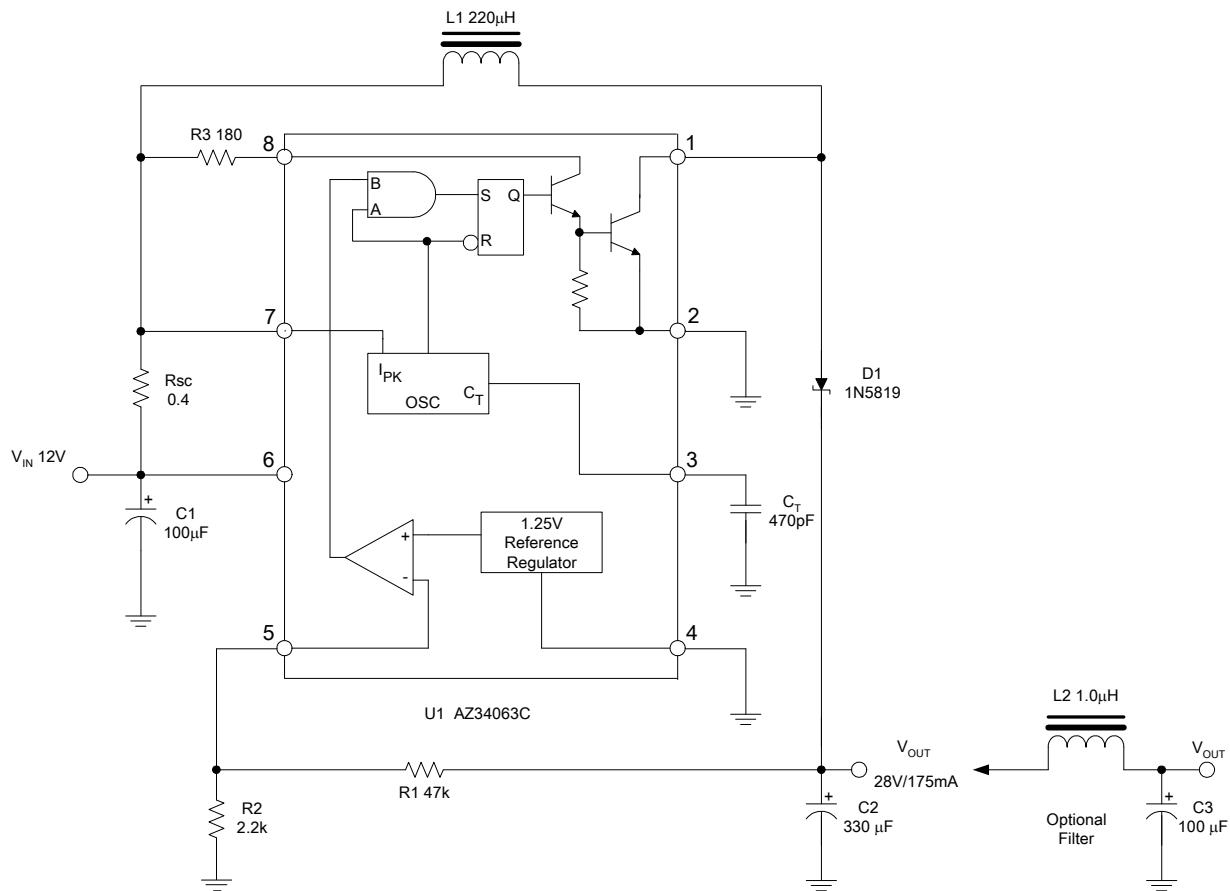
Figure 11. Step-Up Converter (Note 5)

Note 5: This is a typical step-up converter configuration. In the steady state, if the resistor divider voltage at pin 5 is greater than the voltage in the non-inverting input, which is 1.25V determined by the internal reference, the output of the comparator will go low. At the next swithching period, the output switch will not conduct and the output voltage will eventually drop below its nominal voltage until the divider voltage at pin 5 is lower than 1.25V. Then the output of the comparator will go high, the output switch will be allowed to conduct. Since $V_{PIN5} = V_{OUT} * R2/(R1+R2) = 1.25(V)$, the output voltage can be decided by $V_{OUT} = 1.25 * (R1+R2)/R2$ (V).

Typical Applications (Continued)

Figure 12. Step-Down converter (Note 6)

Note 6: This is a typical step-down converter configuration. The working process in the steady state is similar to step-up converter, $V_{PIN5}=V_{OUT}*R2/(R1+R2)=1.25$ (V), the output voltage can be decided by $V_{OUT}=1.25*(R1+R2)/R2$ (V).

1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER   AZ34063C

Typical Applications (Continued)
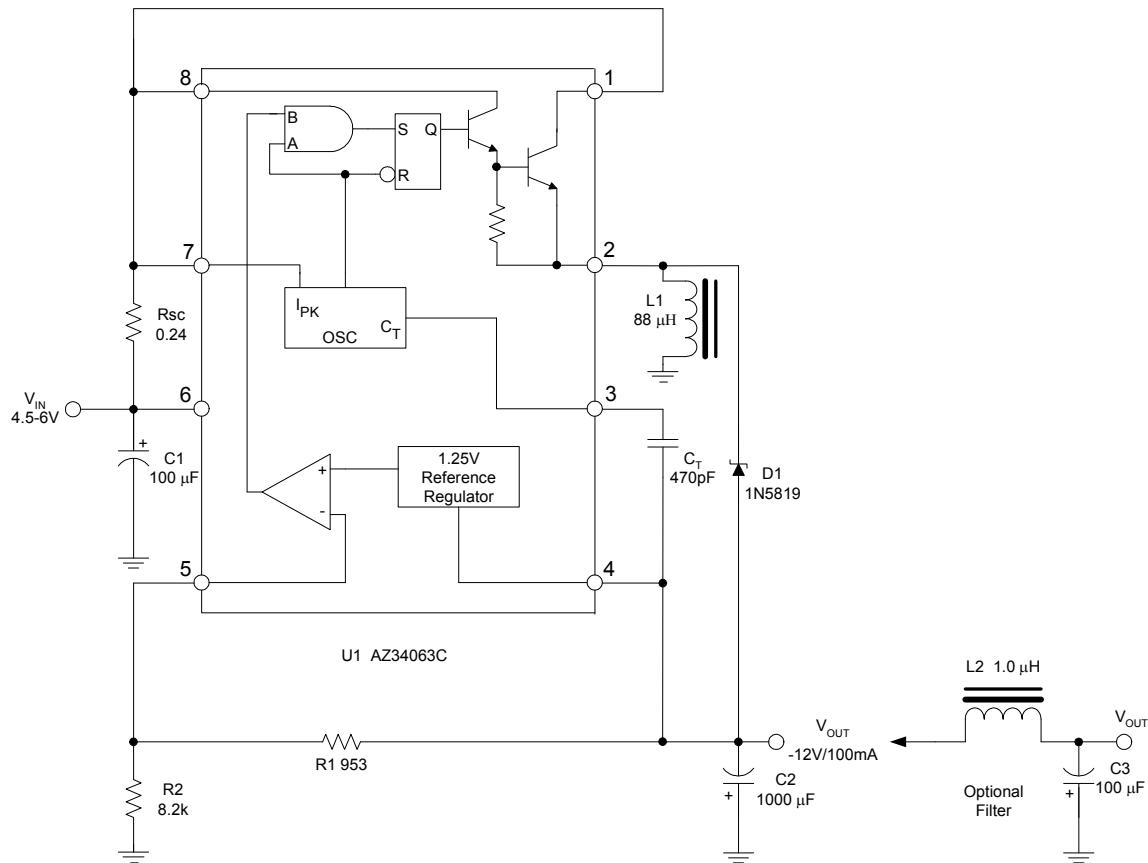
Figure 13. Voltage Inverting Converter (Note 7)

Note 7: This is a typical inverting converter configuration. The working process in the steady state is similar to step-up converter, the difference in this situation is that the voltage at the non-inverting pin of the comparator is equal to $1.25V+V_{OUT}$, then $V_{PIN5}=V_{OUT}*R2/(R1+R2)=1.25V+V_{OUT}$, so the output voltage can be decided by $V_{OUT}=-1.25*(R1+R2)/R1$ (V).

1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER   AZ34063C

Mechanical Dimensions

SOIC-8   **Unit: mm(inch)**

4.700(0.185)
5.100(0.201)

7°

7°

1.270(0.050)
TYP

0.100(0.004)
0.300(0.012)

1.350(0.053)
1.750(0.069)

0.675(0.027)
0.725(0.029)

0.320(0.013)

8°

8°

D

5.800(0.228)
6.200(0.244)

D
20:1

Φ 0.800(0.031)

0.200(0.008)

0°
8°

1.000(0.039)

R0.150(0.006)

3.800(0.150)
4.000(0.157)

0.330(0.013)
0.510(0.020)

0.190(0.007)
0.250(0.010)

0.900(0.035)

1°
5°

R0.150(0.006)

0.450(0.017)
0.800(0.031)
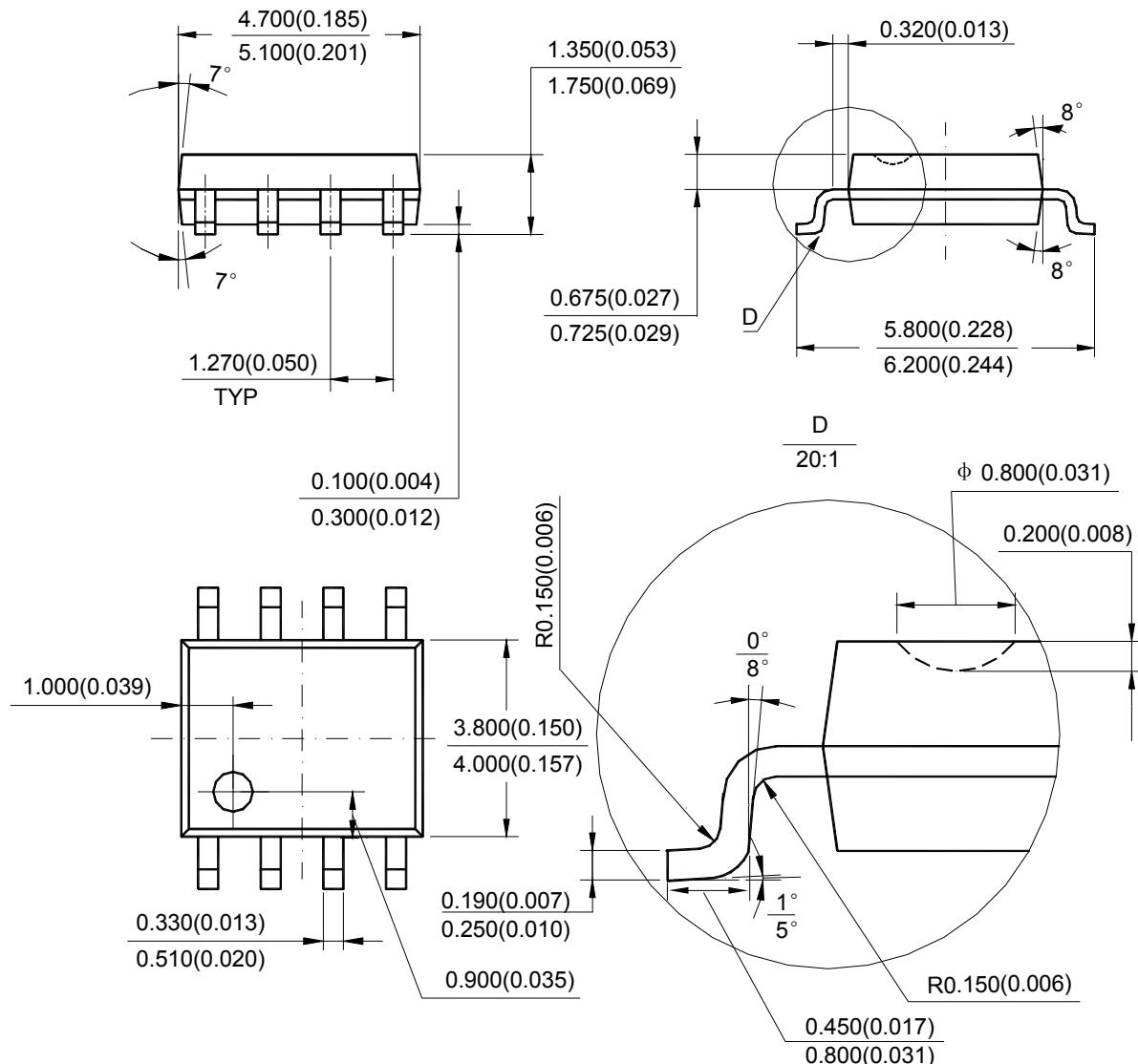
1A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER   **AZ34063C**

Mechanical Dimensions (Continued)

DIP-8   **Unit: mm(inch)**
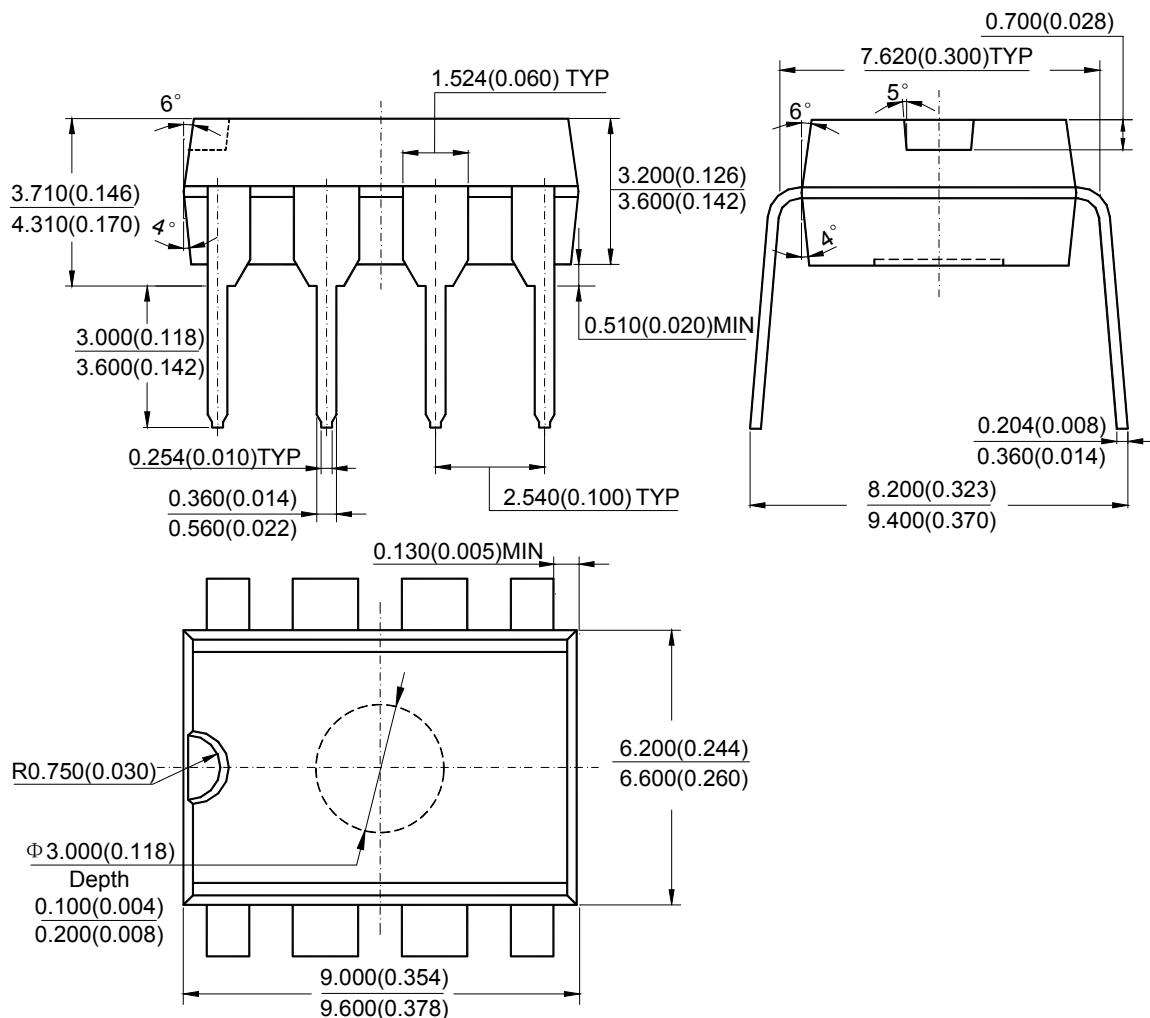
Note: Eject hole, oriented hole and mold mark is optional.

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