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# 65 V, 100 mA NPN general-purpose transistors Rev. 9 — 25 September 2012 P

**Product data sheet** 

#### 1. **Product profile**

### **1.1 General description**

NPN general-purpose transistors in Surface-Mounted Device (SMD) plastic packages.

#### Table 1. **Product overview**

Type number <sup>[1]</sup>	Package	Package		
	NXP	JEITA	JEDEC	
BC846	SOT23	-	TO-236AB	BC856
BC846W	SOT323	SC-70	-	BC856W
BC846T	SOT416	SC-75	-	BC856T

[1] Valid for all available selection groups.

### 1.2 Features and benefits

- General-purpose transistors
- SMD plastic packages
- Two different gain selections

### 1.3 Applications

General-purpose switching and amplification

### 1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	65	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; $I_C$ = 2 mA	110	-	450	
	h <sub>FE</sub> group A		110	180	220	
	h <sub>FE</sub> group B		200	290	450	



65 V, 100 mA NPN general-purpose transistors

## 2. Pinning information

Table 3.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
SOT23, 5	SOT323, SOT416		
1	base		
2	emitter	3	3
3	collector		1
		1 2 006aaa144	2 sym021

# 3. Ordering information

Table 4. Order	ing inform	ation			
Type number <sup>[1]</sup>	Package	e			
	Name	Description	Version		
BC846	-	plastic surface-mounted package; 3 leads	SOT23		
BC846W	SC-70	plastic surface-mounted package; 3 leads	SOT323		
BC846T	SC-75	plastic surface-mounted package; 3 leads	SOT416		

[1] Valid for all available selection groups.

### 4. Marking

Table 5. Marking codes	
Type number	Marking code <sup>[1]</sup>
BC846	1D*
BC846A	1A*
BC846B	1B*
BC846W	1D*
BC846AW	1A*
BC846BW	1B*
BC846T	1M
BC846AT	1A
BC846BT	1B

[1] \* = placeholder for manufacturing site code

#### 65 V, 100 mA NPN general-purpose transistors

### 5. Limiting values

Table 6. In accorda	Limiting values nce with the Absolute Maximur	m Rating System (IEC	C 60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	65	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	6	V
l <sub>C</sub>	collector current		-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	200	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms	-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1]		
	SOT23		-	250	mW
	SOT323		-	200	mW
	SOT416		-	150	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u>			
	SOT23		-	-	500	K/W
	SOT323		-	-	625	K/W
	SOT416		-	-	833	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

65 V, 100 mA NPN general-purpose transistors

### 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}$		-	-	15	nA
	current	$\label{eq:VCB} \begin{array}{l} V_{CB} = 30 \; V; \; I_{E} = 0 \; A; \\ T_{j} = 150 \; ^{\circ}C \end{array}$		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; $I_{C}$ = 10 $\mu$ A					
	h <sub>FE</sub> group A			-	180	-	
	h <sub>FE</sub> group B			-	290	-	
	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}$		110	-	450	
	h <sub>FE</sub> group A			110	180	220	
	h <sub>FE</sub> group B			200	290	450	
CLSat	at collector-emitter saturation voltage	$I_{C} = 10 \text{ mA}; I_{B} = 0.5 \text{ mA}$		-	90	200	mV
		$I_C = 100 \text{ mA}; I_B = 5 \text{ mA}$	<u>[1]</u>	-	200	400	mV
V <sub>BEsat</sub>	base-emitter	$I_{C} = 10 \text{ mA}; I_{B} = 0.5 \text{ mA}$	[2]	-	760	-	mV
	saturation voltage	$I_{C} = 100 \text{ mA}; I_{B} = 5 \text{ mA}$	[2]	-	900	-	mV
V <sub>BE</sub>	base-emitter voltage	$I_C = 2 \text{ mA}; V_{CE} = 5 \text{ V}$	[3]	580	660	700	mV
		$I_C$ = 10 mA; $V_{CE}$ = 5 V	[3]	-	180 290 - 180 290 90 200 760 900 0 660 -	770	mV
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA};$ f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB} = 10 \text{ V};  \text{I}_{E} = \text{i}_{e} = 0 \text{ A}; \\ \text{f} = 1 \text{ MHz} \end{array}$		-	2	3	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = i_{c} = 0 \text{ A};$ f = 1 MHz		-	11	-	pF
NF	noise figure	$I_C = 200 \ \mu$ A; V <sub>CE</sub> = 5 V; R <sub>S</sub> = 2 kΩ; f = 1 kHz; B = 200 Hz		-	2	10	dB

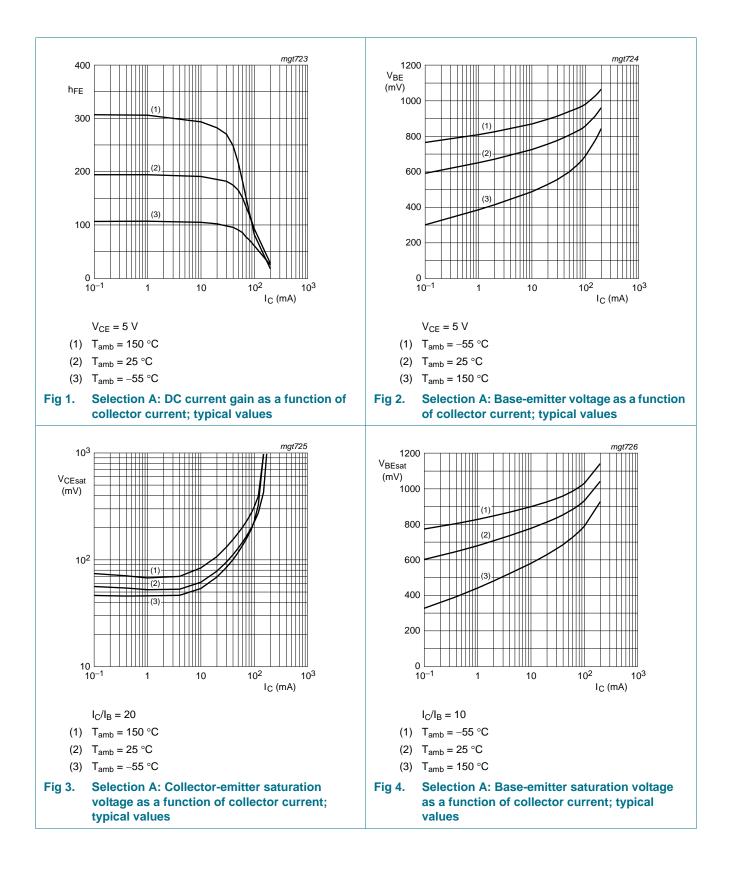
[2] V<sub>BEsat</sub> decreases by approximately 1.7 mV/K with increasing temperature.

[3] V<sub>BE</sub> decreases by approximately 2 mV/K with increasing temperature.

### **NXP Semiconductors**

# **BC846** series

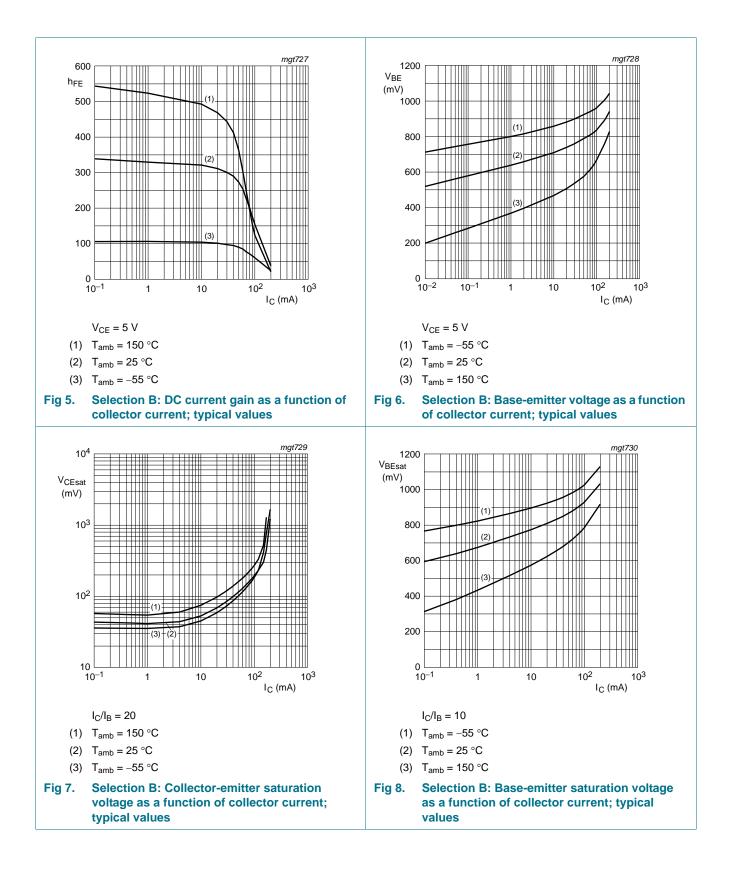
#### 65 V, 100 mA NPN general-purpose transistors



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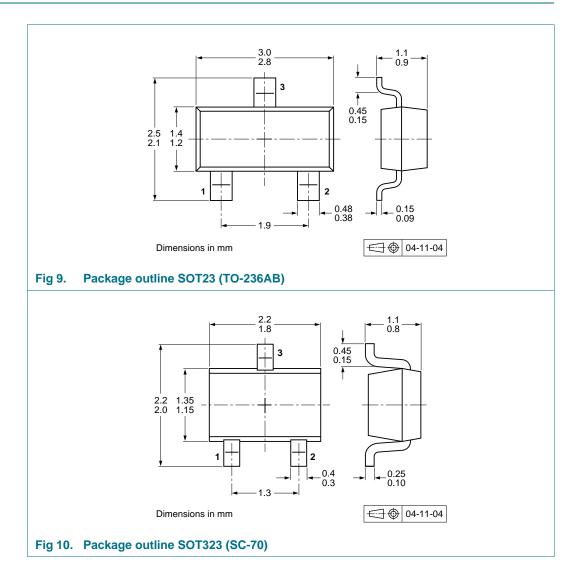
# **BC846** series

#### 65 V, 100 mA NPN general-purpose transistors



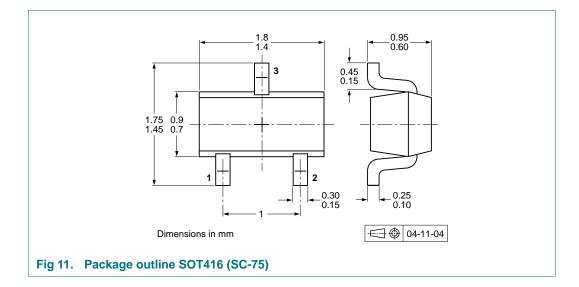
65 V, 100 mA NPN general-purpose transistors

## 8. Package outline



BC846\_SER Product data sheet

65 V, 100 mA NPN general-purpose transistors



### 9. Packing information

#### Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

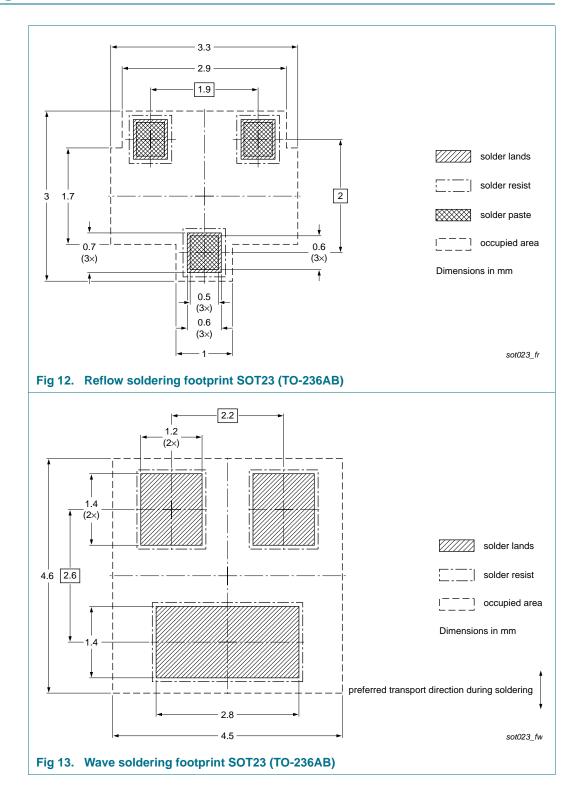
Туре	Package	ackage Description F		Packing quantity		
number <sup>[2]</sup>			1000	3000	4000	
BC846	SOT23	4 mm pitch, 8 mm tape and reel	-215	-	-235	
BC846W	SOT323	4 mm pitch, 8 mm tape and reel	-115	-	-135	
BC846T	SOT416	4 mm pitch, 8 mm tape and reel	-115	-	-135	

[1] For further information and the availability of packing methods, see <u>Section 13</u>.

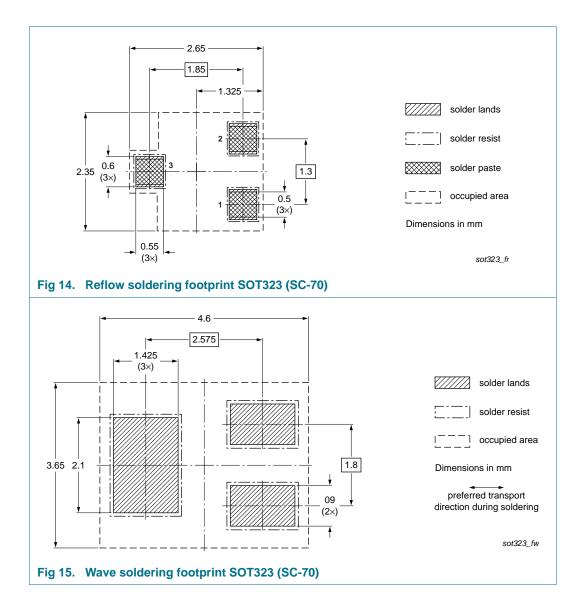
[2] Valid for all available selection groups.

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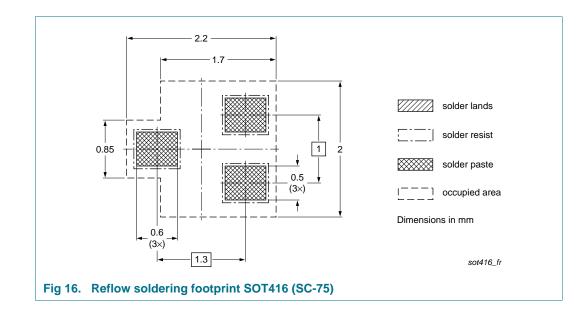
### **10. Soldering**



#### 65 V, 100 mA NPN general-purpose transistors



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## **11. Revision history**

Table 10.         Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BC846_SER v.9	20120925	Product data sheet	-	BC846_SER v.8
Modifications:	<ul> <li>Table 6 "Lir</li> </ul>	niting values": P <sub>tot</sub> values co	orrected	
BC846_SER v.8	20120424	Product data sheet		BC846_BC546_SER v.7
BC846_BC546_SER v.7	20091117	Product data sheet	-	BC846_BC546_SER v.6
BC846_BC546_SER v.6	20060207	Product data sheet	-	-

65 V, 100 mA NPN general-purpose transistors

### 12. Legal information

#### **12.1 Data sheet status**

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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#### 65 V, 100 mA NPN general-purpose transistors

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#### 65 V, 100 mA NPN general-purpose transistors

### 14. Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	2
3	Ordering information	2
4	Marking	2
5	Limiting values	3
6	Thermal characteristics	3
7	Characteristics	4
8	Package outline	7
9	Packing information	8
10	Soldering	9
11	Revision history	12
12	Legal information	13
12.1	Data sheet status	13
12.2	Definitions	13
12.3	Disclaimers	13
12.4	Trademarks	14
13	Contact information	14
14	Contents	15

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