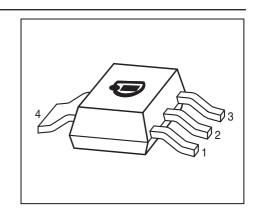


PNP Silicon AF Transistor

- For general AF applications
- High collector current
- High current gain
- Low collector-emitter saturation voltage
- Complementary type: BCP68 (NPN)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Pin Configuration					Package	
BCP69-25	25*	1=B	2=C	3=E	4=C	-	-	SOT223

^{*} Marking is the same as type-name

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	20	V
Collector-emitter voltage	V_{CES}	25	
Collector-base voltage	V_{CBO}	25	
Emitter-base voltage	V_{EBO}	5	
Collector current	I _C	1	A
Peak collector current, $t_p \le 10 \text{ ms}$	I _{CM}	2	
Base current	I_{B}	100	mA
Peak base current	I _{BM}	200	
Total power dissipation-	P _{tot}	3	W
<i>T</i> _S ≤ 114 °C			
Junction temperature	T_{i}	150	°C
Storage temperature	T _{stg}	-65 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 12	K/W

1



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	1
DC Characteristics	•		•	•	
Collector-emitter breakdown voltage	V _{(BR)CEO}	20	-	-	V
$I_{\rm C}$ = 30 mA, $I_{\rm B}$ = 0					
Collector-base breakdown voltage	V _{(BR)CBO}	25	-	-	
$I_{\rm C}$ = 10 μ A, $I_{\rm E}$ = 0					
Collector-emitter breakdown voltage	V _{(BR)CES}	25	-	-	
$I_{\rm C}$ = 10 μ A, $V_{\rm BE}$ = 0					
Emitter-base breakdown voltage	$V_{(BR)EBO}$	5	-	-	
$I_{\rm E}$ = 10 μ A, $I_{\rm C}$ = 0					
Collector-base cutoff current	I _{CBO}				μA
$V_{\text{CB}} = 25 \text{ V}, I_{\text{E}} = 0$		-	-	0.1	
V_{CB} = 25 V, I_{E} = 0 , T_{A} = 150 °C		-	-	100	
DC current gain ²⁾	h _{FE}				-
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 10 V		50	-	-	
$I_{\rm C}$ = 500 mA, $V_{\rm CE}$ = 1 V, BCP69-16		100	160	250	
$I_{\rm C}$ = 500 mA, $V_{\rm CE}$ = 1 V, BCP69-25		160	250	375	
$I_{\rm C}$ = 1 A, $V_{\rm CE}$ = 1 V		60	-	-	
Collector-emitter saturation voltage ²⁾	V _{CEsat}	-	-	0.5	V
$I_{\rm C}$ = 1 A, $I_{\rm B}$ = 100 mA					
Base-emitter voltage ²⁾	V _{BE(ON)}				
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 10 V		-	0.6	-	
I _C = 1 A, V _{CE} = 1 V		-	-	1	
AC Characteristics					
Transition frequency	f_{T}	-	100	-	MHz
$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5 V, f = 100 MHz					

 $^{^{1}}$ For calculation of R_{thJA} please refer to Application Note AN077 (Thermal Resistance Calculation)

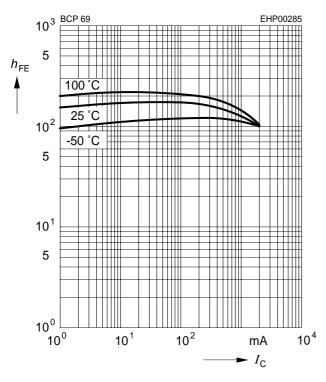
2

 $^{^2}$ Pulse test: t < 300 μ s; D < 2%



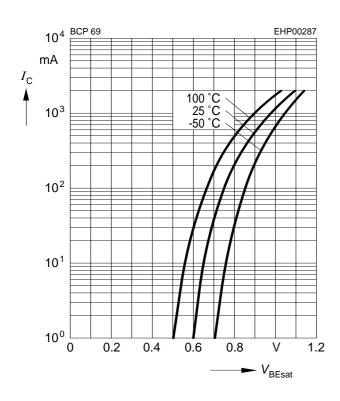
DC current gain $h_{FE} = f(I_C)$

$$V_{CE} = 1 \text{ V}$$



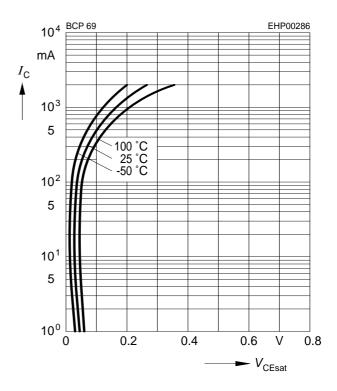
Base-emitter saturation voltage

$$I_{\text{C}} = f(V_{\text{BEsat}}), h_{\text{FE}} = 10$$



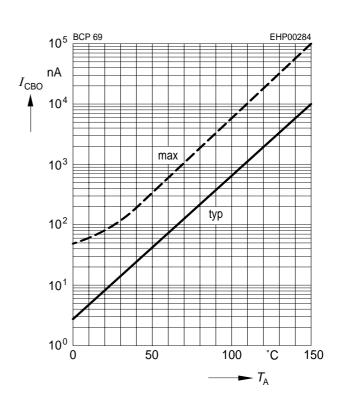
Collector-emitter saturation voltage

$$I_{\text{C}} = f(V_{\text{CEsat}}), h_{\text{FE}} = 10$$



Collector cutoff current $I_{CBO} = f(T_A)$

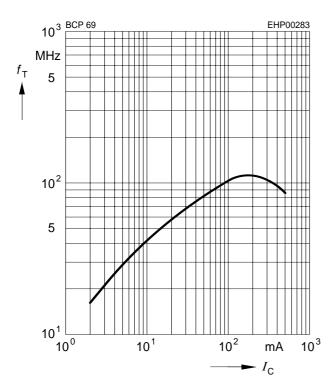
$$V_{\rm CBO}$$
 = 25 V



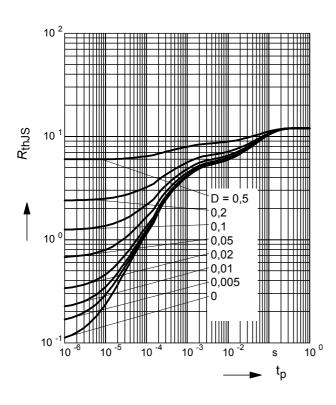


Transition frequency $f_T = f(I_C)$

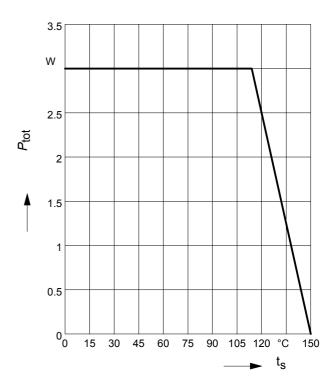
 V_{CE} = 5 V



Permissible Pulse Load $R_{thJS} = f(t_p)$

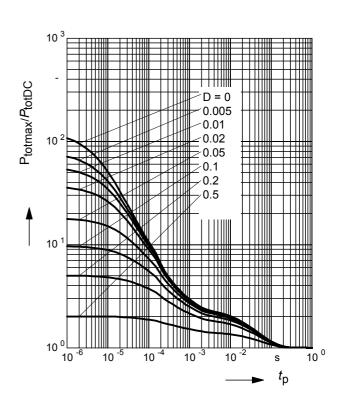


Total power dissipation $P_{tot} = f(T_S)$



Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$





Package Outline 1.6±0.1 6.5 ± 0.2 0.1 MAX 3±0.1 MAX. $\tilde{\Omega}$ 3.5 ± 0.2 7±0.3 2 2.3 0.7±0.1 0.28 ±0.04 4.6 0...10° ⊕ 0.25 M A = 0.25 M B Foot Print 3.5 1.2 1.1 Marking Layout (Example) **(**infineon Manufacturer 2005, 24 CW Date code (YYWW) 0524 16 BCP52-16 Type code Pin 1 Packing Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel 0.3 MAX. \oplus 7.55

6.8

5

1.75



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6

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