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Kind regards,

Team Nexperia

INTEGRATED CIRCUITS

DATA SHEET

74F06Inverter/buffer drivers

Product data Supersedes data of 1992 Jul 24





Hex inverter/buffer drivers (open-collector)

74F06

FEATURES

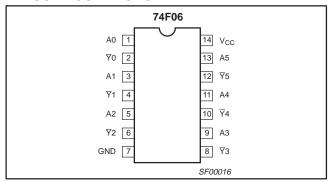
- Open Collector output drive 64mA
- High speed
- 12V output termination voltage

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F06	3.5ns	30mA

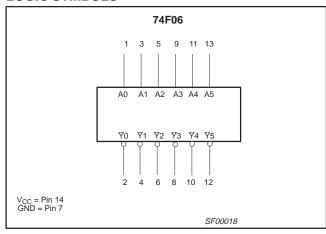
ORDERING INFORMATION

DESCRIPTION	TYPE NUMBER	PKG DWG #
14-pin plastic dual in-line package	N74F06N	SOT27-1
14-pin plastic small outline package	N74F06D	SOT108-1

PIN CONFIGURATIONS



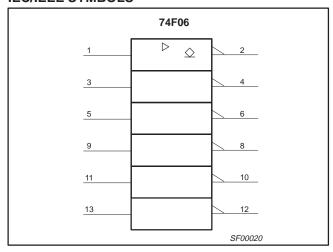
LOGIC SYMBOLS



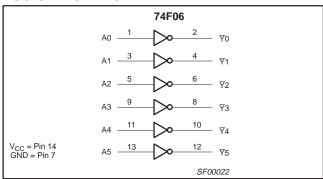
Hex inverter/buffer drivers (open-collector)

74F06

IEC/IEEE SYMBOLS



LOGIC DIAGRAMS



INPUT AND OUTPUT LOADING AND FAN OUT TABLE

PINS	DESCRIPTION	74F (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
An	Data inputs	1.0/1.0	20μA/0.6mA
₹n	Data outputs	OC/106.7	OC/64mA

- One (1.0) FAST unit load is defined as: 20μA in the High state and 0.6mA in the Low state.
 OC = Open Collector

FUNCTION TABLE

INPUTS	OUTPUTS
An	Ϋ́n
L	Н
Н	L

NOTES:

- H = High voltage level
 L = Low voltage level

Hex inverter/buffer drivers (open-collector)

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ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limit set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	-0.5 to +7.0	V
V _{IN}	Input voltage	-0.5 to +7.0	V
I _{IN}	Input current	−30 to +5	mA
V _{OUT}	Voltage applied to output in High output state	-0.5 to 12	V
l _{OUT}	Current applied to output in Low output state	128	mA
T _{amb}	Operating free air temperature range	0 to +70	°C
T _{stg}	Storage temperature range	-65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER		UNIT		
		MIN	NOM	MAX	1
V _{CC}	Supply voltage	4.5	5.0	5.5	V
V_{IH}	High-level input voltage	2.0			V
V_{IL}	Low-level input voltage			0.8	V
I _{lk}	Input clamp current			-18	mA
V _{OH}	High-level output voltage			12	V
I _{OL}	Low-level output current			64	mA
T _{amb}	Operating free air temperature range	0		+70	°C

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DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER		TEST	TEST CONDITIONS ¹			LIMITS		UNIT
			MIN	TYP ²	MAX				
I _{OH}	High-level output current	$V_{CC} = MIN, V_{IL} = V_{OH} = MAX, V_{IH}$	$V_{CC} = MIN, V_{IL} = MAX,$ $V_{OH} = MAX, V_{IH} = MIN$				250	μА	
V _{OL}	Low-level output voltage	$V_{CC} = MIN,$ $V_{IL} = MAX,$				0.30	0.50	V	
			$V_{IH} = MIN$	$V_{IH} = MIN$ $\pm 5\% V_{CC}$			0.30	0.50	V
V _{IK}	Input clamp voltage	$V_{CC} = MIN, I_I = I_I$	$V_{CC} = MIN, I_I = I_{IK}$			-0.73	-1.2	V	
I _I	Input current at maximum i voltage	nput	$V_{CC} = MAX, V_I =$	7.0V				100	μА
lін	High-level input current		V _{CC} = MAX, V _I =	2.7V				20	μΑ
I _{IL}	Low-level input current		$V_{CC} = MAX, V_I = 0.5V$					-0.6	mA
I _{CC}	Supply current (total)	I _{CCH}	V _{CC} = MAX	$V_{CC} = MAX$			5.0	8.0	mA
		I _{CCL}	1				30	43	mA

NOTES:

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
 All typical values are at V_{CC} = 5V, T_{amb} = 25°C.

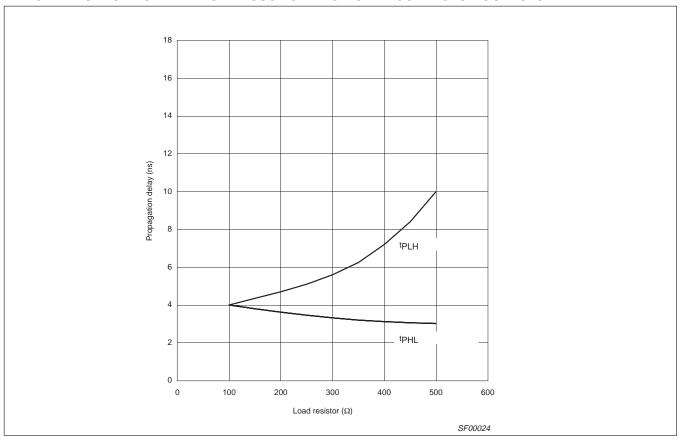
AC ELECTRICAL CHARACTERISTICS

			LIMITS					
SYMBOL	PARAMETER	TEST CONDITION	T,	/ _{CC} = +5.0\ _{amb} = +25° 50pF, R _L =	С	V _{CC} = +5. T _{amb} = 0°C C _L = 50pF,	c to +70°C	UNIT
			Min	Тур	Max	Min	Max	
t _{PLH} t _{PHL}	Propagation delay An to Yn	Waveform 1	2.0 1.5	3.5 3.0	6.0 5.5	1.5 1.0	6.5 6.0	ns

Hex inverter/buffer drivers (open-collector)

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TYPICAL PROPAGATION DELAYS VERSUS LOAD FOR OPEN COLLECTOR OUTPUTS



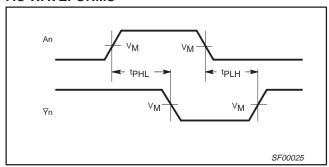
NOTE:

When using Open-Collector parts, the value of the pull-up resistor greatly affects the value of the t_{PLH} . For example, changing the specified pull-up resistor value from 500Ω to 100Ω will improve the t_{PLH} up to 50% with only a slight increase in the t_{PHL} . However, if the value of the pull-up resistor is changed, the user must make certain that the total t_{OL} current through the resistor and the total t_{IL} 's of the receivers does not exceed the t_{OL} maximum specification.

Hex inverter/buffer drivers (open-collector)

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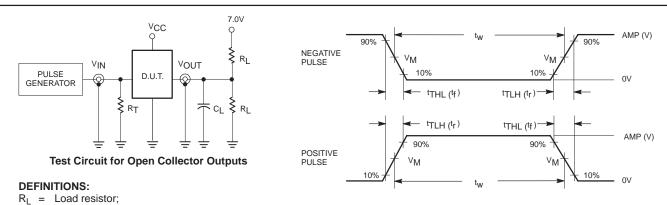
AC WAVEFORMS



Waveform 1. Propagation delay for inverting outputs

For all waveforms, $V_M = 1.5V$.

TEST CIRCUIT AND WAVEFORMS



see AC electrical characteristics for value.

Load capacitance includes jig and probe capacitance; see AC electrical characteristics for value.

Termination resistance should be equal to $Z_{\mbox{\scriptsize OUT}}$ of pulse generators.

Input Pulse Definition

family	INP	UT PU	LSE REQU	REMEN	TS	
family	amplitude	V_{M}	rep. rate	t _w	t _{TLH}	t _{THL}
74F	3.0V	1.5V	1MHz	500ns	2.5ns	2.5ns

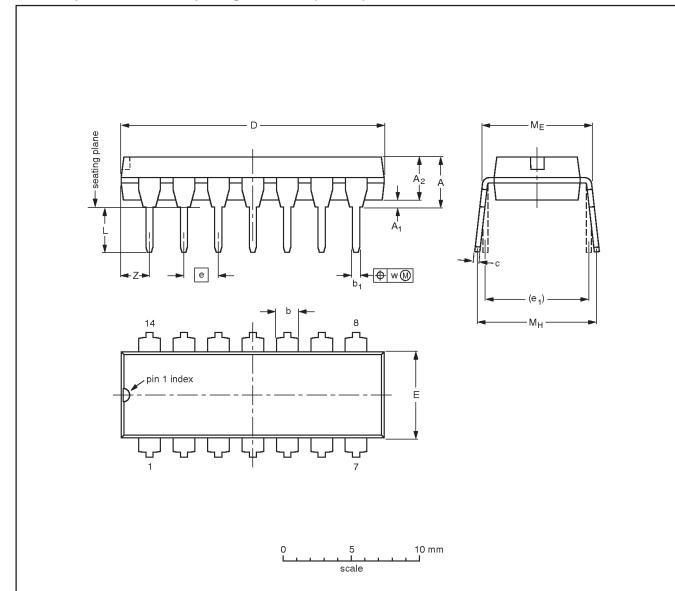
SF00027

Hex inverter/buffer drivers (open-collector)

74F06

DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	С	D ⁽¹⁾	E (1)	е	e ₁	L	ME	Мн	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.13	0.53 0.38	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.2
inches	0.17	0.02	0.13	0.068 0.044	0.021 0.015	0.014 0.009	0.77 0.73	0.26 0.24	0.1	0.3	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.087

Note

1. Plastic or metal protrusions of 0.25 mm (0.01 inch) maximum per side are not included.

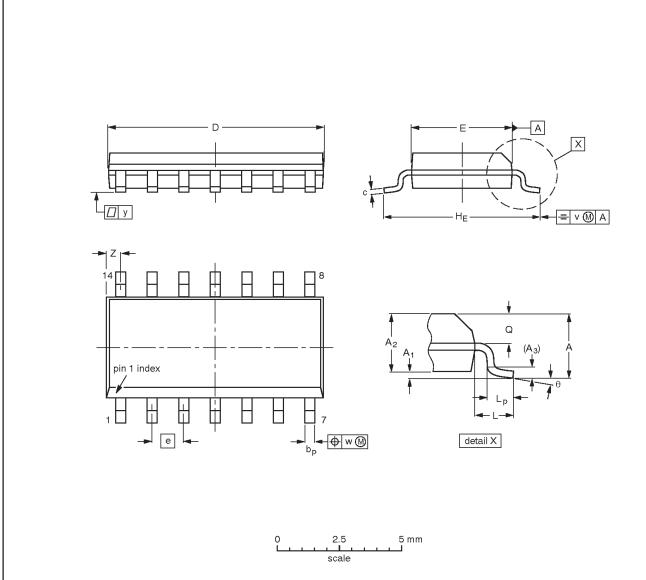
OUTLINE		REFEF	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT27-1	050G04	MO-001	SC-501-14		99-12-27 03-02-13

Hex inverter/buffer drivers (open-collector)

74F06

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.35 0.34	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ICCUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT108-1	076E06	MS-012				99-12-27 03-02-19	

Hex inverter/buffer drivers (open-collector)

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REVISION HISTORY

Rev	Date	Description
_3	20040312	Product data (9397 750 13034); supersedes data sheet 74F06_A_7_A_2 of 1992 Jul 24 (9397 750 05054). Modifications: Delete all references to 74F06A and 74F07A (product discontinued).
		Separate 74F06 and 74F07 into standalone data sheets.
_2	19920724	Product data (9397 750 05054); supersedes previous version.

Hex inverter/buffer drivers (open-collector)

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Data sheet status

Level	Data sheet status [1]	Product status ^{[2] [3]}	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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