C0G Dielectric, 10 – 200 VDC (Commercial Grade)



Overview

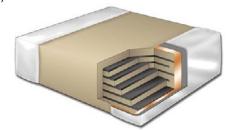
KEMET's COG dielectric features a 125°C maximum operating temperature and is considered "stable." The Electronics Components, Assemblies & Materials Association (EIA) characterizes COG dielectric as a Class I material. Components of this classification are temperature compensating and are suited for resonant circuit applications or those where Q and

stability of capacitance characteristics are required. COG exhibits no change in capacitance with respect to time and voltage and boasts a negligible change in capacitance with reference to ambient temperature. Capacitance change is limited to ±30 ppm/°C from -55°C to +125°C.

Benefits

- -55°C to +125°C operating temperature range
- RoHS Compliant
- EIA 0201, 0402, 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, and 2225 case sizes
- DC voltage ratings of 10 V, 16 V, 25 V, 50 V, 100 V, and 200 V
- Capacitance offerings ranging from 0.5 pF up to 0.47 μF
- Available capacitance tolerances of ±0.10 pF, ±0.25 pF, ±0.5 pF, ±1%, ±2%, ±5%, ±10%, and ±20%
- · No piezoelectric noise
- Extremely low ESR and ESL
- · High thermal stability
- · High ripple current capability
- Preferred capacitance solution at line frequencies and into the MHz range

- · No capacitance change with respect to applied rated DC voltage
- Negligible capacitance change with respect to temperature from -55°C to +125°C
- · No capacitance decay with time
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability
- SnPb plated termination finish option available upon request (5% minimum)



Ordering Information

С	1206	С	104	J	3	G	Α	С	TU
Ceramic	Case Size (L" x W")	Specification/ Series ¹	Capacitance Code (pF)	Capacitance Tolerance ²	Voltage	Dielectric	Failure Rate/ Design	Termination Finish ³	Packaging/Grade (C-Spec) ⁴
	0201 0402 0603 0805 1206 1210 1808 1812 1825 2220 2225	C = Standard	2 significant digits + number of zeros. Use 9 for 1.0 – 9.9 pF Use 8 for 0.5 – .99 pF e.g., 2.2 pF = 229 e.g., 0.5 pF = 508	B = ±0.10 pF C = ±0.25 pF D = ±0.5 pF F = ±1% G = ±2% J = ±5% K = ±10% M = ±20%	8 = 10 V 4 = 16 V 3 = 25 V 5 = 50 V 1 = 100 V 2 = 200 V	G = C0G	A = N/A	C = 100% Matte Sn	Blank = Bulk TU = 7" Reel Unmarked

¹ Flexible termination option is available. Please see FT-CAP product bulletin C1062_C0G_FT-CAP_SMD

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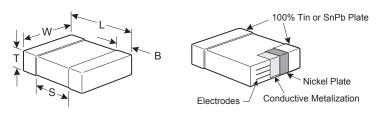
² Additional capacitance tolerance offerings may be available. Contact KEMET for details.

³ Additional termination finish options may be available. Contact KEMET for details.

⁴ Additional reeling or packaging options may be available. Contact KEMET for details.



Dimensions – Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0201	0603	0.60 (.024) ± 0.03 (.001)	0.30 (.012) ± 0.03 (.001)		0.15 (.006) ± 0.05 (.002)	N/A	Colder Deflow Only
0402	1005	1.00 (.040) ± 0.05 (.002)	0.50 (.020) ± 0.05 (.002)		0.30 (.012) ± 0.10 (.004)	0.30 (.012)	Solder Reflow Only
0603	1608	1.60 (.063) ± 0.15 (.006)	0.80 (.032) ± 0.15 (.006)		0.35 (.014) ± 0.15 (.006)	0.70 (.028)	
0805	2012	2.00 (.079) ± 0.20 (.008)	1.25 (.049) ± 0.20 (.008)		0.50 (0.02) ± 0.25 (.010)	0.75 (.030)	Solder Wave or Solder Reflow
1206	3216	3.20 (.126) ± 0.20 (.008)	1.60 (.063) ± 0.20 (.008)		0.50 (0.02) ± 0.25 (.010)		
1210	3225	3.20 (.126) ± 0.20 (.008)	2.50 (.098) ± 0.20 (.008)	See Table 2 for Thickness	0.50 (0.02) ± 0.25 (.010)		
1808	4520	4.70 (.185) ± 0.50 (.020)	2.00 (.079) ± 0.20 (.008)		0.60 (.024) ± 0.35 (.014)		
1812	4532	4.50 (.177) ± 0.30 (.012)	3.20 (.126) ± 0.30 (.012)		0.60 (.024) ± 0.35 (.014)	N/A	Colder Deflow Only
1825	4564	4.50 (.177) ± 0.30 (.012)	6.40 (.252) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		Solder Reflow Only
2220	5650	5.70 (.224) ± 0.40 (.016)	5.00 (.197) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		
2225	5664	5.60 (.220) ± 0.40 (.016)	6.40 (.248) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		

Applications

Typical applications include critical timing, tuning, circuits requiring low loss, circuits with pulse, high current, decoupling, bypass, filtering, transient voltage suppression, blocking and energy storage.

Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance and Reliability.



Environmental Compliance

RoHS Compliant.



Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	±30 ppm/°C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0%
Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5 ±1 seconds and charge/discharge not exceeding 50 mA)
Dissipation Factor (DF) Maximum Limit @ 25°C	0.1%
Insulation Resistance (IR) Limit @ 25°C	1,000 megohm microfarads or 100 G Ω (Rated voltage applied for 120 ±5 seconds @ 25°C)

To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to $G\Omega$ limit. Select the lower of the two limits. Capacitance and Dissipation Factor (DF) measured under the following conditions:

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

Post Environmental Limits

	High Temperatu	ıre Life, Biased	Humidity, Mois	ture Resistance	
Dielectric	Rated DC Voltage	Capacitance Value	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance
C0G	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit

¹ MHz ±100 kHz and 1.0 Vrms ±0.2 V if capacitance ≤ 1,000 pF

¹ kHz ±50 Hz and 1.0 Vrms ±0.2 V if capacitance > 1,000 pF



Table 1A – Capacitance Range/Selection Waterfall (0201 – 1206 Case Sizes)

		Case Size / Series		C020	1C		C	040)2C				(C 06	030	-				C08	3050	С			(C12	060	;	
Capacitance	Сар	Voltage Code	- 1	3 4	3	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2
	Code	Rated Voltage (VD	c) {	2 6	52	9	9	52	20	9	200	10	9	52	20	100	200	9	9	25	20	9	200	9	9	25	20	100	200
		Capacitance Tolerance	,						Pr	rodi	uct			bilit or C		nd (Chip					ode							
0.50 & 0.75 pF	508 & 758	BCD				ВВ			ВВ			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC	DC	DC							
1.0 - 9.1 pF*	109 - 919*	B C D F G J K		D1 A D1	A D1	BB BB			BB			CB	CB	CB CB	CB CB	CB CB	CB CB	DC DC	DC	DC				EB EB	EB EB	EB EB	EB EB	EB EB	EB EB
10 pF 11 pF	100 110	F G J K		B¹ AB¹	AB.	BB			BB BB			CB CB	CB CB	CB	CB	CB	CB	DC	DC DC	DC DC				EB	EB	EB	EB	EB	EB
12 pF	120	F G J K		B² AB²	AB ²	BB			BB			CB	CB	CB	CB	CB	CB	DC	DC	DC				EB	EB	EB	EB	EB	EB
13 pF	130	FGJK				ВВ			ВВ			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		_		EB	EB	EB	EB	EB	EB
15 pF	150	F G J K		B² AB²	AB ²	ВВ			ВВ			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		1		EB	EB	EB	EB	EB	EB
16 pF	160	F G J K		.		BB			BB			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		1		EB	EB	EB	EB	EB	EB
18 pF	180	F G J K		B² AB²	AB²	BB			BB			CB	CB	CB	CB	CB	CB	DC	DC	DC		1		EB	EB	EB	EB	EB	EB
20 pF 22 pF	200 220	F G J K		B² AB²	ΔR2	BB BB		_	BB BB			CB CB	CB CB	CB CB	CB CB	CB CB	CB CB	DC DE	DC DE	DC DE	_	DC	_	EB EB	EB EB	EB EB	EB EB	EB EB	EB EB
24 pF	240	F G J K		7.0	/ (5	BB			BB			CB	CB	CB	CB	CB	CB	DC	DC	DC		DC		EB	EB	EB	EB	EB	EB
27 pF	270	F G J K		B² AB²	AB ²	ВВ			вв			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		DC		ЕВ	EB	EB	EB	EB	EB
30 pF	300	F G J K	M			ВВ			ВВ			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC			DC	ЕВ	EB	EB	EB	EB	EB
33 pF	330	F G J K		B ² AB ²	AB ²				BB			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		_		EB	EB	EB	EB	EB	EB
36 pF	360	F G J K		D2 A D2	A D2	BB			BB			CB	CB	CB	CB	CB	CB	DC	DC		DC			EB	EB	EB	EB	EB	EB
39 pF 43 pF	390 430	F G J K		B² AB²	AB	BB BB			BB BB			CB CB	CB CB	CB CB	CB CB	CB CB	CB CB	DC DC	DC DC	DC DC		1		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB
47 pF	470	F G J K		B² AB²	AB ²	BB			BB			CB	CB	CB	CB	CB	CB	DC	DC	DC				EB	EB	EB	EB	EB	EB
51 pF	510	F G J K				вв			вв			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC				ЕВ	EB	EB	EB	EB	EB
56 pF	560	F G J K		B² AB²	AB ²	ВВ			ВВ			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC				ЕВ	EB	EB	EB	EB	EB
62 pF	620	F G J K				ВВ			BB			СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC				EB	EB	EB	EB	EB	EB
68 pF	680	F G J K		B² AB²	AB ²				BB			CB	CB	CB	CB	CB	CB	DC	DC	DC				EB	EB	EB	EB	EB	EB
75 pF 82 pF	750 820	F G J K F G J K		B² AB²	ΔR2	BB BB			BB BB			CB CB	CB CB	CB CB	CB CB	CB CB	CB CB	DC DC	DC DC	DC DC				EB EB	EB EB	EB EB	EB EB	EB EB	EB EB
91 pF	910	F G J K		ם אם	ΛD	BB			BB			CB	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		_		EB	EB	EB	EB	EB	EB
100 pF	101	F G J K		B² AB²	AB ²	ВВ				ВВ	вв	СВ	СВ	СВ	CF	СВ	СВ	DC	DC	DC				EB	EB	EB	EB	EB	EB
110 - 270 pF*	111 - 271*	F G J K				ВВ	ВВ	BB	ВВ		ВВ	СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC	DC	DC	DC	ЕВ	EB	EB	EB	EB	EB
300 pF	301	F G J K				ВВ					BD	СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		DC		EB	EB	EB	EB	EB	EB
330 pF	331	F G J K				BB			_	_	BD	CB	CB	CB	CF	CB	CB	DC	DC	DC	_	_	_	EB	EB EB	EB	EB	EB EB	EB EB
360 pF 390 pF	361 391	F G J K				BB BB				BB BB		CB CB	CB CB	CB CB	CB CB	CB CB	CB CB	DC DC	DC DC	DC DC				EB EB	EB	EB EB	EB EB	EB	EB
430 pF	431	F G J K				BB				BB		CB	CB	CB	CB	CB	CB	DC	DC	DC				EB	EB	EB	EB	EB	EB
470 pF	471	F G J K				ВВ				BB		СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC				EB	EB	EB	EB	EB	EB
510 pF	511	F G J K				ВВ				ВВ		СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		_		EB	EB	EB	EB	EB	EB
560 pF	561	F G J K				BB				BB		СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		1		EB	EB	EB	EB	EB	EB
620 pF 680 pF	621 681	F G J K				BB BB				BB BB		CB CB	CB CB	CB CB	CB CB	CB CB	CB CB	DC DC	DC DC	DC DC		1		EB EB	EB EB	EB EB	EB EB	EB EB	EB EB
750 pF	751	F G J K				BB				BB		СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	DC		DC		EB	EB	EB	EB	EB	EB
820 pF	821	F G J K								BB			СВ	CB	СВ	CB			DC		DC	1			EB	EB	EB	EB	EB
910 pF	911	F G J K	M			ВВ	ВВ	вв в	ВВ	ВВ		СВ	СВ	СВ	СВ	СВ	СВ	DC	DC	_	DC	_	DD	_	EB	EB	EB	EB	EB
1,000 pF	102	F G J K					ВВ			ВВ			СВ	СВ	СВ						DC				EB	EB	EB	EB	EB
1,100 pF	112	F G J K					ВВ		BB			СВ											DC		EB	EB		EB	
1,200 pF 1,300 pF	122 132	F G J K						BB E				CB CB		CB									DC DC		EB EB			EB EC	
1,500 pF	152	F G J K				BB	BB	BB E	BB			СВ	СВ										DC		EB	EB		ED	
1,600 pF	162	F G J K					BB					CB	CB			CB					DD				EB	EB		ED	
1,800 pF	182	F G J K				ВВ	ВВ						СВ	СВ	СВ	СВ	СН	DD	DD	DD	DD	DD	DC	EB	EB	EB	EB	ED	ED
2,000 pF	202	F G J K						ВВ				СВ									DC				EB			ED	
2,200 pF	222	F G J K	M			BB	ВВ	BB									CH	DC	DC	DC	DC	DC	DC	EB	EB			EE	
2,400 pF	242		F G J K M		10		"0	10		-	0			CB	_	CB	0	1	_				DC	$\overline{}$	EB				
Compaitance	Сар	Rated Voltage (VD	''		22	9	_	_		<u>ع</u>	200 د	10	9	25	20	100	200	9	9	25	+	190		9	9	25	0 <u>c</u>	100	200
Capacitance	Code	Voltage Code			3	8	4		5	-	2	8	4	3	5		2	8	4	3	5		2	8	4	3	\Box	1	2
		Case Size / Ser	ies	C020	1C			040	12C				•	C06	U3C	<u> </u>				C08	3050	<i>:</i>				C12	U6C	-	

^{*}Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91).

These products are protected under US Patents 7,172,985 & 7,670,981, other patents pending, and any foreign counterparts.

xx1 Available only in D, J, K,M tolerance

xx² Available only in J, K, M tolerance.



Table 1A - Capacitance Range/Selection Waterfall (0201 - 1206 Case Sizes) cont'd

	Can					Siz es		<u> </u>	С	020)10	;		(C04	020				(C 06	030				(C08	050				(C12	060		
Capacitance	Cap Code		٧	olta	age	Cod	de		8	4	3	3	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2
	Code	F	Rate	d Vo	olta	ige ((VD	C)	은	9	7,	3	9	16	22	20	9	200	9	9	22	20	9	200	9	9	25	20	9	200	9	16	22	20	9	200
			Ca			tan)			-					P	roc	luct e Ta	Av	aila 2 f	bilit or C	y a	nd (Chip	Th	ick s Di	nes mei	s C	ode	s						
2,700 pF	272			F	F	3 J	K	M				Т							СВ	СВ	СВ	СВ	СВ		DC	DC	DC	DC	DC	DC	EB	EB	EB	EB	EC	EC
3,000 pF	302			F	F	3 J	K	M				- 1							СВ	СВ	СВ	СВ	СВ		DD	DD	DD	DD	DC	DC	EC	EC	EC	EC	EC	EB
3,300 pF	332			F	F	3 J	K	M	l			- 1							СВ	СВ	СВ	СВ	СВ		DD	DD	DD	DD	DC	DC	EC	EC	EC	EC	EE	EB
3,600 pF	362			F	F	3 J	K	M	l			- 1							СВ	СВ	СВ	СВ	СВ		DD	DD	DD	DD	DC	DD	EC	EC	EC	EC	EE	EB
3,900 pF	392				F		K	M				П							СВ	СВ	СВ	СВ	СВ		DE	DE	DE	DE	DC	DD	EC	EC	EC	EC	EF	EB
4,300 pF	432			F	F C	3 J	k	М	İ			- 1							СВ	СВ	СВ	СВ	СВ		DE	DE	DE	DE	DC	DD	EC	EC	EC	EC	EC	EB
4,700 pF	472			F	F	3 J	k	М	İ			- 1							СВ	СВ	СВ	СВ	СВ		DE	DE	DE	DE	DC	DD	EC	EC	EC	EC	EC	EB
5,100 pF	512			F	F C	3 J	k	М	İ			- 1							СВ	СВ	СВ	СВ			DE	DE	DE	DE	DC	DD	ED	ED	ED	ED	ED	EB
5,600 pF	562			F	F C	3 J	k	М	İ			- 1							СВ	СВ	СВ	СВ			DC	DC	DC	DC	DC	DD	ED	ED	ED	ED	ED	EB
6,200 pF	622			F	F	3 J	K	M											СВ	СВ	СВ	СВ			DC	DC	DC	DC	DC	DG	EB	EB	EB	EB	EB	EB
6,800 pF	682			F	F 0	3 J	K	M				- 1							СВ	СВ	СВ	СВ			DC	DC	DC	DC	DC	DG	EB	EB	EB	EB	EB	EB
7,500 pF	752			l F	F 0		k					- 1							СВ	СВ	СВ				DC	DC	DC	DC	DC	DG	EB	EB	EB	EB	EB	EB
8,200 pF	822			F	F		k	М	l			- 1							СВ	СВ	СВ				DC	DC	DC	DC	DC	DG	EC	EC	EC	EC	EB	EC
9,100 pF	912			F	F														СВ	СВ	CB				DC	DC		DC	DC		EC	EC	EC	EC	EB	EC
10,000 pF	103				= 0		_					Т							СВ	СВ	СВ				DC	DC		DC	DD		ED	ED	ED	ED	EB	EC
12,000 pF	123			- 1	F 0	. .	K		İ			- 1							СВ	СВ	СВ				DC	DC		DC	DE		EB	EB	EB	EB	EB	ED
15,000 pF	153			F	F 0	- -	K		İ			- 1							СВ	СВ	СВ				DC	DC		DD	DG		EB	EB	EB	EB	EB	EF
18,000 pF	183			- 1	F 0		K												"	"	"				DC	DC		DD			EB	EB	EB	EB	EB	EH.
22,000 pF	223			- 1	F		K												l						DD			DF			EB	EB	EB	EB	EC	EH
27,000 pF	273				F	_	_																		DF	DF	DF				EB	EB	EB	EB	EE	
33,000 pF	333				F	. .																			DG.	DG.					EB	EB	EB	EB	EE	
39,000 pF	393			- 1 -	F	. .	l K																		DG		DG				EC	EC	EC	EE	EH	
47,000 pF	473			- 1 -	F	. .																				DG					EC	EC	EC	EE	EH	
56,000 pF	563			1.	F	. .																				53	55				ED	ED	ED	EF		
68,000 pF	683				F		_																								EF	EF	EF	EH.		
82,000 pF	823			- 1	F	. .													l												EH	EH	EH	EH		
0.10 µF	104			- 1	F																										EH		EH			
0 0 p.		F	Rate				_	_	ę	9	ř,	a †	9	16	25	20	5	200	9	9	52	20	9	200	2	9	52	20	100	200	₽	19	52	20	9	200
Capacitance	Сар	Ė				Cod		٠,	8	4	3	-	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2	8	4	3	5	1	2
Capacitanio	Code	C			Ŭ			ies	Ė	020		+	-		C04				Ė		C06				Ė		C08				Ė		_	06C		_

^{*}Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91). xx^1 Available only in D, J, K,M tolerance

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xx² Available only in J, K, M tolerance.



Table 1B – Capacitance Range/Selection Waterfall (1210 – 2225 Case Sizes)

			Ca	ase			1			C12	10C			C	1808	IC	С	1812	C.	С	1825	iC	С	2220)C	C	2225	iC
Capacitance	Сар	H		oltac	rie			8	_	3	5	1	2	5	1	2	5	1	2	5	1	2	_	1	2	5	1	_
Capacitance	Code	H						9	4 9	25	5	90	200	50	<u> </u>	200	20	8	200		9 1	200	5 99	100	700	20	100	2 007
			Ca	d Vol	cita	nc		-	-	7	ū	=	Pro	duct	Ava	ilabi	lity a	nd C	hip	Thick	nes	s Co	des	7		ū	7	7
1.0 - 9.1 pF*	100 010*	В	_	ole	ran	ce		ED.	ΓD	FD	- FD	ΓD	FB	ee Ta	able	2 for	Chip	Thi	ckne	ss D	imer	ision	S					
1.0 - 9.1 pF 10 - 91 pF*	109 - 919* 100 - 910*	l d		F	G	J	КМ	FB FB	FB FB	FB FB	FB FB	FB FB	FB							l			l					
100 - 300 pF*	101 - 301*	i		F			КМ		FB	FB	FB	FB	FB	İ						İ			İ					
330 - 430 pF*	331 - 431*			F			K M		FB	FB	FB	FB	FB	LF	LF	LF												
470 - 910 pF*	471 - 911*			F			K M		FB	FB	FB	FB	FB	LF	LF	LF	GB	GB	GB									
1,000 pF	102			F	1 - 1		KM		FB	FB	FB FB	FB	FB FB	LF LF	LF	LF	GB	GB GB	GB									
1,100 pF 1,200 pF	112 122			F	1 - 1		K M		FB FB	FB FB	FB	FB FB	FB	LF	LF LF	LF LF	GB GB	GB	GB GB									
1,300 pF	132			F			K M		FB	FB	FB	FB	FC	LF	LF	LF	GB	GB	GB	l								
1,500 pF	152			F	-	- 1	K M		FB	FB	FB	FB	FE	LF	LF	LF	GB	GB	GB	İ			İ					
1,600 pF	162			F	G	J	K M	FB	FB	FB	FB	FB	FE	LF	LF	LF	GB	GB	GB									
1,800 pF	182			F			K M		FB	FB	FB	FB	FE	LF	LF	LF	GB	GB	GB									
2,000 pF	202			F	- 1		K M		FB	FB	FB	FC	FE	LF	LF	LF	GB	GB	GB									
2,200 pF	222 242			F	1 1		K M		FB	FB	FB	FC	FG	LF	LF	LF LF	GB	GB	GB	ŀ								
2,400 pF 2,700 pF	242			F		_	K M		FB FB	FB FB	FB FB	FC FC	FC FC	LF LF	LF LF	LF	GB	GB	GB									
3,000 pF	302			F			K M		FB	FB	FB	FC	FF	LF	LF		OB	OB	OD	l			l					
3,300 pF	332			F			K M		FB	FB	FB	FF	FF	LF	LF		GB	GB	GB	l								
3,600 pF	362			F			K M	FB	FB	FB	FB	FF	FF	LF	LF													
3,900 pF	392			F		_	K M		FB	FB	FB	FF	FF	LF	LF		GB	GB	GB	НВ	НВ	НВ						
4,300 pF	432			F	-		K M		FB	FB	FB	FF	FF	LF	LF		0.0	0.0	0.0			un					V-	WE
4,700 pF 5,100 pF	472 512			F			K M		FF FB	FF FB	FF FB	FG FG	FG FG	LF	LF		GB	GB	GD	НВ	НВ	НВ				KE KE	KE KE	KE KE
5,600 pF	562			F			K M		FB	FB	FB	FG	FG				GB	GB	GH	НВ	НВ	НВ				KE	KE	KE
6,200 pF	622			F			KM		FB	FB	FB	FG	FB				OD	OD	OII	IIID	TID	טוו	i			KE	KE	KE
6,800 pF	682	П	Т	F			K M		FB	FB	FB	FG	FB				GB	GB	GJ	НВ	НВ	НВ	JE	JE	JB	KE	KE	KE
7,500 pF	752			F	1 - 1	J	K M		FC	FC	FC	FC	FB	İ						İ			İ			KE	KE	KE
8,200 pF	822			F	-		K M		FC	FC	FC	FC	FB				GB	GH	GB	НВ	HB	HB	JE	JE	JB	KE	KE	KE
9,100 pF	912			F	1 - 1	- 1	K M		FE	FE	FE	FE	FB				0.0		0.0				_ ا			KE	KE	KE
10,000 pF	103 123			F	-	_	K M		FF FG	FF FG	FF FG	FF FB	FB FB				GB GB	GH	GB GB	HB HB	HB HB	HE	JE JE	JE JE	JB JB	KE KE	KE KE	KE KE
12,000 pF 15,000 pF	153			F			K M		FG	FG	FG	FB	FC				GB	GB	GB	НВ	НВ	ПЕ	JE	JE	JB	KE	KE	KE
18,000 pF	183			F			K M		FB	FB	FB	FB	FC				GB	GB	GB	НВ	HE		JE	JE	JB	KE	KE	INL.
22,000 pF	223			F	- 1		K M		FB	FB	FB	FB	FF				GB	GB	GB	НВ	HE		JE	JB	JB	KE	KE	
27,000 pF	273			F		J	K M	FB	FB	FB	FB	FB	FG				GB	GB	GB	НВ	HG		JE	JB	JB	KE	KE	
33,000 pF	333			F	1 - 1	- 1	K M		FB	FB	FB	FB	FH				GB	GB	GB				JB	JB	JB	KE		
39,000 pF	393			F			K M		FB	FB	FB FB	FE	FH				GB	GB	GB				JB	JB	JB			
47,000 pF 56,000 pF	473 563			F	1 - 1		K M		FB FB	FB FB	FB FB	FE FF	FJ				GB GB	GB GB	GD GD	1			JB JB	JB JB	JB JB			
68,000 pF	683			F			K M		FB	FB	FC	FG					GB	GB	GK				JB	JB	JB			
82,000 pF	823			F	-	-	K M		FC	FC	FF	FH					GB	GB	GM				JB	JB	JB			
0.10 µF	104			F	G	J	K M	FE	FE	FE	FG	FM					GB	GD	GM				JB	JB	JD			
0.12 µF	124			F			K M		FG	FG	FH						GB	GH					JB	JB	JD			
0.15 µF	154			F			KM		FH	FH	FM						GD	GN					JB	JB	JG			
0.18 μF 0.22 μF	184 224			F			K M		FJ FK	FJ FK							GH GK						JB JB	JD	JG			
0.22 μF 0.27 μF	224 274			F			K M		rr.	FK							GK						JB	JF				
0.27 μΓ 0.33 μF	334			F			KM										l						JD	JG		l		
0.39 µF	394			F	G	J	K M							l			l			1			JG	-				
0.47 μF	474	Ш		F			K M																JG					
	Сар	R		d Vol				10	16	25	20	100	200	20	100	200	50	100	200	20	100	200	20	100	200	20	100	200
Capacitance	Code	L	٧	oltag	ge Co	ode		8	4	3	5	1	2	5	1	2	5	1	2	5	1	2	5	1	2	5	1	2
		Ca	ıse	Siz	e/	Se	ries			C12	10C			С	1808	С	С	1812	С	С	1825	С	С	2220	C	С	2225	С

^{*}Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91). These products are protected under US Patents 7,172,985 & 7,670,981, other patents pending, and any foreign counterparts.



Table 2 – Chip Thickness/Packaging Quantities

Thickness	Case	Thickness ±	Paper C	Quantity	Plastic (Quantity
Code	Size	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
AB BB BD CB CF	0201 0402 0402 0603 0603	0.30 ± 0.03 0.50 ± 0.05 0.55 ± 0.05 0.80 ± 0.07 0.80 ± 0.07*	15,000 10,000 10,000 4,000 4,000	0 50,000 50,000 10,000 15,000	0 0 0 0	0 0 0 0
CH DE DC DD DF	0603 0603 0805 0805 0805	0.85 ± 0.07 0.85 ± 0.07 0.70 ± 0.20 0.78 ± 0.10 0.90 ± 0.10 1.10 ± 0.10	4,000 4,000 4,000 4,000 0	10,000 10,000 10,000 10,000 0	0 0 0 0 0 2,500	0 0 0 0 0 10,000
DG EB EC ED EE	0805 1206 1206 1206 1206	1.25 ± 0.15 0.78 ± 0.10 0.90 ± 0.10 1.00 ± 0.10 1.10 ± 0.10	0 4,000 0 0 0	0 10,000 0 0	2,500 4,000 4,000 2,500 2,500	10,000 10,000 10,000 10,000 10,000
EF EH FB FC FE	1206 1206 1210 1210 1210	1.20 ± 0.15 1.60 ± 0.20 0.78 ± 0.10 0.90 ± 0.10 1.00 ± 0.10	0 0 0 0	0 0 0 0	2,500 2,000 4,000 4,000 2,500	10,000 8,000 10,000 10,000 10,000
FF FG FH FM FJ	1210 1210 1210 1210 1210	1.10 ± 0.10 1.25 ± 0.15 1.55 ± 0.15 1.70 ± 0.20 1.85 ± 0.20	0 0 0 0 0	0 0 0 0 0	2,500 2,500 2,000 2,000 2,000	10,000 10,000 8,000 8,000 8,000
FK NC LF GB GD	1210 1706 1808 1812 1812	2.10 ± 0.20 1.00 ± 0.15 1.00 ± 0.15 1.00 ± 0.10 1.25 ± 0.15	0 0 0 0	0 0 0 0	2,000 4,000 2,500 1,000 1,000	8,000 10,000 10,000 4,000 4,000
GH GG GK GJ GN	1812 1812 1812 1812 1812	1.40 ± 0.15 1.55 ± 0.10 1.60 ± 0.20 1.70 ± 0.15 1.70 ± 0.20	0 0 0 0	0 0 0 0	1,000 1,000 1,000 1,000 1,000	4,000 4,000 4,000 4,000 4,000
GM HB HE HG JB	1812 1825 1825 1825 2220	2.00 ± 0.20 1.10 ± 0.15 1.40 ± 0.15 1.60 ± 0.20 1.00 ± 0.15	0 0 0 0	0 0 0 0	500 1,000 1,000 1,000 1,000	2,000 4,000 4,000 4,000 4,000
JD JE JF JG KE	2220 2220 2220 2220 2225	1.30 ± 0.15 1.40 ± 0.15 1.50 ± 0.15 1.70 ± 0.15 1.40 ± 0.15	0 0 0 0	0 0 0 0	1,000 1,000 1,000 1,000 1,000	4,000 4,000 4,000 4,000 4,000
Thickness Code	Case Size	Thickness ± Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
Code	Size	Kange (mm)	Paper C	Quantity	Plastic (Quantity

Package quantity based on finished chip thickness specifications.



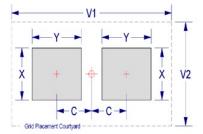
Table 3 - Chip Capacitor Land Pattern Design Recommendations per IPC-7351

EIA Size Code	Metric Size Code	ı	Maxi	sity Lev mum (N)		Medi	sity Lev an (Nor rotrusio)		Mini	sity Lev mum (L rotrusio)
Jour	oouc	С	Y	X	V1	V2	С	Y	X	V1	V2	С	Y	Х	V1	V2
0201	0603	0.38	0.56	0.52	1.80	1.00	0.33	0.46	0.42	1.50	0.80	0.28	0.36	0.32	1.20	0.60
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00
1808	4520	2.30	1.75	2.30	7.40	3.30	2.20	1.55	2.20	6.50	2.70	2.10	1.35	2.10	5.80	2.40
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70
1825	4564	2.15	1.60	6.90	6.90	7.90	2.05	1.40	6.80	6.00	7.30	1.95	1.20	6.70	5.30	7.00
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60
2225	5664	2.70	1.70	6.90	8.10	7.90	2.60	1.50	6.80	7.20	7.30	2.50	1.30	6.70	6.50	7.00

¹ Only for capacitance values ≥ 22 μF

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).



Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- All other EIA case sizes are limited to solder reflow only

Recommended Soldering Profile:

• KEMET recommends following the guidelines outlined in IPC/JEDEC J-STD-020



Table 4 – Performance & Reliability: Test Methods and Conditions

Stress	Reference	Test or Inspection Method
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for C0G. Flexible termination system – 3.0 mm (minimum).
		Magnification 50 X. Conditions:
Caldarahilitu	J-STD-002	a) Method B, 4 hours @ 155°C, dry heat @ 235°C
Solderability	J-51D-002	b) Method B @ 215°C category 3
		c) Method D, category 3 @ 260°C
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 2 hours after test conclusion.
Diagod Humidika	MIL CTD 202 Matha d 402	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Biased Humidity	MIL-STD-202 Method 103	Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 2 hours after test conclusion.
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Unpowered. Measurement at 24 hours +/- 2 hours after test conclusion.
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Number of cycles required – 300, maximum transfer time – 20 seconds, dwell time – 15 minutes. Air – Air.
High Temperature Life	MIL-STD-202 Method 108 /EIA-198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.

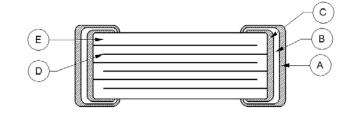
Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature—reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction

Reference	Ite	em	Material
А		Finish	100% Matte Sn
В	Termination System	Barrier Layer	Ni
С	, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Base Metal	Cu
D	Inner El	ectrode	Ni
Е	Dielectric	: Material	CaZrO ₃



Note: Image is exaggerated in order to clearly identify all components of construction.

Capacitor Marking (Optional):

Laser marking option is not available on:

- C0G, Ultra Stable X8R and Y5V dielectric devices
- EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

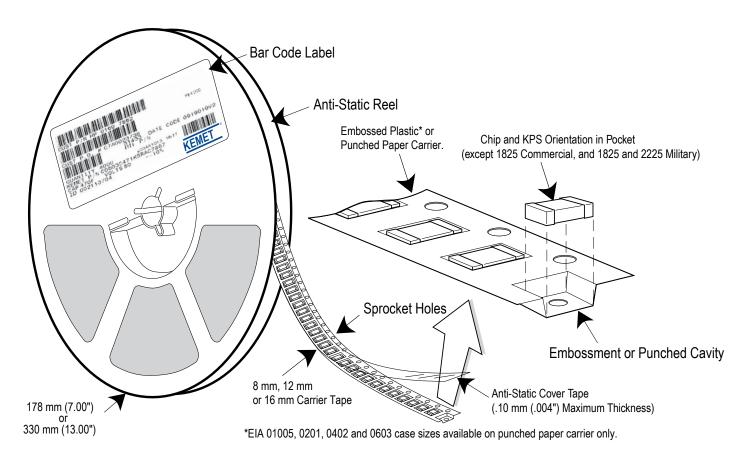


Table 5 – Carrier Tape Configuration – Embossed Plastic & Punched Paper (mm)

EIA Case Size	Tape Size (W)*	Pitch (P₁)*
01005 – 0402	8	2
0603 – 1210	8	4
1805 – 1808	12	4
≥ 1812	12	8
KPS 1210	12	8
KPS 1812 & 2220	16	12
Array 0508 & 0612	8	4

^{*}Refer to Figures 1 & 2 for W and P, carrier tape reference locations.

^{*}Refer to Tables 6 & 7 for tolerance specifications.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

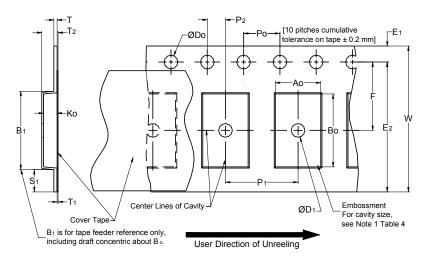


Table 6 - Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)				(1.181)			
Variable Dimensions — Millimeters (Inches)									
Tape Size Pitch B ₁ Maximum E ₂ F		F	P ₁	T ₂ Maximum	W Maximum	A_0,B_0	& K ₀		
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Not	te 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).
- 3. If $S_1 < 1.0$ mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).
- 4. B, dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by A_{o} , B_{o} and K_{o} shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).
 - (e) for KPS Series product, A_a and B_a are measured on a plane 0.3 mm above the bottom of the pocket.
 - (f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 – Punched (Paper) Carrier Tape Dimensions

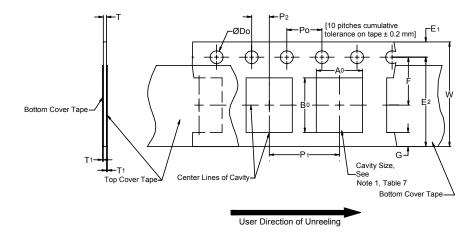


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)								
Tape Size	D _o	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2	
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)	
	Variable Dimensions — Millimeters (Inches)							
Tape Size	Pitch	E2 Minimum	F	P ₁	T Maximum	W Maximum	A_0B_0	
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1	
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	Note I	

- 1. The cavity defined by A_{o} , B_{o} and T shall surround the component with sufficient clearance that:
 - a) the component does not protrude beyond either surface of the carrier tape.
 - b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - c) rotation of the component is limited to 20° maximum (see Figure 3).
 - d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).
 - e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).



Packaging Information Performance Notes

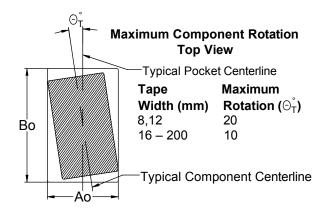
- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength		
8 mm	0.1 to 1.0 Newton (10 to 100 gf)		
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)		

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165 $^{\circ}$ to 180 $^{\circ}$ from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 \pm 10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624.*

Figure 3 – Maximum Component Rotation



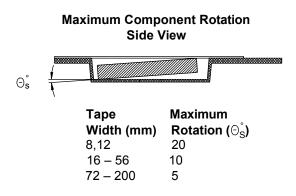


Figure 4 – Maximum Lateral Movement

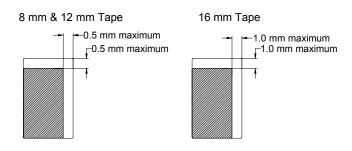


Figure 5 – Bending Radius

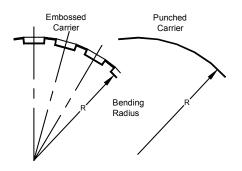
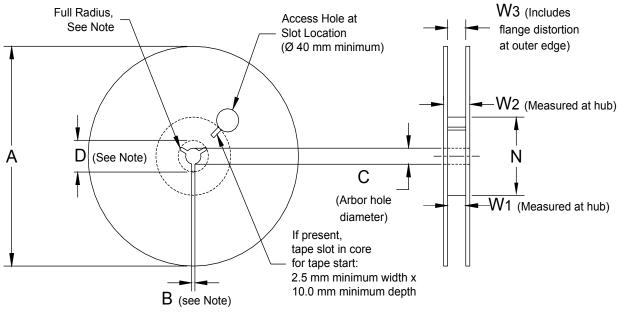




Figure 6 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 - Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)								
Tape Size	A	B Minimum	С	D Minimum				
8 mm	178 ±0.20		13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)				
12 mm	(7.008 ±0.008) or	1.5 (0.059)						
16 mm	330 ±0.20 (13.000 ±0.008)	,	,					
	Variable Dimensions — Millimeters (Inches)							
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃				
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)					
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference				
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)					



Figure 7 - Tape Leader & Trailer Dimensions

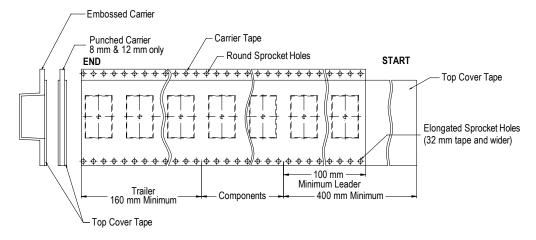
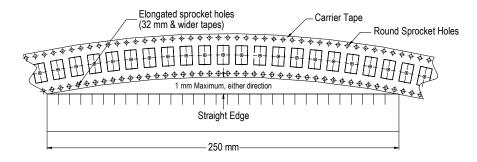
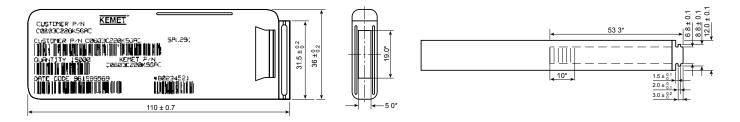


Figure 8 – Maximum Camber



Bulk Cassette Packaging (Ceramic Chips Only)

Meets Dimensional Requirements IEC–286 and EIAJ 7201 *Unit mm *Reference*



Capacitor Dimensions for Bulk Cassette

Cassette Packaging - Millimeters

EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation Minimum	T Thickness	Number of Pieces/Cassette
0402	1005	1.0 ±0.05	0.5 ±0.05	0.2 to 0.4	0.3	0.5 ±0.05	50,000
0603	1608	1.6 ±0.07	0.8 ±0.07	0.2 to 0.5	0.7	0.8 ±0.07	15,000



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CGA3E1X7R1C474K