

NTE5408 thru NTE5410 Silicon Controlled Rectifier (SCR) 3 Amp Sensitive Gate, TO5

Description:

The NTE5408 through NTE5410 sensitive gate SCRs are designed to be driven directly with IC and MOS devices. These SCRs feature proprietary, void–free glass–passivated chips and are hermetically sealed in TO5 type packages. These 4A devices are available in voltages up to 600V and with a gate current of $200\mu A$.

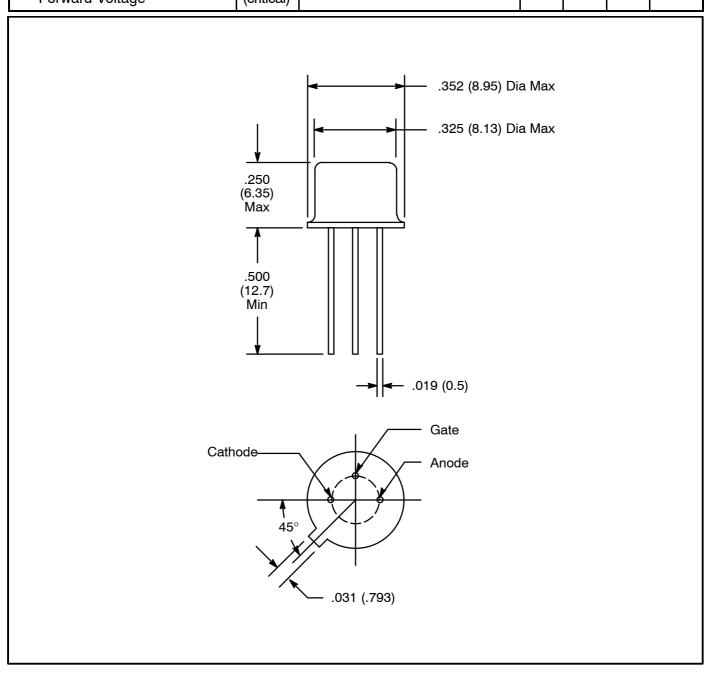
These NTE SCRs are reverse–blocking triode thyristors and may be switched from off–state to conduction by a current pulse applied to the gate terminal. The NTE5408 through NTE5410 are designed for control applications in lighting, heating, cooling, and static switching relays.

Absolute Maximum Ratings:

$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Repetitive Peak Reverse Voltage (T _C = +100°C), V _{RRM}	00\/
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NTE5410		
RMS On–State Current (T_C = +75°C, Conduction Angle of 180°), $I_{T(RMS)}$		
Peak Surge (Non–Repetitive) On–State Current (One Cycle at 50 or 60Hz), I_{TSM}	NTE5410	00V
Peak Gate-Trigger Current (3 μ s Max), I _{GTM}	RMS On–State Current ($T_C = +75^{\circ}C$, Conduction Angle of 180°), $I_{T(RMS)}$. 4A
$\begin{array}{llllllllllllllllllllllllllllllllllll$	Peak Surge (Non-Repetitive) On-State Current (One Cycle at 50 or 60Hz), I _{TSM}	40A
	Peak Gate-Trigger Current (3μs Max), I _{GTM}	. 1A
Operating Temperature Range, T_{opr}	Peak Gate–Power Dissipation ($I_{GT} \le I_{GTM}$ for $3\mu s$ Max), P_{GM}	20W
Storage Temperature Range, T _{stg}	Average Gate Power Dissipation, $P_{G(AV)}$	mW
	Operating Temperature Range, Topr40° to +10	0°C
Typical Thermal Resistance, Junction-to-Case, R _{thJC} +5°C/W	Storage Temperature Range, T _{stg} 40° to +15	0°C
	Typical Thermal Resistance, Junction-to-Case, R _{thJC} +5°C	C/W

<u>Electrical Characteristics:</u> $(T_C = +25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Peak Off-State Current	I _{RRM}	V_{RRM} = Max, V_{DRXM} = Max, T_{C} = +100°C, R_{GK} = 1k Ω	-	_	0.75	mA
	I _{DRXM}		-	_	0.75	mA
Maximum On-State Voltage	V_{TM}	I _T = 10A (Peak)	-	_	2.2	V
DC Holding Current	I _{HOLD}	R _{GK} = 1000Ω	_	_	5	mA
DC Gate-Trigger Current	I _{GT}	$V_D = 6VDC, R_L = 100\Omega$	-	-	200	μΑ
DC Gate-Trigger Voltage	V _{GT}	$V_D = 6VDC, R_L = 100\Omega$	-	-	8.0	V
Gate Controlled Turn-On Time	t _{gt}	I _G x 3 _{GT}	-	1.2	_	μs
I ² t for Fusing Reference	l ² t	For SCR Protection	_	_	2.6	A ² sec
Critical Rate of Applied Forward Voltage	dv/dt (critical)	$R_{GK} = 1k\Omega$, $T_C = +100^{\circ}C$	-	5	_	V/μs



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