

#### Thick Film Chip Resistor Network

#### Performance Specification

Temperature Coefficient  $50\Omega \sim 1M\Omega$ :  $\pm 200PPM/^{\circ}C$ 

<50 $\Omega$  & >1M $\Omega$ : ±250PPM/°C

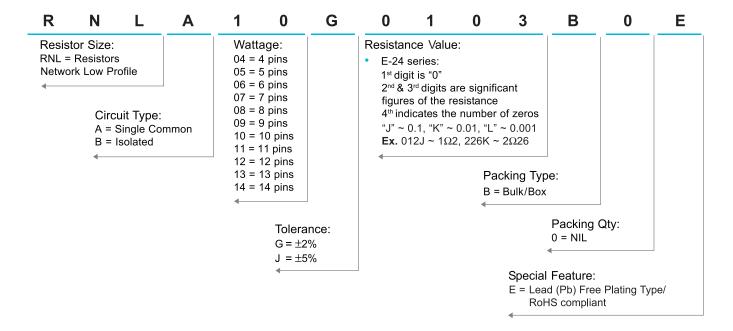
Short Time Overload  $\pm (0.5\% + 0.1\Omega) \text{Max}$ Insulation Resistance Min. 10,000 Mega Ohm

Dielectric Withstanding Voltage No evidence of flashover, mechanical damage, arcing or insulation breakdown.

Terminal Strength  $\pm (0.5\% + 0.1\Omega) \text{Max}$  Resistance to Soldering Heat  $\pm (0.5\% + 0.1\Omega) \text{Max}$  Solderability Min. 95% coverage. Thermal Shock  $\pm (0.5\% + 0.1\Omega) \text{Max}$  Temperature Cycling  $\pm (0.5\% + 0.1\Omega) \text{Max}$  Load Life in Humidity  $\pm (3.0\% + 0.1\Omega) \text{Max}$ 

Load Life  $\pm (3.0\% + 0.1\Omega)$ Max

#### Ordering Procedure: Ex.: RNL A-type, 10 Pins, +/-2%, 10KΩ, B/B

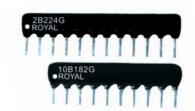




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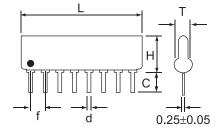
## Features

- High reliability with RUO2 paste
- · Miniature, high density packaging
- · combination of different ohmic values are available

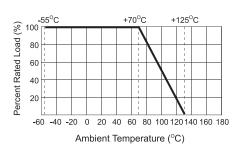


Single Common	Isolated		Neck and Neck	
АВ		С	D	
R1 ≯ R2 ≯ R3 ≯Rn ≯  1 2 3 4n+1  R1=R2==Rn	R1\( \) R2\( \) Rn\( \) Rn\( \) 1 2 3 42n-1 2n R1=R2==Rn	R1 = R2 ==Rn	R1 R2 R3 Rn Rn Rn R1 R1 R1 R1=R2==Rn	
Е	Р	L	R	
R1 > R1 > R1 > R2 > R2 > R2 > R2 > R2 >	R1\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\display\) R1\(\display\) R1\(\display\) R1\(\display\) R2\(\display\) R2\(\display\) R1\(\display\) R1\(\d	R1 R2Rn	R2 \$ R2 \$ R2 \$ R2 \$ R2 \$ R1 \$ R1 \$ R1 \$	
R1=R2= or R1≠R2	R1=R2= or R1≠R2	R1=R2==Rn	R1=R2= or R1≠R2	
Ladder			Double Sided	

## Dimension (mm)

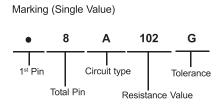


## **Derating Curve**



Dual Value (R1/R2)(Ohm)					
160 / 240	330 / 390				
180 / 390	330 / 470				
220 / 270	1.5K / 3.3K				
220 / 330	3.0K / 6.2K				

Type	L (Max.)	H (Max.)	T(Max.)	$c^{+0.3}_{-0.2}$	d±0.1	f±0.2
4 pins	10.2					
5 pins	12.7					
6 pins	15.3					
7 pins	17.8					
8 pins	20.4					
9 pins	22.9	5.08	2.5	3.2	0.5	2.54
10 pins	25.4					
11 pins	28.2					
12 pins	30.5					
13 pins	31.1					
14 pins	35.6					



•	8	Α	330 / 470	G

Tolerance

Resistance Value

Marking (Dual Value)

Total Pin

Туре	Power Rating at 70°C	Operating Temp. Range	Max Working Voltage	Max Overload Voltage	Dielectric Withstanding Voltage	Tolerance %	Resistance Range
В Туре	0.2W	-550 ~ +125°C	100V	150V	200V	±2% ±5%	R-Type 100Ω ~ 10KΩ
Other	0.125W	-550 ~ +125°C	100V	150V	200V		Others: $10\Omega \sim 1M\Omega$



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