

# SF3001PT THRU SF3006PT

30.0 AMPS. Glass Passivated Super Fast Rectifiers



Voltage Range 50 to 400 Volts Current 30.0 Amperes

# **Features**

- Dual rectifier construction, positive center-tap
- Plastic package has Underwriters Laboratory Flammability Classifications 94V-0
- ♦ Glass passivated chip junctions
- Superfast recovery time, high voltage
- Low forward voltage, high current capability
- Low thermal resistance
- ♦ Low power loss, high efficiency
- High temperature soldering guaranteed: 260°C / 10 seconds, 0.16°(4.06mm) lead lengths at 5 lbs., (2.3kg) tesion

## **Mechanical Data**

- ♦ Cases: JEDEC TO-3P/TO-247AD molded plastic
- Terminals: Leads solderable per MIL-STD-750, Method 2026
- ♦ Polarity: As marked
- ♦ Mounting position: Any
- ♦ Weight: 0.2 ounce, 5.6 grams

# TO-3P/TO-247AD 203(6,16) 193(4,90) 245(6,2) 225(5,7) 225(5,7) 2313(7,9) 370 17(4,3) 370 1

PIN 3 PIN 2

Dimensions in inches and (millimeters)

# **Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

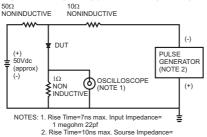
I of capacitive load, derate current by 20%								
TYPE NUMBER	Symbol	SF 3001PT	SF 3002PT	SF 3003PT	SF 3004PT	SF 3005PT	SF 3006PT	Units
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	150	200	300	400	V
Maximum RMS Voltage	$V_{RMS}$	35	70	105	140	210	280	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	150	200	300	400	V
Maximum Average Forward Rectified Current at Tc=100 $^\circ$ C	I <sub>(AV)</sub>	30						Α
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	300						Α
Maximum Instantaneous Forward Voltage @15.0A	$V_{F}$	0.95 1.3					V	
Maximum D.C. Reverse Current @ Tc=25℃ at Rated DC Blocking Voltage @ Tc=100℃	I <sub>R</sub>	10.0 500						uA uA
Maximum Reverse Recovery Time(Note 2) T <sub>J</sub> =25°C	Trr	35						nS
Typical Junction Capacitance (Note 1)	Cj	175.0						pF
Typical Thermal Resistance (Note 3)	$R\theta_{JC}$	2.5						<b>C</b> W
Operating Junction Temperature Range	TJ	-55 to +150						Ç
Storage Temperature Range	T <sub>STG</sub>	-55 to +150						Ç

- Notes: 1. Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.
  - 2. Reverse Recovery Test Conditions: I<sub>F</sub>=0.5A, I<sub>R</sub>=1.0A, Recover to 0.25A.
  - 3. Mounted on 4" x 6" x 0.25" Al-Plate.



### RATINGS AND CHARACTERISTIC CURVES (SF3001PT THRU SF3006PT)

FIG.1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





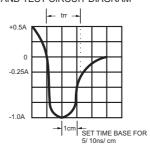


FIG.2- MAXIMUM FORWARD CURRENT DERATING

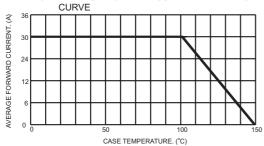
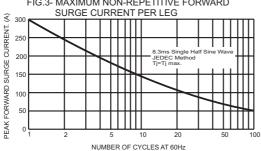


FIG.3- MAXIMUM NON-REPETITIVE FORWARD



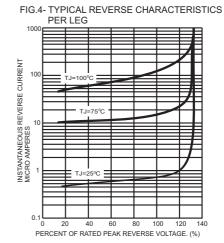
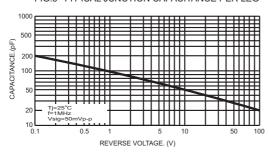
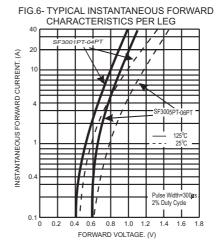


FIG.5- TYPICAL JUNCTION CAPACITANCE PER LEG





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