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Kind regards,

Team Nexperia



2PD601BRL; 2PD601BSL 50 V, 200 mA NPN general-purpose transistors Rev. 1 – 28 June 2010 Pro

Product data sheet

1. **Product profile**

1.1 General description

NPN general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table 1. **Product overview**

Type number	Package	Package	
	NXP	JEDEC	
2PD601BRL	SOT23	TO-236AB	2PB709BRL
2PD601BSL			2PB709BSL

1.2 Features and benefits

- Collector current $I_C \le 200 \text{ mA}$
- Two current gain selections
- AEC-Q101 qualified
- Small SMD plastic package

1.3 Applications

General-purpose switching and amplification

1.4 Quick reference data

Quick reference data Table 2.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	50	V
I _C	collector current		-	-	200	mA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V};$ $I_C = 2 \text{ mA}$	210	-	460	
	h _{FE} group R		210	-	340	
	h _{FE} group S		290	-	460	



50 V, 200 mA NPN general-purpose transistors

2. Pinning information

Table 3.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter		3
3	collector		
			sym021

3. Ordering information

Table 4. Ordering information				
Type number	Package			
	Name	Description	Version	
2PD601BRL	-	plastic surface-mounted package; 3 leads	SOT23	
2PD601BSL				

4. Marking

Table 5. Marking codes	
Type number	Marking code ^[1]
2PD601BRL	ML*
2PD601BSL	MM*

- [1] * = -: made in Hong Kong
 - * = p: made in Hong Kong
 - * = t: made in Malaysia
 - * = W: made in China

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	60	V
V_{CEO}	collector-emitter voltage	open base	-	50	V
V_{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	200	mA
I _{CM}	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$	-	300	mA
I _{BM}	peak base current	single pulse; $t_p \leq 1 \text{ ms}$	-	200	mA

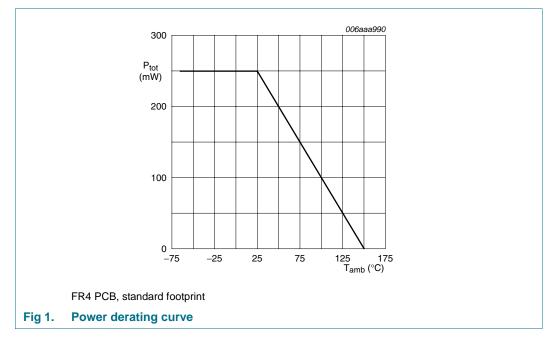
50 V, 200 mA NPN general-purpose transistors

Table 6. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

		/		
Parameter	Conditions	Min	Мах	Unit
total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	250	mW
junction temperature		-	150	°C
ambient temperature		-55	+150	°C
storage temperature		-65	+150	°C
	total power dissipation junction temperature ambient temperature	total power dissipation $T_{amb} \le 25 \ ^{\circ}C$ junction temperatureambient temperature	total power dissipation $T_{amb} \le 25 \ ^{\circ}C$ [1]-junction temperature-ambient temperature-55	total power dissipation $T_{amb} \le 25 \text{ °C}$ [1]-250junction temperature-150ambient temperature-55+150

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



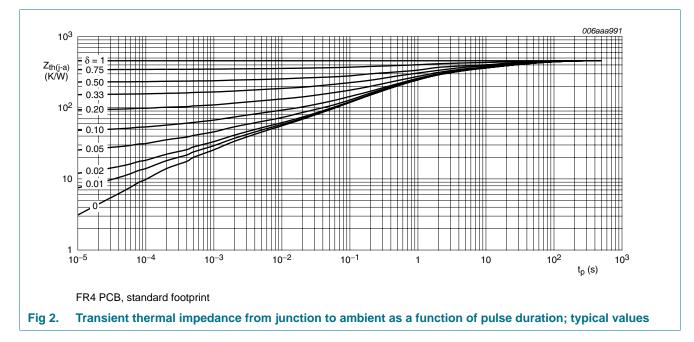
6. Thermal characteristics

Table 7. **Thermal characteristics** Мах Symbol Parameter Conditions Min Unit Тур [1] _ thermal resistance from junction in free air 500 K/W R_{th(j-a)} _ to ambient thermal resistance from junction 140 K/W R_{th(j-sp)} -to solder point

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

2PD601BRL_2PD601BSL

50 V, 200 mA NPN general-purpose transistors



7. Characteristics

Table 8.Characteristics

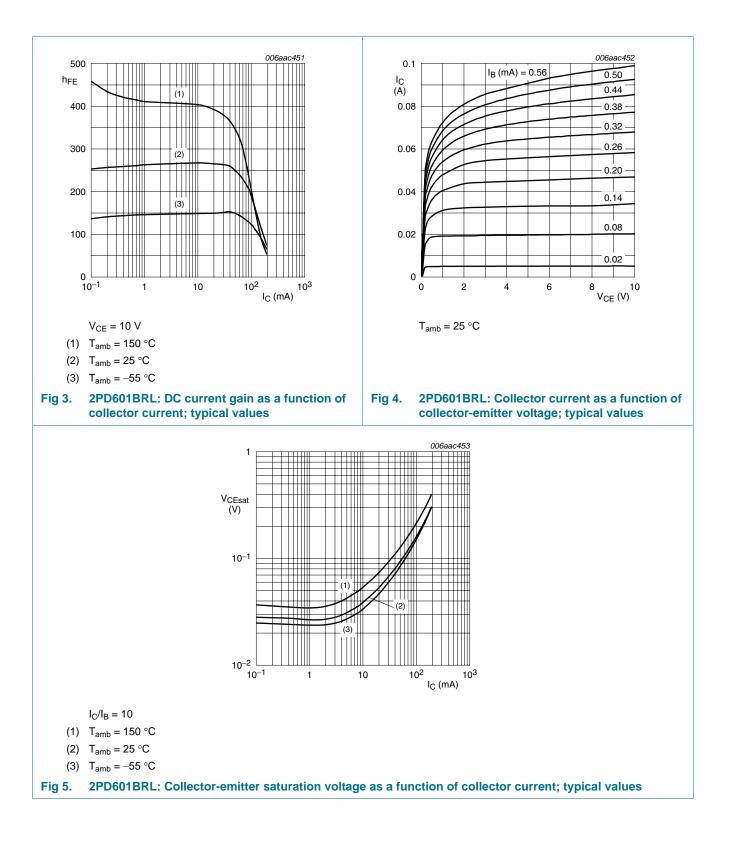
amb - 20	c unless otherwise specified.						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 60 \text{ V}; \text{ I}_{E} = 0 \text{ A}$		-	-	10	nA
		$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$		-	-	5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	10	nA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V};$ $I_C = 2 \text{ mA}$		210	-	460	
	h _{FE} group R			210	-	340	
	h _{FE} group S			290	-	460	
V _{CEsat}	collector-emitter saturation voltage	$I_{\rm C}$ = 100 mA; $I_{\rm B}$ = 10 mA	<u>[1]</u>	-	-	250	mV
f _T	transition frequency	V _{CE} = 6 V; I _C = 10 mA; f = 100 MHz		100	250	-	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V};$ $I_E = i_e = 0 \text{ A};$ f = 1 MHz		-	-	3	pF

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2PD601BRL; 2PD601BSL

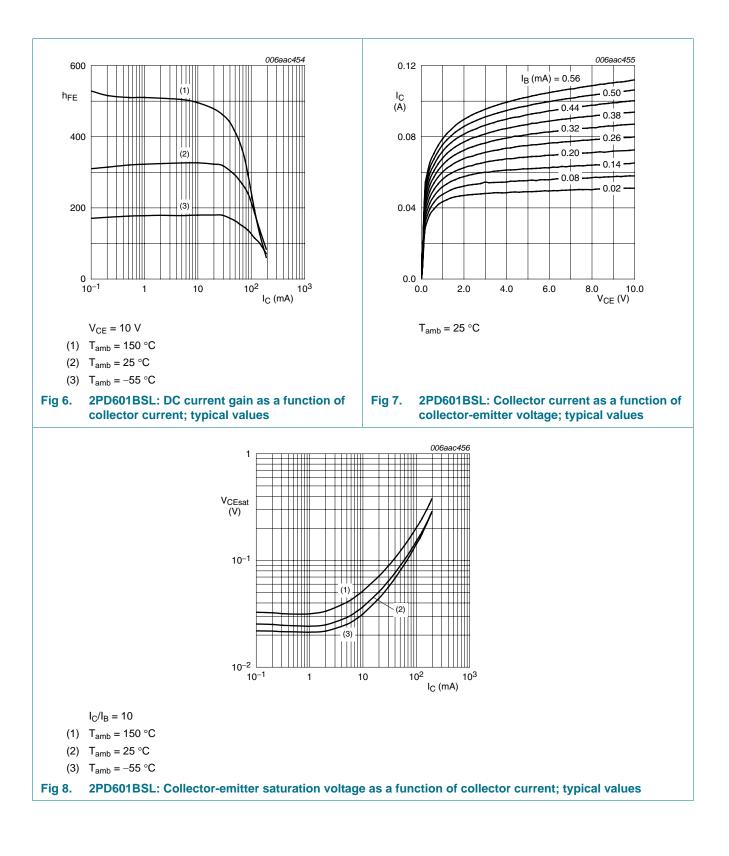
50 V, 200 mA NPN general-purpose transistors



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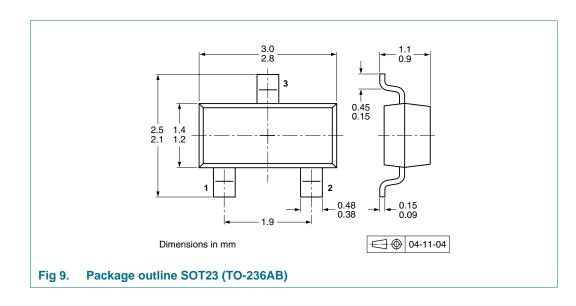
50 V, 200 mA NPN general-purpose transistors

8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



50 V, 200 mA NPN general-purpose transistors

10. Packing information

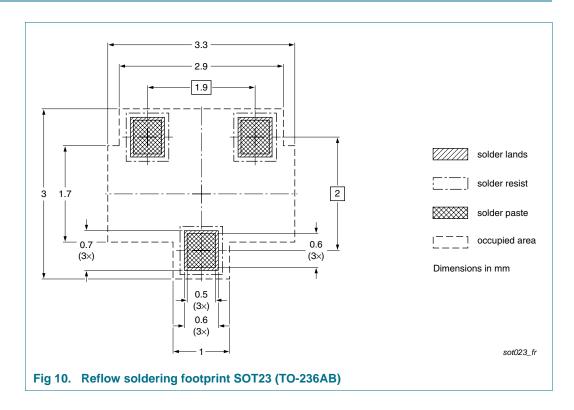
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

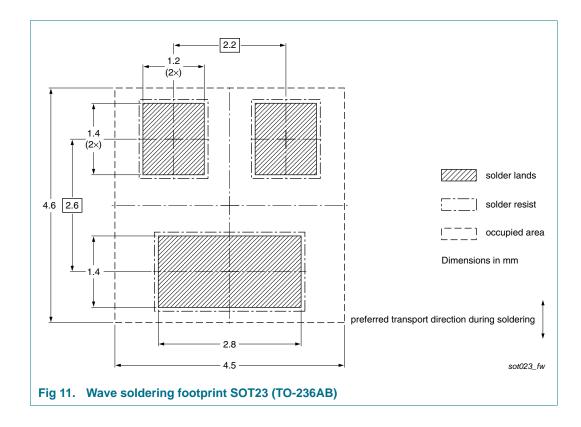
Type number	Package	Description	Packing	Packing quantity		
			3000	10000		
2PD601BRL	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235		
2PD601BSL						

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

11. Soldering



50 V, 200 mA NPN general-purpose transistors



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12. Revision history

Table 10. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
2PD601BRL_2PD601BSL v.1	20100628	Product data sheet	-	-

50 V, 200 mA NPN general-purpose transistors

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Product data sheet

50 V, 200 mA NPN general-purpose transistors

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Date of release: 28 June 2010 Document identifier: 2PD601BRL_2PD601BSL

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