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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR **2SJ202**

P-CHANNEL MOS FET FOR SWITCHING

DESCRIPTION

The 2SJ202 is a P-channel vertical type MOS FET which can be driven by 2.5 V power supply.

As the MOS FET is driven by low voltage and does not require consideration of driving current, it is suitable for appliances including VTR cameras and headphone stereos which need power saving.

FEATURES

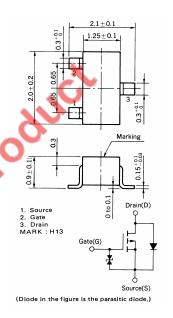
- Directly driven by ICs having a 3 V power supply.
- · Not necessary to consider driving current because of its high input impedance.
- · Possible to reduce the number of parts by omitting the bias resistor.
- · Complementary to 2SK1580

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGS = 0 V	VDSS	-16	٧
Gate to Source Voltage (e to Source Voltage (VDS = 0 V)			٧
Drain Current (DC)		ID(DC)	∓100	mΑ
Drain Current (pulse) Note		I _{D(pulse)}	∓200	mΑ
Total Power Dissipation		Рт	150	mW
Channel Temperature		Tch	150	°C
Storage Temperature		Tstg	-55 to +150	°C

Note PW \leq 10 ms, Duty Cycle \leq 50%

PACKAGE DRAWING (Unit: mm)



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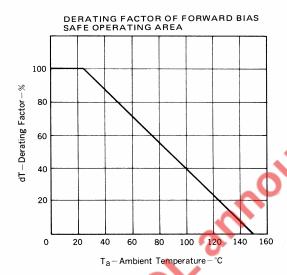


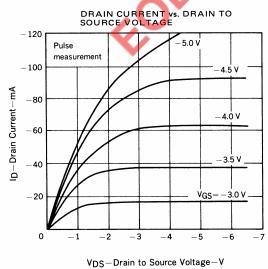
ELECTRICAL CHARACTERISTICS ($T_a = 25$ °C)

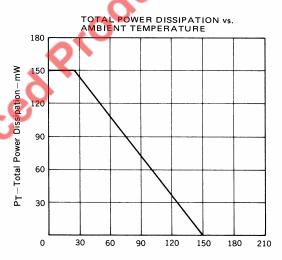
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
Drain Cut-off Current	IDSS			-1.0	μА	$V_{DS} = -16 \text{ V}, V_{GS} = 0$	
Gate Leakage Current	IGSS			∓10	μΑ	$V_{GS} = \mp 3.0 \text{ V}, V_{DS} = 0$	
Gate Cut-off Voltage	V _{GS(off)}	-1.1	-1.7	-2.1	V	$V_{DS} = -3.0 \text{ V, } I_{D} = -1.0 \mu \text{A}$	
Forward Transfer Admittance	ly _{fs} l	20	27		mS	$V_{DS} = -3.0 \text{ V, } I_{D} = -10 \text{ mA}$	
Drain to Source On-State Resistance	R _{DS(on)1}		70	100	Ω	$V_{GS} = -2.5 \text{ V}, I_D = -1.0 \text{ mA}$	
Drain to Source On-State Resistance	R _{DS(on)2}		23	30	Ω	$V_{GS} = -4.0 \text{ V, I}_{D} = -1.0 \text{ mA}$	
Input Capacitance	Ciss		18		рF		
Output Capacitance	Coss		17		pF	$V_{DS} = -3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	
Feedback Capacitance	C _{rss}		3		pF		
Turn-On Delay Time	^t d(on)		40		ns		
Rise Time	t _r		60		ns	$V_{GS(on)} = -4 \text{ V, R}_{G} = 10 \Omega, V_{DD} = -4 \text{ V,}$	
Turn-Off Delay Time	^t d(off)		60		ns	I _D = -1 mA	
Fall Time	tf		100		ns		

TYPICAL CHARACTERISTICS ($T_a = 25$ °C)

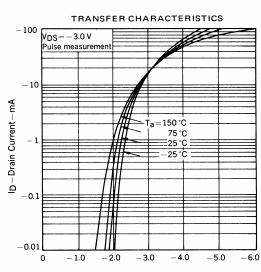




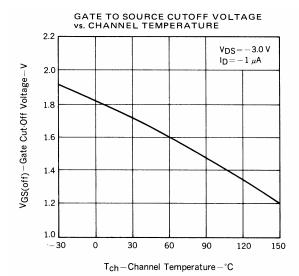


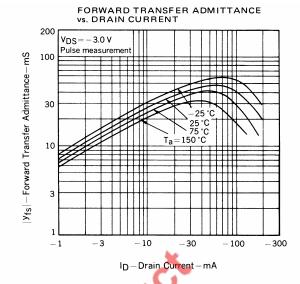


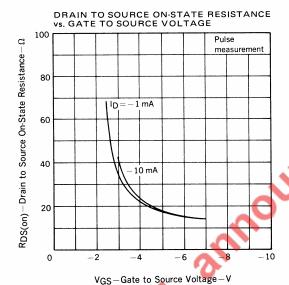


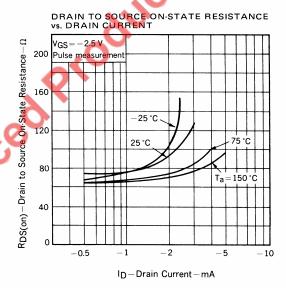


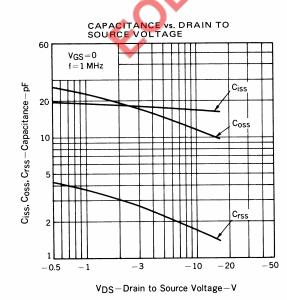
VGS-Gate to Source Voltage-V

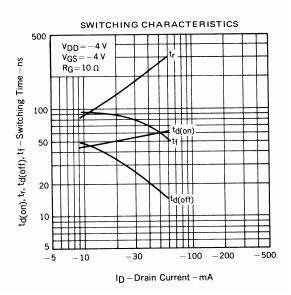


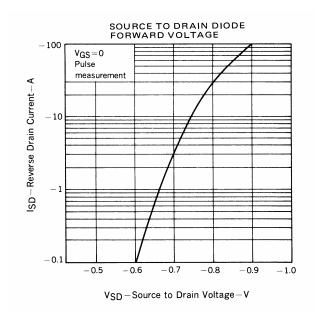


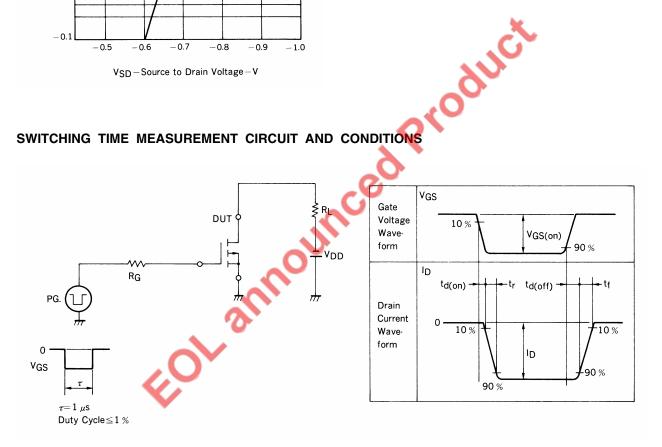












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