2SK4125

N-Channel Power MOSFET 600V, 17A, 610mΩ, TO-3P-3L



http://onsemi.com

Features

- ON-resistance RDS(on)= 0.47Ω (typ.)
- Input capacitance Ciss=1200pF (typ.)
- 10V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		600	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		17	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	А
Allowable Power Dissipation	Do		2.5	W
Allowable Power Dissipation	PD	Tc=25°C (Our ideal heat dissipation condition)*1	170	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *2	EAS		78.8	mJ
Avalanche Current *3	IAV		17	А

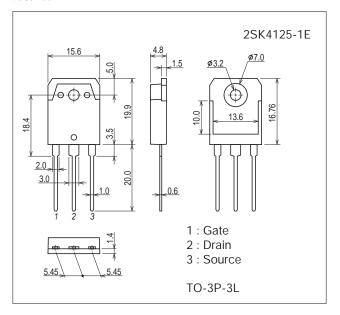
^{*1} Our condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ) 7539-002

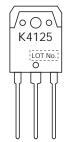


Product & Package Information

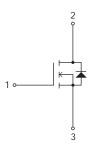
 $\bullet \ Package: TO\text{-}3P\text{-}3L$

JEITA, JEDEC: SC-65, TO-247, SOT-199
Minimum Packing Quantity: 30 pcs./magazine

Marking



Electrical Connection



 $^{^*2~}V_{DD}$ =50V, L=500 μ H, IAV=17A (Fig.1)

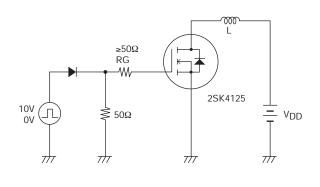
^{*3} L≤500µH, single pulse

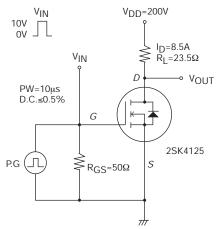
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Linit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	600			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =480V, V _{GS} =0V			100	μΑ	
Gate-to-Source Leakage Current	IGSS	V _G S=±30V, V _D S=0V			±100	nA	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	3		5	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =8.5A	4.5	9		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =7A, V _{GS} =10V		0.47	0.61	Ω	
Input Capacitance	Ciss			1200		pF	
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		220		pF	
Reverse Transfer Capacitance	Crss			50		pF	
Turn-ON Delay Time	t _d (on)			26.5		ns	
Rise Time	t _r	Car Fig 2		82		ns	
Turn-OFF Delay Time	t _d (off)	See Fig.2		145		ns	
Fall Time	t _f			52		ns	
Total Gate Charge	Qg			46		nC	
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =17A		8.3		nC	
Gate-to-Drain "Miller" Charge	Qgd			26.7		nC	
Diode Forward Voltage	V _{SD}	I _S =17A, V _{GS} =0V		1.0	1.3	V	

Fig.1 Avalanche Resistance Test Circuit

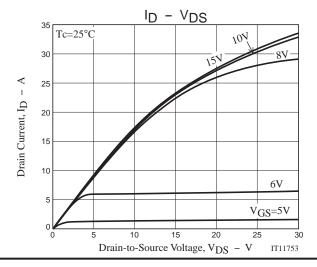
Fig.2 Switching Time Test Circuit

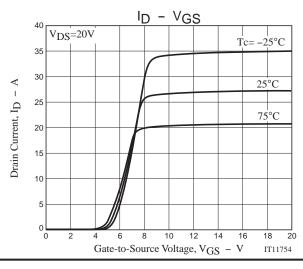


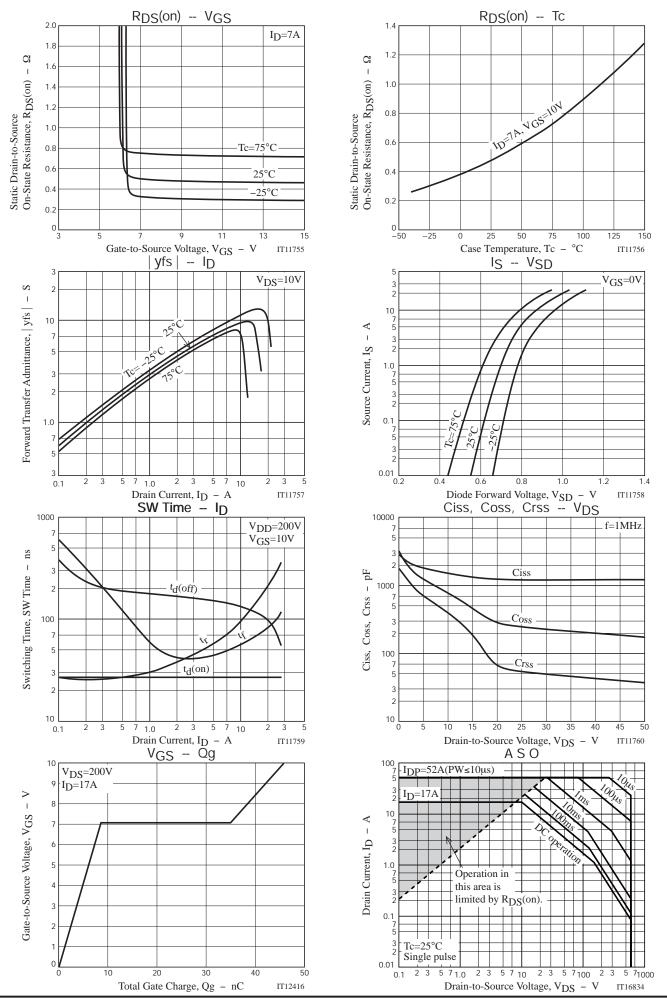


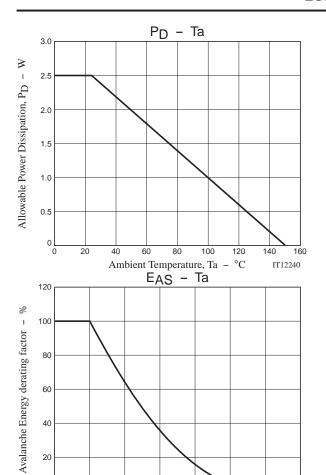
Ordering Information

Device	Package	Shipping	memo	
2SK4125-1E	TO-3P-3L	30pcs./magazine	Pb Free	







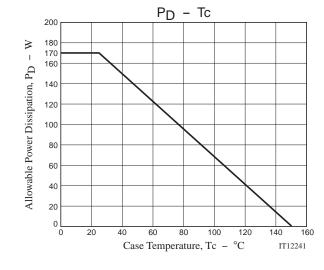


50 75 100 125 Ambient Temperature, Ta – °C

175

150 IT10478

0 0



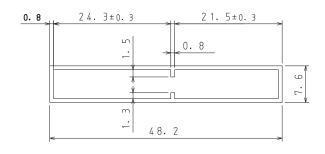
Magazine Specification

2SK4125-1E

1. Packing Format

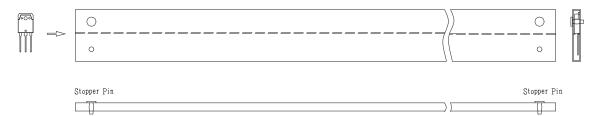
Package Name	Maximum Number of devices contained (pcs)			Packing format		
1 40 14 4 14 14 14	Magazine	Inner box	Outer box	Inner BOX	Outer BOX	
TO-3P-3L	30	450	1800	SPD-0V0001 15 magazines contained Dimensions:mm(external) 568×150×55	SPD-LV0010 4 inner boxes contained Dimensions:mm (external) 590x225x178	

2. Magazine dimensions (unit:mm)



Tolerance=±0.2mm
Thickness=0.8±0.2mm
Length =508.0±1mm
Material =PVC or PET
(Antistatic treatment)

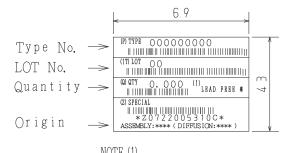
3. Storage method to magazine

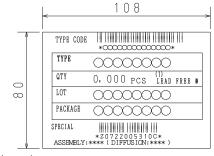


4. Inner box label (unit:mm)



It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



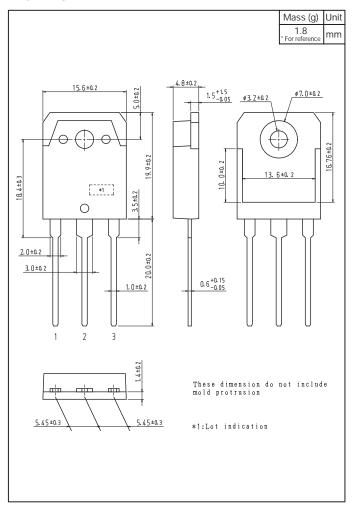


The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase
LEAD FREE	3	JEITA Phase 3A

Outline Drawing

2SK4125-1E



Note on usage: Since the 2SK4125 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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