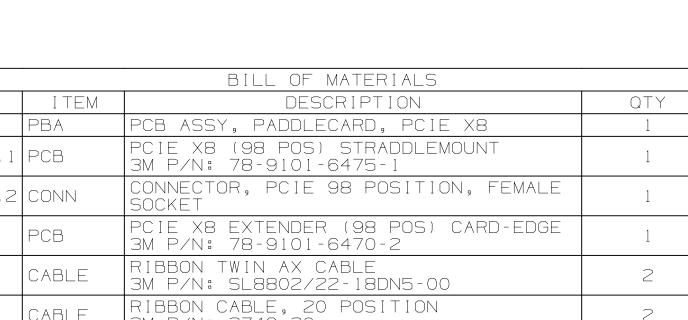
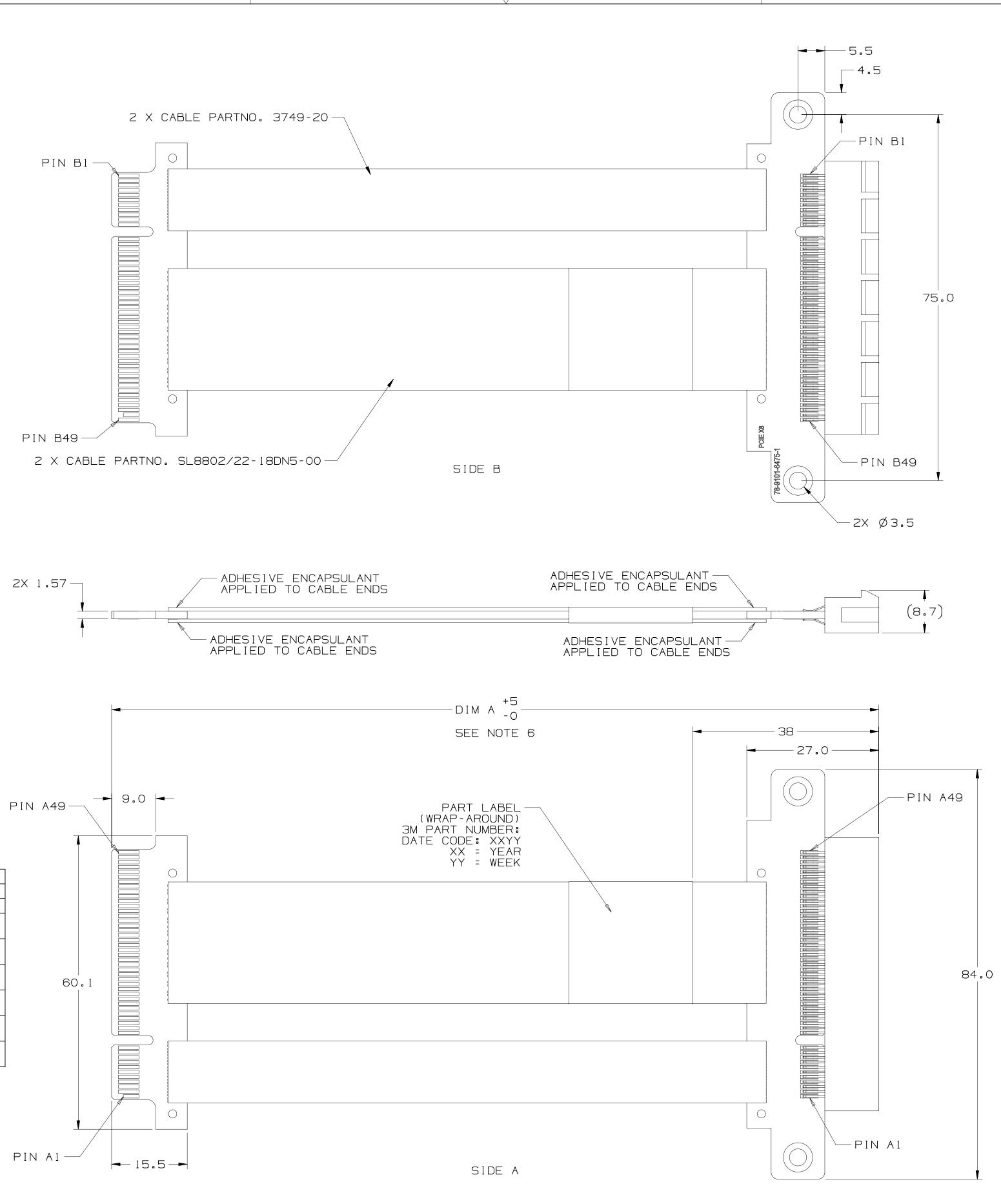
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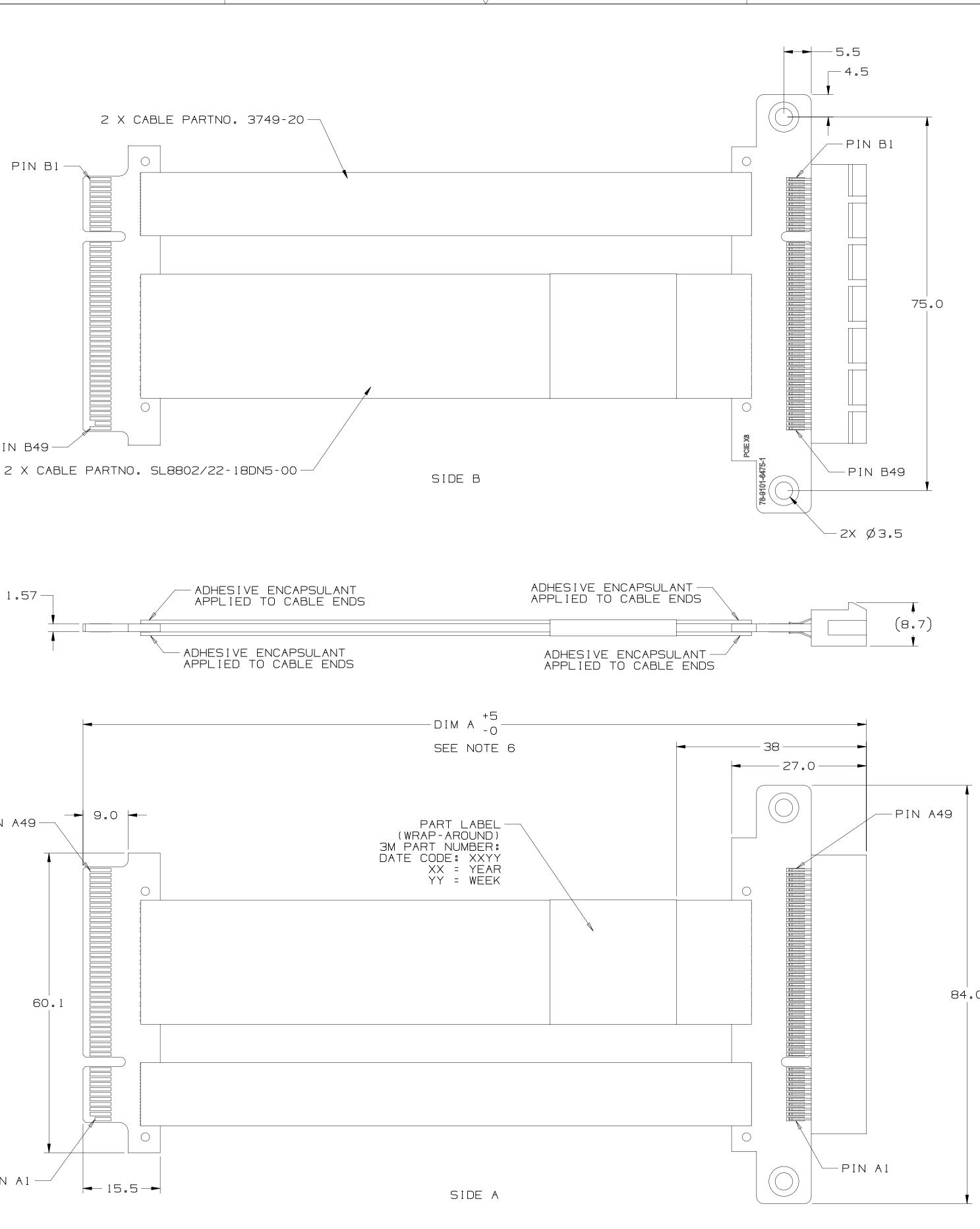
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BILL OF MATERIALS								
	I TEM DESCRIPTION							
1.0	PBA	PCB ASSY, PADDLECARD, PCIE X8	1					
1.1	РСВ	PCIE X8 (98 POS) STRADDLEMOUNT 3M P/N: 78-9101-6475-1	1					
1.2	CONN	CONNECTOR, PCIE 98 POSITION, FEMALE Socket	1					
2.0	РСВ	PCIE X8 EXTENDER (98 POS) CARD-EDGE 3M P/N: 78-9101-6470-2	1					
3.0	CABLE	RIBBON TWIN AX CABLE 3M P/N: SL8802/22-18DN5-00	2					
4.0	CABLE	RIBBON CABLE, 20 POSITION 3M P/N: 3749-20	2					
5.0	STRAIN Relief	ADHESIVE	AS REQUIRED					
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3M™ TWIN AXIAL CABLE ASSEMBLY For PCI⊖ X8 EXTENDER CARD APPLICATIONS

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	1	NOTES	
1	DIN	MENSIONS ARE IN MILLIMETERS.	
2	ЗО IMF OVE	M RIBBON TWINAX DESCRIPTION: AWG, SILVER PLATED SIGNAL WIRE PEDANCE: 85 ±5 OHM ERALL RIBBON WIDTH: 24.90 MM ERALL RIBBON THICKNESS: 0.75 MM	
Э	INF CON WWW	HS COMPLIANT. SEE REGULATORY FORMATION APPENDIX IN "ROHS MPLIANCE" SECTION AT V.3MCONNECTORS.COM & C1 APPLY)	
4	HAS EXF SHC EDC THE INS AT NEE	IS CABLE CONSTRUCTION A THIN ALUMINUM INNER LAYER POSED AT EACH EDGE. USERS OULD ASSESS WHETHER THE EXPOSED DE PRESENTS A SHORTING RISK IN EIR SPECIFIC APPLICATION. GULATING TAPE MAY BE APPLIED THE CABLE ASSEMBLY LEVEL, AS EDED, TO COVER THE EXPOSED DE IN RISK AREAS.	
5	PRC	PLICABLE SPECIFICATIONS: DDUCT SPECIFICATION NUMBER: 0106	
6		DER BY APPLICABLE 3M PART NUMBER: 12-0723-XXXX	
	×××	(X = DIM 'A' IN MILLIMETERS (1000 = 1 METER)	
	8KF	NDARD LENGTH (DIM 'A') 12-0723-0250 (250 MM) 12-0723-0500 (500 MM)	
	UPC	N-STANDARD LENGTHS AVAILABLE ON SPECIAL REQUEST. MAY REQUIRE GHER MOQS AND LONGER LEAD TIMES. O MM MINIMUM LENGTH.	$\square$
7	TW I PCE	AMMABILITY RATING: IN AX CABLE: UL94 HB 3S: UL94V-O HESIVE STRAIN RELIEF: UL94 HB, 3V)	
8	O.7 HAF	DDLECARD FINGER GOLD THICKNESS: 76 µm [30 µ"] MIN ELECTROLYTIC RD GOLD OVER 1.27 µm [50 µ"] N ELECTROLYTIC NICKEL.	

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			С	42467	AUG OE	<b>,</b> 2012	LDS	TS	
					REVISED NOTE 6				
			В	40306	JUN 18	3,2012	LDS	TS	
					REVISED > REVISED \	K8 PCB SHAPE; /iew			
					ORIENTATION; REVISED NOTE ORIENTATION				
			A		MAR OE	3,2012	ARM	TS	
_		ASSEMBLY	REV	ECO	ISSUE DATE AN	ND DESCRIPTION	DRFT	СНКД	
	DISTRIBUTION CODES			CHMIDT	MAR 08,2012	MFG	DATE		
			снкр		DATE	APPVL	DATE		А
[	DIVISION DIVISION CODE			© 3M COPYRIGHT 2012					
	DO NOT SCALE SCALE <u>2</u>	TOLERANCES EXCEPT AS NOTED	E		Paul, 3M property ar distributed wi 55144 disclosed other All rights res	and the information it on a may not be reproduced thout 3M permission, or r than for 3M authorized perved.	or furth used or d purpose	er s.	
	$\begin{array}{c c} \hline \\ \hline $	.0 .00			-0723->	XXXX, C	ABI		
	THIRD ANGLE PROJECTION	.000 ±	A	SSY	, PCIE	X8 BY	PC	ΙE	
	INTERPRET PER ASME Y14.5 - 1994	MILLIMETERS 0 ± 1			X8 FX	IENDER			
	MAX SURFACE ROUGHNESS $125$ / $\Box$ All Surfaces	.0 ±.5 .00 ±.05 .000 ±.005		IBER SIZE	drawing no. 78-510(	0-2511-	3	REV.	
	MARKED ONLY	ANGLES ±1 °	MODI	EL		ET. Ists □yes⊠no S	SHT 1	OF <b>2</b>	
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	3M 3749-20 Cable							3M 3749-20 Cab	
Wire #								Pin Attachment #	Wire
01	Ground Layer							Ground Layer	01
02	B01 - B02							A01	02
03	B01 - B02							Ground Layer	03
04	B01 - B02			PCI-Express	X8 Pin-Out			A02 - A03	04
05	B01 - B02	$\neg$	Pin #	Side B Description	Side A Description	Pin #		A02 - A03	05
06	B01 - B02	$\neg$			Hot plug presence		1 /	A02 - A03	06
07	B01 - B02	_	BO 1	+12 volt power	detect	AO 1			07
		_	B02	+12 volt power	+12 volt power	A02		A02 - A03	
08	Ground Layer		B03	+12 volt power	+12 volt power	EOA		Ground Layer	08
09	B05		B04	Ground	Ground	A04	_	A05	09
10	B06		B05	SMBus clock	ТСК	A05	_	A06	10
	Ground Layer		B06	SMBus data	TDI	A06		A07	
12	B08		В07	Ground	ТДО	A07		A08	12
13	B08	E	B08	+3.3 volt power	TMS	A08	-	Ground Layer	13
14	B09		B09	+TRST#	+3.3 volt power	A09		A09 - A10	14
15	B10		>B10	3.3v volt power	+3.3 volt power	A10		A09 - A10	15
16	B10		B11	Link Reactivation	Power Good	A 1 1	-	A09 - A10	16
17	Ground Layer			Mechani		1,,,,,,	1 <sup>∞</sup>	A09 - A10	17
18	B11		<b>B</b> 12	Reserved	Ground	A12	1	Ground Layer	18
19	Ground Layer		B13	Ground	Reference Clock	A13	1	Ground Layer	19
20	B12		B14	Transmitter Lane 0,	Differential pair	A14	-	A 1 1	20
		,	B15	Differential pair	Ground	A15	-		
	3M SL8802 Cable	$\neg$	B16	Ground	Receiver Lane 0,	A16	_	3M SL8802 Cab	
Wire #			B17	Hotplug detect	Differential pair	A17		Pin Attachment #	Wire
GND	Ground Layer			Ground	Ground	A18	-	Ground Layer	GND
pr_01	B14	-	B18				-	A13	pr_01
pr_01 pr_01	B15		B19	Transmitter Lane 1,	Reserved	A19	-	A14	pr_01
pr_01 pr_02	B17	- /	B20	Differential pair	Ground	A20	-	A16	pr02
pr_02 pr_02	Ground Layer		B21	Ground	Receiver Lane 1,	A21		A17	pr02
	B19	-	B22	Ground	Differential pair	A22			
pr_03	B20		B23	Transmitter Lane 2,	Ground	A23		Ground Layer	03
<u>pr_03</u>	B23	_	B24	Differential pair	Ground	A24	-	A19	pr_03
<u>pr_04</u>	B24		B25	Ground	Receiver Lane 2,	A25		A21	pr_04
pr_04			B26	Ground	Differential pair	A26		A22	pr_04
GND	Ground Layer	E	B27	Transmitter Lane 3,	Ground	A27		Ground Layer	GND
<u>pr_05</u>	B27		B28	Differential pair	Ground	A28		A25	pr05
<u>pr_05</u>	B28		B29	Ground	Receiver Lane 3,	A29		A26	05
pr_06	B30		B30	Reserved	Differential pair	0EA		A29	06
pr_06	B31		B31	Hot plug detect	Ground	A31	_	A30	pr06
pr_07	B33		B32	Ground	Reserved	A32	_	A32	07
pr_07	B34		B33	Transmitter Lane 4,	Reserved	A33	4	A33	pr_07
GND	Ground Layer		B34	Differential pair	Ground	A34	4	Ground Layer	GND
pr_08	B37	$\rightarrow$	B35	Ground	Receiver Lane 4,	A35		A35	08
pr_08	B38		B36	Ground	Differential pair	A36	4	A36	pr_08
pr_09	B41		B37	Transmitter Lane 5,	Ground	A37		A39	pr09
pr_09	B42		B38	Differential pair	Ground	A38		A40	09
or_10	B45	$ \rightarrow $	B39	Ground	Receiver Lane 5,	A39		A43	pr_10
pr_10	846		B40	Ground	Differential pair	A40		A44	pr_10
or_11	B48		B41	Transmitter Lane 6,	Ground	A41		A47	pr_11
or_11	Ground Layer		B42	Differential pair	Ground	A42		A48	pr1
GND	Ground Layer		B43	Ground	Receiver Lane 6,	A43		Ground Layer	GND
			B44	Ground	Differential pair	A44	7 /		
			B45	Transmitter Lane 7,	Ground	A45	7 /		
			B46	Differential pair	Ground	A46	1 /		
			B47	Ground	Receiver Lane 7,	A47			
			B48	Hot plug detect	Differential pair	A48			
			B49	Ground	Ground	A49	-		

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3M 3749-20 Cab Pin Attachment #	Wire #
Ground Layer	01
AO 1	02
Ground Layer	03
A02 - A03	04
A02 - A03	05
A02 - A03	06
A02 - A03	07
Ground Layer	08
A05	09
A06	10
A07	1 1
808	12
Ground Layer	13
A09 - A10	14
A09 - A10	15
A09 - A10	16
A09 - A10	17
Ground Layer	18
Ground Layer	19
A 1 1	20

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C 42467 AUG 06,2012 LDS TS   REVISED NOTE 6 B 40306 JUN 18,2012 LDS TS   B 40306 JUN 18,2012 LDS TS   REVISED X8 PCB SHAPE; REVISED VIEW ORIENTATION; REVISED REVISED VIEW ORIENTATION; REVISED TS   DESIGN REFERENCE NEXT ASSEMBLY REV ECO ISSUE DATE AND DESCRIPTION DRT CHAD   DESIGN REFERENCE NEXT ASSEMBLY REV ECO ISSUE DATE AND DESCRIPTION DRT CHAD   DIVISION DIVISION CODE DATE OHD DATE DATE DATE   DO NOT SCALE TOLERANCES EXCEPT AS NOTE DATE <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th>									
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Design reference Next Assembly   Division Division code   Division Division code   Scale Tolerances   Scale Tolerances   Note Note   Note Note   Scale Tolerances   Note Note   Note Nothorized purposes.					REVISED N	NOTE 6			
REVISED VIEW ORIENTATION: REVISED NOTE ORIENTATION: REVISED NOTE ORIENTATION   A MAR 08,2012 ARM TS   DESIGN REFERENCE NEXT ASSEMBLY REV ECO ISSUE DATE AND DESCRIPTION DRT CHKD   DISTRUCTION DIVISION COCE DATE MAR 08,2012 ARM TS   DIVISION DIVISION COCE DATE OPPL DATE   DO NOT SCALE TOLERANCES EXCEPT AS   SCALE TOLERANCES EXCEPT AS MM ESI44   DRAWING INCHES TITLE MM SSI44   MAX SURFACE ROUGHNESS 0 ±1   NAX SURFACE ROUGHNESS 0 ±1   125 ALL SURFACES OUS ±.005 MODEL				40306	JUN 18	,2012	LDS	TS	
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V MARKED ONLY ANGLES ±1 • LISTS UYES NO SHT 2 OF 2	DO NOT SCALE DRAWING 1	TOLERANCES EXCEPT AS NOTED INCHES .0 ± .00 ± .00 ± .00 ± .00 ± .00 ± .00 ± .00 ± .5 .00 ± .05 .000 ± .005	CA NUM	KHZ SSY	This document Poul, 55144 This document M property and distributed wi disclosed othe All rights res PCIE X8 EX DRAWING NO. 78-5100	and the information it of d may not be reproduced thout 3M permission, or r than for 3M authorized erved. XXX, C X8 BY FENDER 0 - 2511 -	ABL PC	er s. IE REV.	

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