## GaAs SPST Switch

## Features

- Very Low Power Consumption: $50 \mu \mathrm{~W}$
- Low Insertion Loss: 1.0 dB
- High Isolation: 35 dB up to 2 GHz
- Very High Intercept Point: 46 dBm IP3
- Nanosecond Switching Speed
- Temperature Range: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
- Lead-Free SOIC-8 Plastic Package
- 100\% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- $260^{\circ} \mathrm{C}$ Reflow Compatible
- RoHS* Compliant Version of SW-259


## Description

The MASWSS0162 is a GaAs MMIC SPST switch in a lead-free SOIC-8 lead surface mount plastic package.

The MASWSS0162 is ideally suited for use where low power consumption is required. Typical applications include transmit/receive switching, switch matrices and switched filter banks in systems such as radio and cellular equipment, PCM, GPS, fiber optic modules, and other battery powered radio equipment.

The MASWSSO162 is fabricated using a monolithic GaAs MMIC using a mature 1 micron process. The process features full chip passivation for increased performance and reliability.

## Ordering Information ${ }^{1}$

| Part Number | Package |
| :---: | :---: |
| MASWSS0162 | Bulk Packaging |
| MASWSS0162TR | 1000 piece reel |

1. Reference Application Note M513 for reel size information.

## Functional Schematic



## Pin Configuration

| Pin\# | Description |
| :---: | :---: |
| 1 | Ground |
| 2 | A |
| 3 | B |
| 4 | Ground |
| 5 | RF Port 2 |
| 6 | Ground |
| 7 | Ground |
| 8 | RF Port 1 |

[^0]Electrical Specifications ${ }^{2}: \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{C}}=-5 \mathrm{~V} / 0 \mathrm{~V}, \mathrm{P}_{\mathrm{IN}}=0 \mathrm{dBm}$

| Parameter | Test Conditions | Units | Min. | Typ. ${ }^{3}$ | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss ${ }^{3}$ | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & 0.5-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \end{aligned}$ | dB | - | $\begin{aligned} & 0.8 \\ & 0.9 \\ & 1.1 \end{aligned}$ | $\overline{1.2}$ |
| Isolation ${ }^{3}$ | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & 0.5-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \end{aligned}$ | dB | $45$ | $\begin{aligned} & 65 \\ & 53 \\ & 40 \end{aligned}$ | - |
| VSWR On VSWR Off | DC - 2.0 GHz | Ratio | - | 1.2:1 | - |
| 1 dB Compression | $\begin{gathered} \mathrm{P}_{\text {IN }} @ 0.05 \mathrm{GHz} \\ \mathrm{P}_{\mathrm{IN}} @ 0.5-2.0 \mathrm{GHz} \end{gathered}$ | dBm | - | $\begin{aligned} & 18 \\ & 23 \end{aligned}$ | - |
| $\mathrm{T}_{\text {RISE }}, \mathrm{T}_{\text {FALL }}$ | 10\% to $90 \%$ RF, $90 \%$ to $10 \%$ RF | ns | - | 4 | - |
| Ton, ${ }_{\text {OfF }}$ | 50\% Control to 90\% RF, 50\% Control to 10\% RF | ns | - | 8 | - |
| Transients | In-Band | mV | - | 35 | - |
| 2nd Order Intercept | Measured Relative to Input Power, two-tone up to 5 dBm $\begin{gathered} 0.05 \mathrm{GHz} \\ 0.5-2.0 \mathrm{GHz} \end{gathered}$ | dBm | - | $\begin{aligned} & 55 \\ & 68 \end{aligned}$ | - |
| 3rd Order Intercept | Measured Relative to Input Power, two-tone up to 5 dBm $\begin{gathered} 0.05 \mathrm{GHz} \\ 0.5-2.0 \mathrm{GHz} \end{gathered}$ | dBm | - | $\begin{aligned} & 40 \\ & 46 \end{aligned}$ | - |
| Control Current | - | $\mu \mathrm{A}$ | - | - | 25 |

2. All measurements with $0,-5 \mathrm{~V}$ control voltages at 1.0 GHz in a $50 \Omega$ system, unless otherwise specified.
3. Typical values listed are based on average of frequency range noted.

## Absolute Maximum Ratings ${ }^{4,5}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power |  |
| 0.05 GHz |  |
| $0.5-2.0 \mathrm{GHz}$ | 34 dBm |
| Control Voltage | $+5 \mathrm{~V},-8.5 \mathrm{~V}$ |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

## Truth Table ${ }^{7,8}$

| Control Inputs |  | Condition of Switch |
| :---: | :---: | :---: |
| A | B | RF State |
| 1 | 0 | On |
| 0 | 1 | Off |

7. " 0 " $=0$ to $-0.2 \mathrm{~V} @ 20 \mu \mathrm{~A}$ maximum.
8. "1" = $-5 \mathrm{~V} @ 20 \mu \mathrm{~A}$ typical to $-8 \mathrm{~V} @ 600 \mu \mathrm{~A}$ maximum.
9. Exceeding any one or combination of these limits may cause permanent damage to this device.
10. MACOM does not recommend sustained operation near these survivability limits.
11. When the RF Input power is applied to a terminated port, the absolute maximum is +32 dBm .

## GaAs SPST Switch

## Electrical Schematic



## Typical Performance Curves

Insertion Loss


Isolation


VSWR


## Lead-Free SOIC-8 ${ }^{\dagger}$


† Reference Application Note M538 for lead-free solder reflow recommendations.

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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

