

# HSMP-3866

Quad PIN Diode Pi Attenuator 300 kHz to 3 GHz  
in SOT 25 Package



## Data Sheet



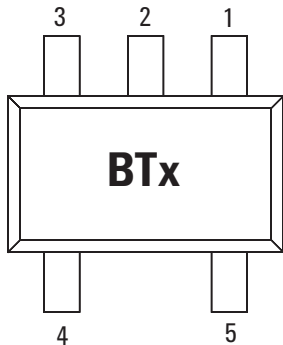
### Description

Avago Technology's HSMP-3866 is a wideband, low insertion loss, low current, Quad PIN Diode Pi Attenuator in a low cost surface mount SOT-25 package. It provides a good match and flat attenuation over an extremely wide band from 300 kHz to 3 GHz.

The SOT-25 packages gives a reduction in part count and takes up less space on board compared to multi package solutions.

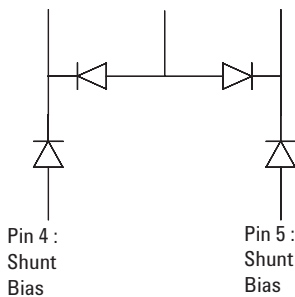
Four PIN Diodes in one package encourages performance repeatability for improved production yield at board level.

### Package Marking and Pin connections



Note:  
Package marking provides orientation and identification  
"BT" = Device Code  
"x" = Month code indicates the month of manufacture

Pin 3 : RF In/Out  
Pin 2 : Series Bias  
Pin 1 : RF In/Out



### Features

- 4 PIN Diodes in a SOT-25 package
- 300 kHz to 3 GHz usable frequency band
- Low Current
- Low insertion Loss
- MSL-1 and Lead-free
- Tape & Reel packaging option available

### Specification At 1 GHz, V+=1.2V

- IIP3 = 30 dBm (Typical)
- Attenuation = 36 dB (Typical)
- Insertion Loss = -2.5 dB (Typical)
- Return Loss = -18 dB (Typical)

### Application

- Broadband system application (i.e., CATV, WCDMA, etc)
- General purpose Voltage-Control-Attenuator for low current applications.

### Absolute Max Ratings <sup>[1]</sup>, Tc = +25°C

Symbol	Parameter	Unit	Abs Max
I <sub>f</sub>	Forward Current (1 μs Pulse)	Amp	1
P <sub>IV</sub>	Peak Inverse Voltage	V	50
T <sub>j</sub>	Junction Temperature	°C	150
T <sub>stg</sub>	Storage Temperature	°C	-65 to 150
θ <sub>jb</sub>	Thermal Resistance <sup>[2]</sup>	°C/W	167

Notes :

1. Operation in excess of any one of these conditions may result in permanent damage to the device.
2. Thermal Resistance is measured from junction to board using IR method.

### Electrical Specifications, Tc = +25°C (Each Diode)

	Minimum Breakdown Voltage V <sub>BR</sub> (V)	Typical Series Resistance R <sub>S</sub> (Ω)	Typical Total Capacitance C <sub>T</sub> (pF)
	<b>50</b>	<b>3.0/1.5*</b>	<b>0.22</b>
Test Condition	V <sub>R</sub> = V <sub>BR</sub> Measure I <sub>R</sub> ≤ 10 μA	I <sub>F</sub> = 10 mA, f = 100 MHz I <sub>F</sub> = 100 mA*	V <sub>R</sub> = 50 V f = 1 MHz

### Typical Performance, Tc = +25°C (Each Diode)

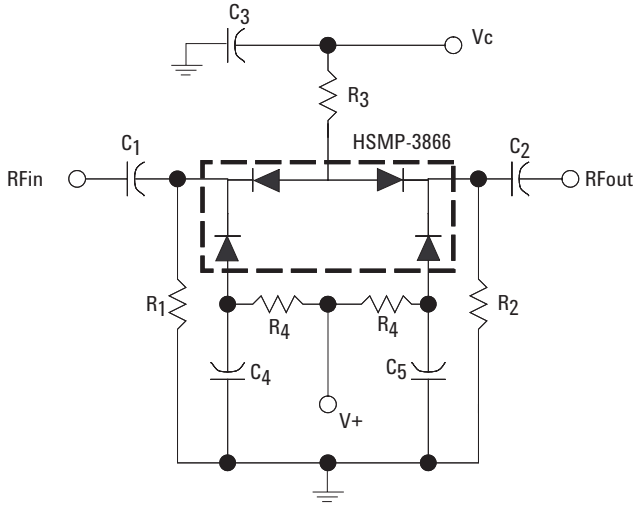
	Total Resistance R <sub>T</sub> (Ω)	Carrier Lifetime τ (ns)	Reverse Recovery Time T <sub>rr</sub> (ns)	Total Capacitance C <sub>T</sub> (pF)
	<b>22</b>	<b>500</b>	<b>80</b>	<b>0.22</b>
Test Condition	I <sub>F</sub> = 1 mA f = 1 MHz	I <sub>F</sub> = 10mA I <sub>R</sub> = 250 mA	V <sub>R</sub> = 50 V I <sub>F</sub> = 10 mA 90% Recovery	V <sub>R</sub> = 50V f = 1MHz

### Typical Performance for HSMP-3866 Quad PIN Diode π Attenuator @ +25°C

Parameter	Test Condition	Units	Typical
Insertion Loss	V <sub>c</sub> = 5V, V <sub>+</sub> = 1.2V, Freq = 1 GHz	dB	-2.5
Return Loss	V <sub>c</sub> = 0V, V <sub>+</sub> = 1.2V, Freq = 1 GHz	dB	-18
Attenuation	V <sub>c</sub> = 0V, V <sub>+</sub> = 1.2V, Freq = 1 GHz	dB	36
IP3	V <sub>c</sub> = 1.5V, V <sub>+</sub> = 1.2V, Freq = 1 GHz	dBm	30
IP3	V <sub>c</sub> = 5.0V, V <sub>+</sub> = 1.2V, Freq = 1 GHz	dBm	40
IP3	V <sub>c</sub> = 1.5V, V <sub>+</sub> = 1.2V, Freq = 300 MHz	dBm	25
IP3	V <sub>c</sub> = 5.0V, V <sub>+</sub> = 1.2V, Freq = 300 MHz	dBm	37
IP3	V <sub>c</sub> = 1.5V, V <sub>+</sub> = 1.2V, Freq = 100 MHz	dBm	23
IP3	V <sub>c</sub> = 5.0V, V <sub>+</sub> = 1.2V, Freq = 100 MHz	dBm	35

Notes :

1. Measurement above obtained using Wideband RF circuit design shown in Figure 1 & 2



Component	Value
R1, R2	620 Ω
R3	390 Ω
R4	2200 Ω
C1 - C5	10 nF

Figure 1. Wideband Quad PIN Diode  $\pi$  Attenuator Circuit

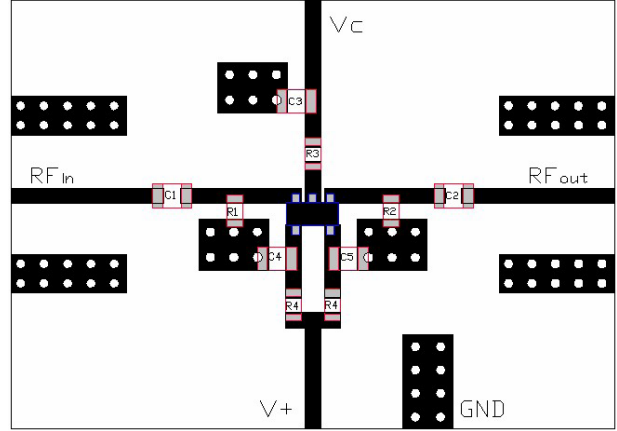


Figure 2. Circuit Board Layout.

### Typical Performance Curves for Single Diode @ $T_c = +25^\circ\text{C}$

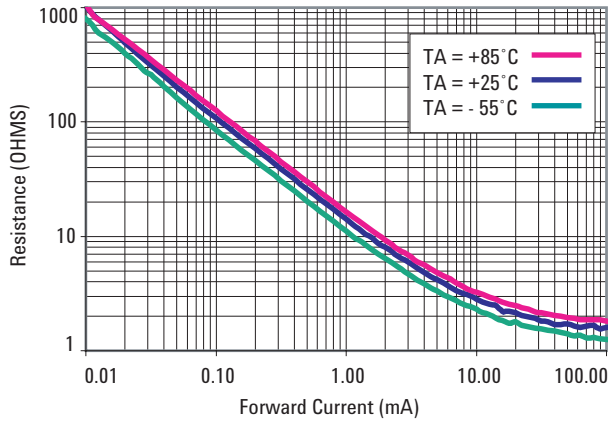


Figure 3. RF Resistance vs. Forward Bias Current

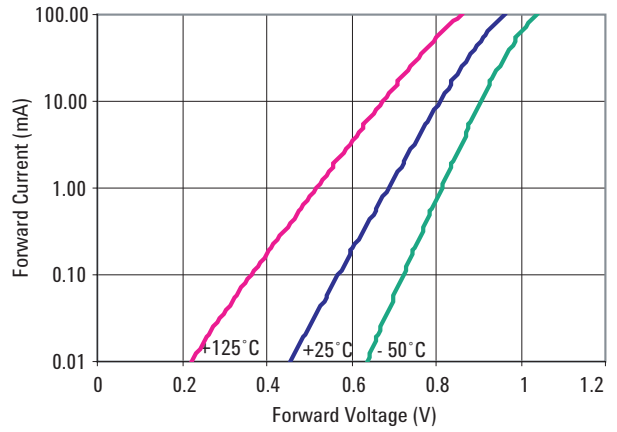


Figure 4. Forward Current vs. Forward Voltage

### Typical Performance Curves for Single Diode @ $T_c = +25^\circ\text{C}$

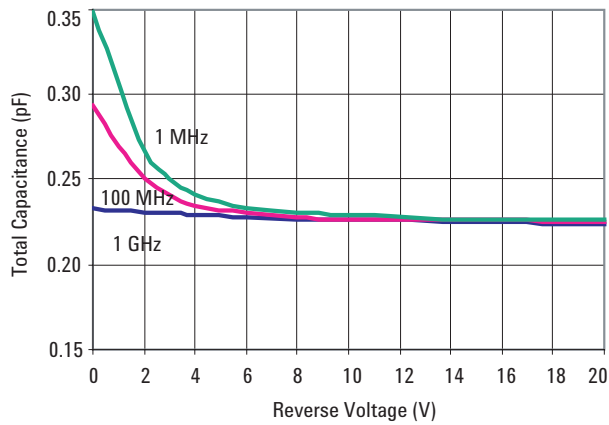


Figure 5. RF Capacitance vs Reverse Bias

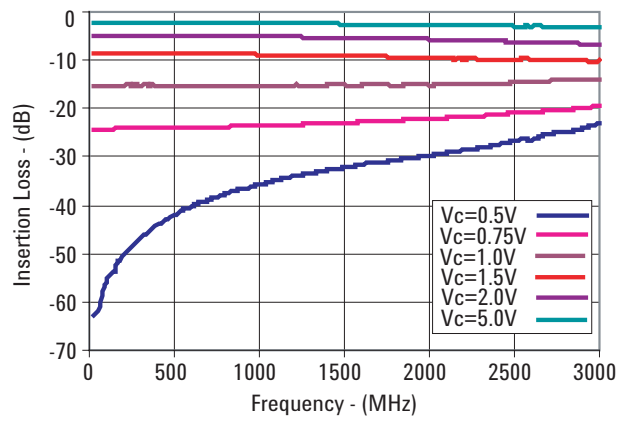


Figure 6. Insertion Loss vs. Frequency

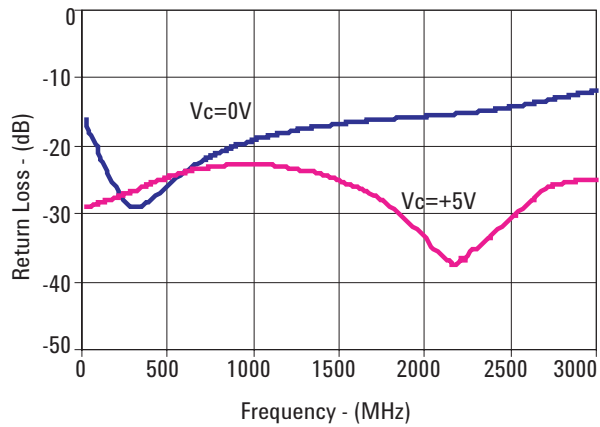


Figure 7. Return Loss vs. Frequency

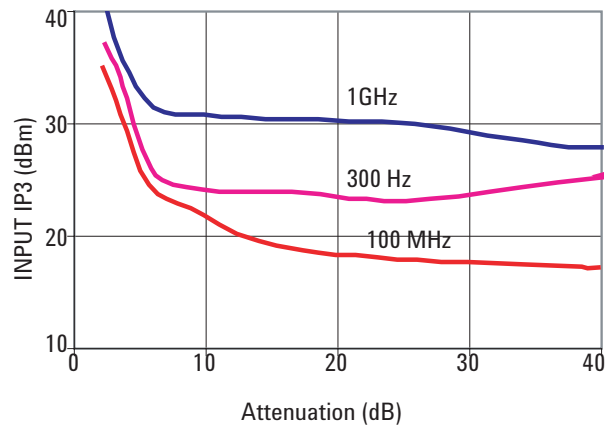


Figure 8. Input IP3 vs. Attenuation

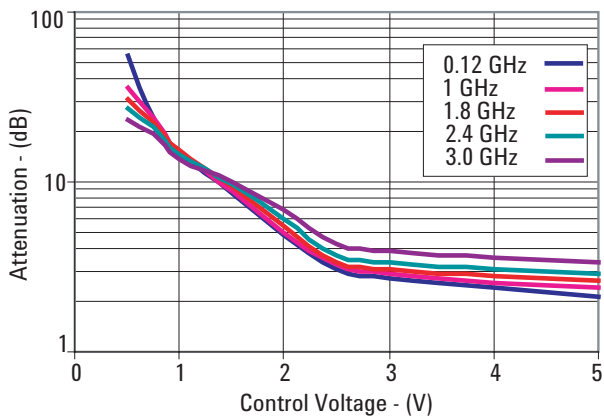
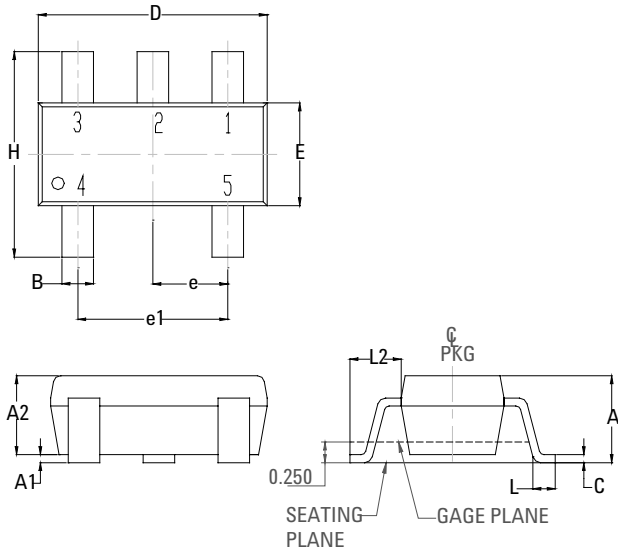


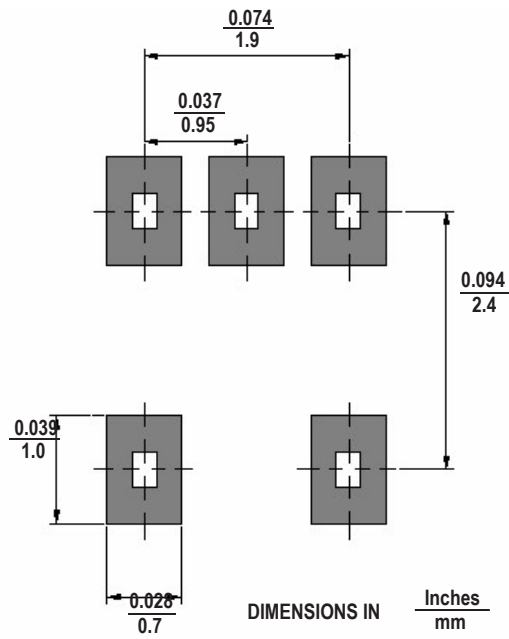
Figure 9. Attenuation vs. Control Voltage

## Package Outline & Dimension

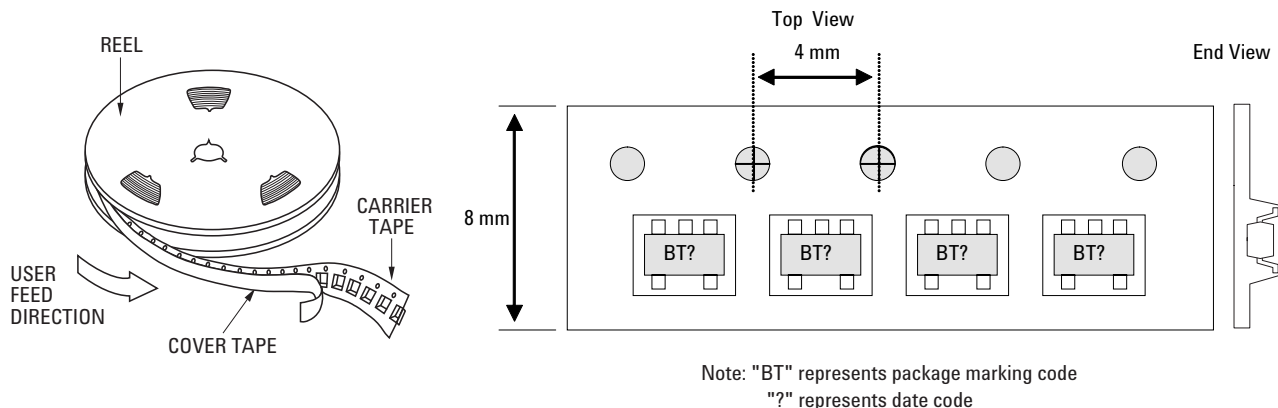


Symbol	Dimension		
	Minimum	Nominal	Maximum
D	2.80	2.90	3.00
H	2.60	2.80	3.00
E	1.50	1.60	1.70
e1	1.88	1.90	1.92
e	0.93	0.95	0.97
B	0.35		0.50
A2	0.9	1.15	1.30
C	0.08		0.22
L	0.35		0.60
A1	0		0.15
A	0.9		1.40

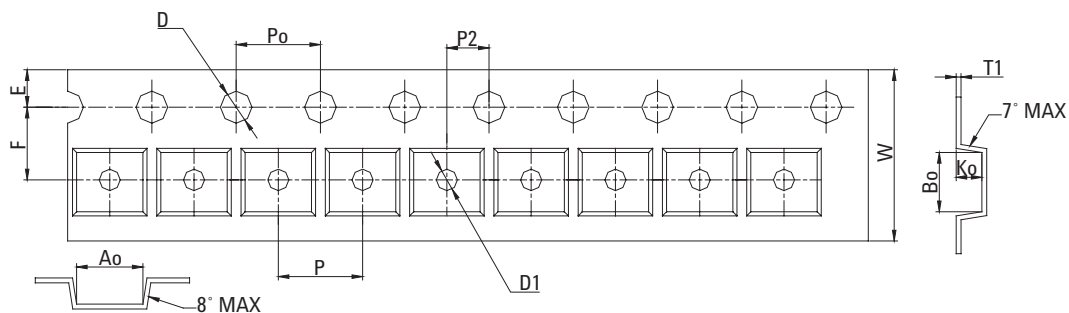
## PCB Footprint



## Device Orientation



## Tape Dimension



Symbol	Millimeters		
	Nominal	Minimum	Maximum
V	8.00 (0.315)	7.90 (0.311)	8.30 (0.327)
P	4.00 (0.157)	3.90 (0.154)	4.10 (0.161)
E	1.75 (0.069)	1.65 (0.065)	1.85 (0.073)
F	3.50 (0.138)	3.45 (0.136)	3.55 (0.140)
J	1.50 (0.059)	N/A	1.60 (0.063)
J1	1.00 (0.039)	N/A	1.25 (0.049)
Po	4.00 (0.157)	3.90 (0.154)	4.10 (0.161)
P2	2.00 (0.079)	1.95 (0.077)	2.05 (0.081)
Ao	3.23 (0.127)	3.13 (0.123)	3.33 (0.131)
Jo	3.81 (0.125)	3.08 (0.121)	3.28 (0.129)
Fo	1.60(0.063)	1.50 (0.059)	1.70 (0.067)
T1	0.254 (0.0100)	0.241 (0.0095)	0.267 (0.0105)

## Part Number Ordering Information

Part number	No of Device	Container
HSMP-3866-BLKG	100	Anti-static bag
HSMP-3866-TR1G	3000	7" reel
HSMP-3866-TR2G	10000	13" reel

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