

A Business Partner of Renesas Electronics Corporation. CEL California Eastern Laboratories

# μPG2214TB

GaAs Integrated Circuit for L, S-Band SPDT Switch

Data Sheet R09DS0050EJ0400 Rev.4.00 Sep 10, 2012

# DESCRIPTION

The  $\mu$ PG2214TB is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which was developed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 5.3 V. This device can operate frequency from 0.05 to 3.0 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin super minimold package. And this package is able to high-density surface mounting.

# FEATURES

•	Switch control voltage	: Vcont (H) = 1.8 to 5.3 V (3.0 V TYP.)
		: Vcont (L) = -0.2 to +0.2 V (0 V TYP.)
•	Low insertion loss	: Lins1 = 0.25 dB TYP. @ f = 0.05 to 0.5 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: Lins2 = 0.25 dB TYP. @ f = 0.5 to 1.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: Lins3 = 0.30 dB TYP. @ f = 1.0 to 2.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: Lins4 = 0.35 dB TYP. @ f = 2.0 to 2.5 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: Lins5 = 0.35 dB TYP. @ f = 2.5 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
•	High isolation	: ISL1 = 32 dB TYP. @ f = 0.05 to 0.5 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: ISL2 = 28 dB TYP. @ f = 0.5 to 1.0 GHz, $V_{cont}$ (H) = 3.0 V, $V_{cont}$ (L) = 0 V
		: ISL3 = 27 dB TYP. @ f = 1.0 to 2.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
		: ISL4 = 26 dB TYP. @ f = 2.0 to 2.5 GHz, V cont (H) = 3.0 V, V cont (L) = 0 V
		: ISL5 = 24 dB TYP. @ f = 2.5 to 3.0 GHz, Vcont (H) = 3.0 V, Vcont (L) = 0 V
•	Handling power	: Pin (1 dB) = +27.0 dBm TYP. @ f = 0.5 to 3.0 GHz, $V_{cont}$ (H) = 3.0 V, $V_{cont}$ (L) = 0 V
		: Pin (1 dB) = +20.0 dBm TYP. @ f = 0.5 to 3.0 GHz, Vcont (H) = 1.8 V, Vcont (L) = 0 V
	High-density surface mounting	: 6-pin super minimold package (2.0 $\times$ 1.25 $\times$ 0.9 mm)

High-density surface mounting : 6-pin super minimold package  $(2.0 \times 1.25 \times 0.9 \text{ mm})$ 

#### APPLICATIONS

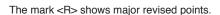
- L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth<sup>™</sup> etc.

#### <R> ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
<i>µ</i> РG2214TB-E4	6-pin super minimold (2012) (Pb-Free)	G4J	<ul> <li>Embossed tape 8 mm wide</li> <li>Pin 4, 5, 6 face the perforation side of the tape</li> <li>Qty 3 kpcs/reel</li> </ul>

**Remark** To order evaluation samples, contact your nearby sales office. Part number for sample order: µPG2214TB-A

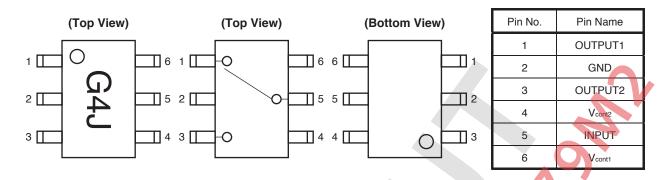
Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.



The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.



# PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



#### **TRUTH TABLE**

Vcont1	Vcont2	INPUT-OUTPUT1	INPUT-OUTPUT2	
Low	High	ON	OFF	
High	Low	OFF	ON	

# ABSOLUTE MAXIMUM RATINGS (TA = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	Vcont	+6.0 <sup>Note</sup>	V
Input Power	Pin	+30	dBm
Operating Ambient Temperature	Та	-45 to +85	°C
Storage Temperature	Tstg	-55 to +150	°C

Note  $V_{cont1} - V_{cont2} \le 6.0 V$ 

#### **RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	Vcont (H)	1.8	3.0	5.3	V
Switch Control Voltage (L)	Vcont (L)	-0.2	0	0.2	V



# **ELECTRICAL CHARACTERISTICS**

(TA = +25°C, V<sub>cont</sub> (H) = 3.0 V, V<sub>cont</sub> (L) = 0 V, DC cut capacitors = 100 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	Lins1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	-	0.25	0.45	dB
Insertion Loss 2	Lins2	f = 0.5 to 1.0 GHz	-	0.25	0.45	dB
Insertion Loss 3	Lins3	f = 1.0 to 2.0 GHz		0.30	0.50	dB
Insertion Loss 4	Lins4	f = 2.0 to 2.5 GHz	-	0.35	0.55	dB
Insertion Loss 5	Lins5	f = 2.5 to 3.0 GHz	-	0.35	0.60	dB
Isolation 1	ISL1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	29	32	$\mathbf{Y}$	dB
Isolation 2	ISL2	f = 0.5 to 1.0 GHz	25	28	-	dB
Isolation 3	ISL3	f = 1.0 to 2.0 GHz	24	27	-	dB
Isolation 4	ISL4	f = 2.0 to 2.5 GHz	23	26	-	dB
Isolation 5	ISL5	f = 2.5 to 3.0 GHz	21	24	_	dB
Input Return Loss 1	RLin1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	15	20	_	dB
Input Return Loss 2	RLin2	f = 0.5 to 3.0 GHz	15	20	-	dB
Output Return Loss 1	RLout1	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	15	20	-	dB
Output Return Loss 2	RLout2	f = 0.5 to 3.0 GHz	15	20	-	dB
0.1 dB Loss Compression	Pin (0.1 dB)	f = 2.0/2.5 GHz	+21.0	+23.0	-	dBm
Input Power Note 2		f = 0.5 to 3.0 GHz	-	+23.0	-	dBm
1 dB Loss Compression Input Power <sup>Note 3</sup>	Pin (1 dB)	f = 0.5 to 3.0 GHz	-	+27.0	-	dBm
2nd Harmonics	2fo	f = 2.0 GHz, Pin = +15 dBm	_	-55	-47	dBc
		f = 2.5 GHz, Pin = +15 dBm	-	-55	-47	dBc
3rd Harmonics	3fo	f = 2.0 GHz, Pin = +15 dBm	-	-55	-47	dBc
		f = 2.5 GHz, Pin = +15 dBm	-	-55	-47	dBc
Intermodulation Intercept Point	IIP3	f = 0.5 to 3.0 GHz, 2 tone, Pn = +16 dBm, 5 MHz spicing	-	+58	-	dBm
Switch Control Current	Icont		-	4	20	μA
Switch Control Speed	tsw	50% CTL to 90/10% RF	-	20	200	ns

Notes 1. DC cut capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

- 2. Pin (0.1 dB) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.
- **3.** Pin (1 dB) is measured the input power level when the insertion loss increases more 1 dB than that of linear range.



# **ELECTRICAL CHARACTERISTICS**

(TA = +25°C, V<sub>cont</sub> (H) = 1.8 V, V<sub>cont</sub> (L) = 0 V, DC cut capacitors = 100 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 6	Lins6	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	-	0.25	0.50	dB
Insertion Loss 7	Lins7	f = 0.5 to 1.0 GHz	-	0.25	0.50	dB
Insertion Loss 8	Lins8	f = 1.0 to 2.0 GHz		0.30	0.55	dB
Insertion Loss 9	Lins9	f = 2.0 to 2.5 GHz	-	0.35	0.60	dB
Insertion Loss 10	Lins10	f = 2.5 to 3.0 GHz	-	0.35	0.65	dB
Isolation 6	ISL6	f = 0.05 to 0.5 GHz <sup>Note 1</sup>	27	30		dB
Isolation 7	ISL7	f = 0.5 to 2.0 GHz	23	27	-	dB
Isolation 8	ISL8	f = 2.0 to 2.5 GHz	21	25	-	dB
Isolation 9	ISL9	f = 2.5 to 3.0 GHz	20	24	-	dB
Input Return Loss 3	RLin3	f = 0.05 to 3.0 GHz <sup>Note 1</sup>	15	20	-	dB
Output Return Loss 3	RL <sub>out3</sub>	f = 0.05 to 3.0 GHz <sup>Note 1</sup>	15	20	-	dB
0.1 dB Loss Compression	Pin (0.1 dB)	f = 2.0/2.5 GHz	+14.0	+17.0	-	dBm
Input Power Note 2		f = 0.5 to 3.0 GHz		+17.0	-	dBm
1 dB Loss Compression Input Power <sup>Note 3</sup>	Pin (1 dB)	f = 0.5 to 3.0 GHz	-	+20.0	-	dBm
Switch Control Current	Icont		-	4	20	μA
Switch Control Speed	tsw	50% CTL to 90/10% RF	_	20	200	ns

Notes 1. DC cut capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

- 2. Pin (0.1 dB) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.
- **3.** Pin (1 dB) is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

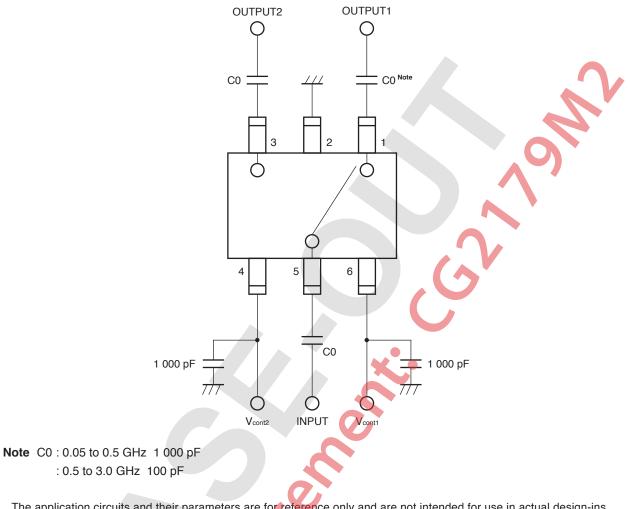
Caution This device is used it is necessary to use DC cut capacitors.

The value of DC cut capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC cut capacitor value is less than 100 pF.





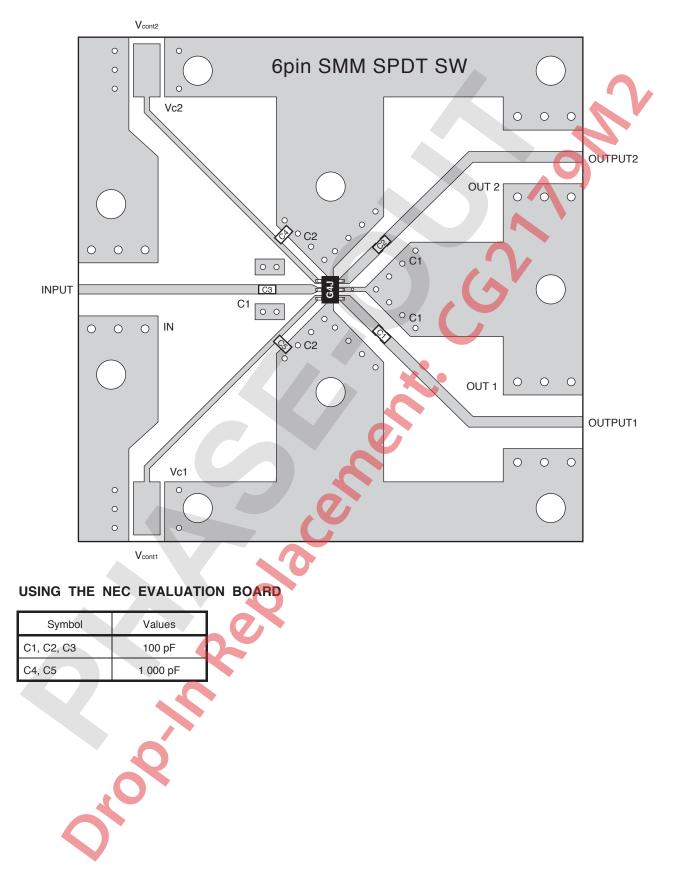
# **EVALUATION CIRCUIT**



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

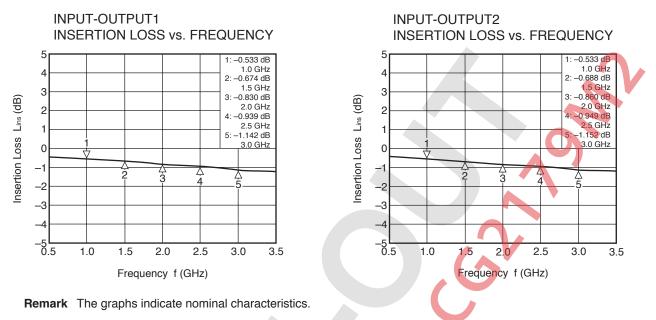


# $_{\mbox{\scriptsize R}\mbox{\scriptsize >}}$ illustration of the test circuit assembled on evaluation board



# **TYPICAL CHARACTERISTICS**

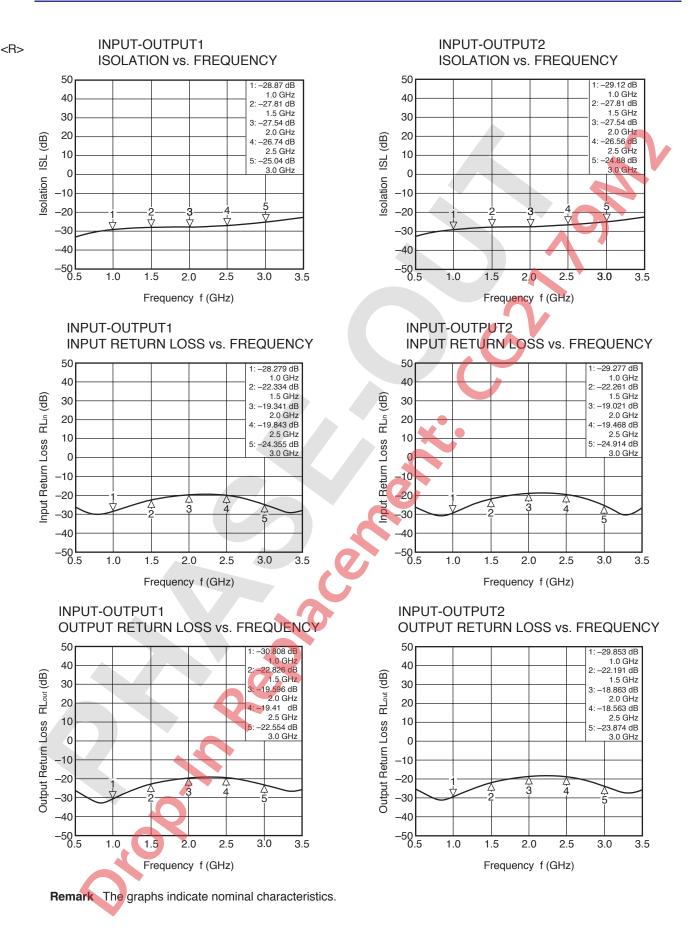
(TA = +25°C, V<sub>cont</sub> (H) = 3.0 V, V<sub>cont</sub> (L) = 0 V, DC cut capacitors = 100 pF, unless otherwise specified)



Caution These characteristics values include the losses of the NEC evaluation board.

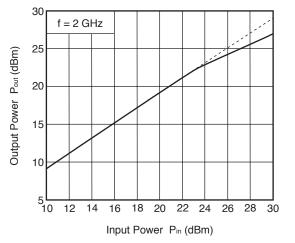


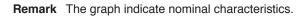






OUTPUT POWER vs. INPUT POWER





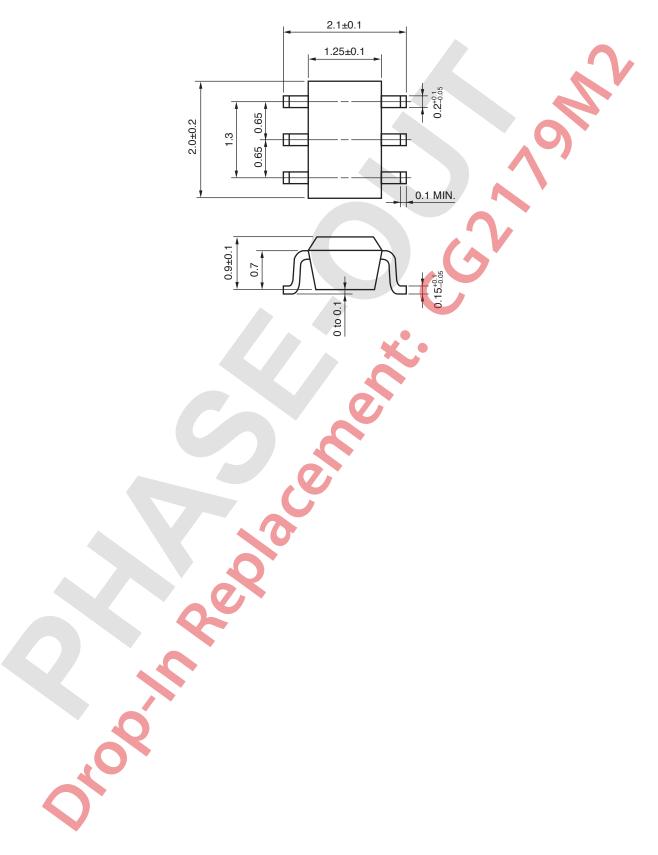
R09DS0050EJ0400 Rev.4.00 Sep 10, 2012



cerner

# PACKAGE DIMENSIONS

# 6-PIN SUPER MINIMOLD (UNIT: mm)





# **RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Soldering Conditions			
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260		
VPS	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215		
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260		
Partial Heating	Peak temperature (pin temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	350°C or below : 3 seconds or less :0.2%(Wt.) or below	H\$350		

Caution Do not use different soldering methods together (except for partial heating).





Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	<ol><li>Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.</li></ol>
	Do not burn, destroy, cut, crush, or chemically dissolve the product.
	Do not lick the product or in any way allow it to enter the mouth.

×·



**Revision History** 

# μPG2214TB Data Sheet

×

		Description		
Rev.	Date	Page	Summary	
1.00	Mar 10, 2004	-	First edition issued	
2.00	Apr 12, 2004	pp.3,4	Modification of ELECTRICAL CHARACTERISTICS	
3.00	Oct 20, 2004	p.1	Modification of ORDERING INFORMATION	
		pp.7 to 9	Addition of TYPICAL CHARACTERISTICS	
4.00	Sep 10, 2012	p.1	Modification of ORDERING INFORMATION	
		p.6	Modification of ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD	
		p.8	Modification of TYPICAL CHARACTERISTICS	

Cerci

#### NOTICE

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- California Eastern Laboratories has used reasonable care in preparing the information included in this document, but California Eastern Laboratories does not warrant that such information is error free. California Eastern Laboratories and Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- 3. California Eastern Laboratories and Renesas Electronics do not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of California Eastern Laboratories or Renesas Electronics or others.
- 4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc. "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc. Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. California Eastern Laboratories and Renesas Electronics product for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by California Eastern Laboratories or Renesas Electronics.
- 6. You should use the Renesas Electronics products described in this document within the range specified by California Eastern Laboratories, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. California Eastern Laboratories shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
- 8. Please contact a California Eastern Laboratories sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. California Eastern Laboratories and Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- 9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations.
- 10. It is the responsibility of the buyer or distributor of California Eastern Laboratories, who distributes, disposes of, or otherwise places the Renesas Electronics product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
- 11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of California Eastern Laboratories.
- 12. Please contact a California Eastern Laboratories sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

NOTE 1: "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries. NOTE 2: "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics. NOTE 3: Products and product information are subject to change without notice.

CEL Headquarters • 4590 Patrick Henry Drive, Santa Clara, CA 95054 • Phone (408) 919-2500 • www.cel.com

For a complete list of sales offices, representatives and distributors, Please visit our website: www.cel.com/contactus