

### LVPECL 5 x 3.2mm 3.3V Oscillator Freq: 0.016 MHz to 670 MHz

Rev. 05/29/2015

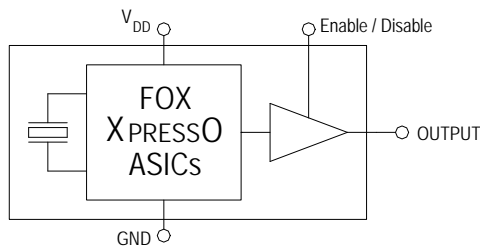
#### Features

- ULTRA Low Jitter
- Low Cost
- XPRESS Delivery
- Frequency Resolution to six decimal places
- Stabilities to  $\pm 20$  PPM
- 20 to +70°C or -40 to +85°C operating temperatures
- Tri-State Enable / Disable Feature
- Industry Standard Package, Footprint & Pin-Out
- Fully RoHS compliant
- Gold over Nickel Termination Finish
- Serial ID with Comprehensive Traceability



#### Applications

- ANY application requiring a high performance LVPECL oscillator
- SONET
- Ethernet
- Storage Area Network
- Broadband Access
- Microprocessors / DSP / FPGA
- Industrial Controllers
- Test and Measurement Equipment



For more information -- Click on the drawing

#### Description

The Fox XpressO-ULTRA Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. XpressO-ULTRA utilizes a family of proprietary ASICs, designed and developed by Fox, with a key focus on noise reduction technologies.

The 4<sup>th</sup> order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has the ability to select the output type and supply voltage.

With the XpressO-ULTRA lead-time, low cost, low noise, wide frequency range, excellent ambient performance, XpressO-ULTRA is an excellent choice over the conventional technologies.

Finished XpressO-ULTRA parts are 100% final tested.

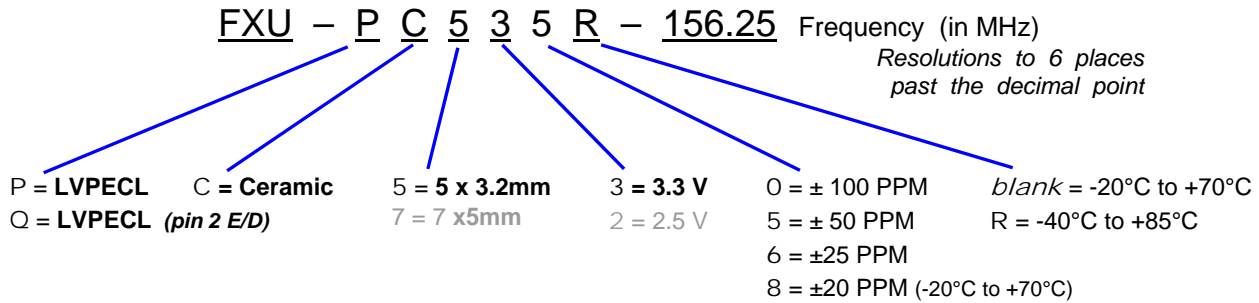
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**Model Selection Guide & Fox Part Number**

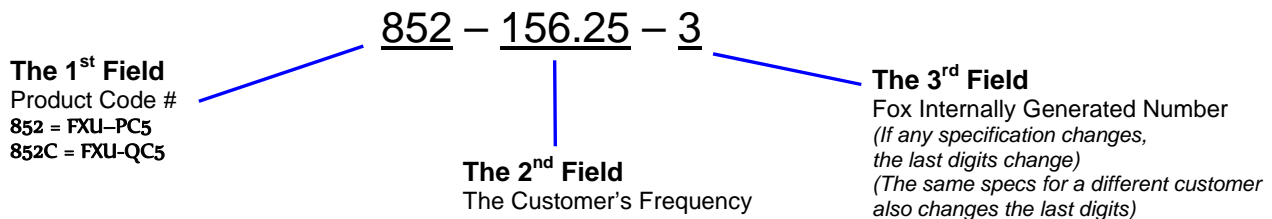
**STEP #1:** Customer selects the Model Description and provides to Fox Customer Service

Model Description



**STEP #2:** The Fox Customer Service team provides a customer specific Part Number for use on their Bill Of Materials (BOM).

Fox Part Number (The assigned Fox Part Number must be on the BOM – not the above Model Description)  
 (This will ensure receipt of the proper part)



This example, **FXU-PC535R-156.25** = LVPECL Output, Ceramic, 5 x 3.2mm Package, 3.3V, ±50 PPM Stability, -40 to +85°C Temperature Range, at 156.25 MHz



<b>Electrical Characteristics</b>			
<b>Parameters</b>	<b>Symbol</b>	<b>Condition</b>	<b>Maximum Value</b> (unless otherwise noted)
Frequency Range	F <sub>O</sub>		0.016 MHz to 670MHz <sup>1</sup>
Frequency Stability <sup>1</sup>		0.016 MHz to 670 MHz	100, 50, 25, 20 <sup>2</sup> PPM
Temperature Range	T <sub>O</sub>	Standard operating 0.016 MHz to 670 MHz	-20°C to +70°C
		Optional operating 0.016 MHz to 670 MHz	-40°C to +85°C
	T <sub>STG</sub>	Storage	-55°C to +125°C
Supply Voltage	V <sub>DD</sub>	Standard	3.3 V ± 5%
Input Current (@ Standard Load)	I <sub>DD</sub>	0.016 MHz to 212.5 MHz 212.5+ MHz to 400 MHz 400+ MHz to 670 MHz	115 mA 128 mA 142 mA
Output Load		Standard	50 Ohms into V <sub>DD</sub> -2V <sub>DC</sub> . TYP.
Start-Up Time	T <sub>S</sub>		10 mS
Output Enable / Disable Time			100 nS
Moisture Sensitivity Level	MSL	JEDEC J-STD-020	1
Termination Finish			Au

Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, 1<sup>st</sup> year aging, shock and vibration.

Note 2 –. ±20 PPM stability -20°C to +70°C only.

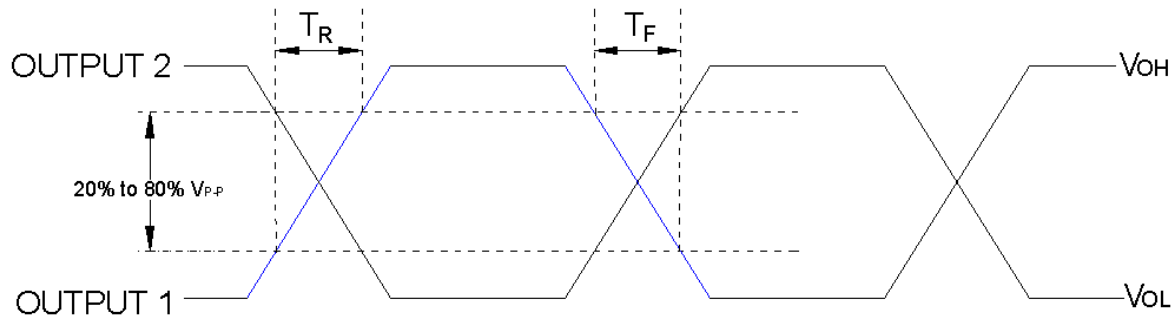
<b>Absolute Maximum Ratings</b> (Useful life may be impaired. For user guidelines only, not tested)			
<b>Parameters</b>	<b>Symbol</b>	<b>Condition</b>	<b>Maximum Value</b> (unless otherwise noted)
Input Voltage	V <sub>DD</sub>		-0.5V to +5.0V
Operating Temperature	T <sub>AMAX</sub>		-55°C to +85°C
Storage Temperature	T <sub>STG</sub>		-55°C to +125°C
Junction Temperature			125°C
ESD Sensitivity	HBM	Human Body Model	1 kV

### Output Wave Characteristics

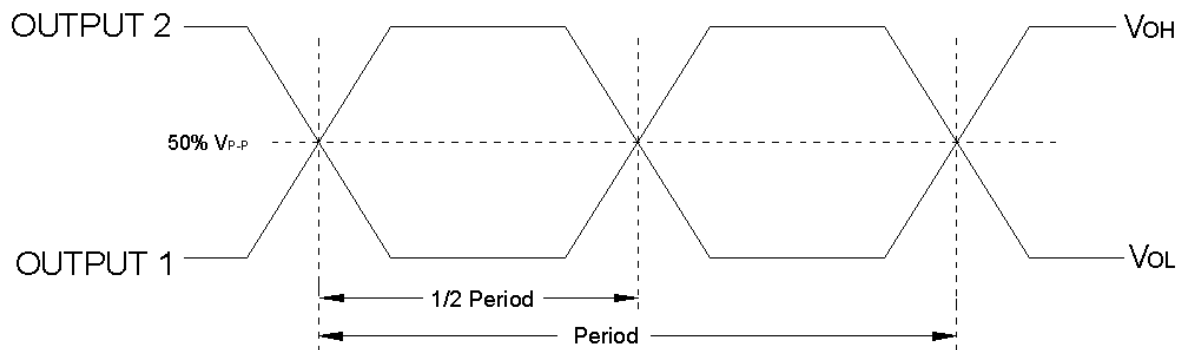
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Low Output Voltage	$V_{OL}$	0.016 MHz to 670 MHz	1.1V to 1.65V
High Output Voltage	$V_{OH}$	0.016 MHz to 670 MHz	1.85V to 2.3V
Output Symmetry @ 50% $V_{P-P}$ Level (See Drawing Below)		0.016 MHz to 312.5 MHz 312.5+ MHz to 670 MHz	45% ~ 55% 40% ~ 60%
Output Enable <sup>1</sup> (PIN # 1) Voltage	$V_{IH}$		$\geq 70\% V_{DD}$
Output Disable <sup>2</sup> (PIN # 1) Voltage	$V_{IL}$		$\leq 30\% V_{DD}$
Cycle Rise Time	$T_R$	(20%~80%) $V_{P-P}$	400 pS
Cycle Fall Time	$T_F$	(80%~20%) $V_{P-P}$	400 pS

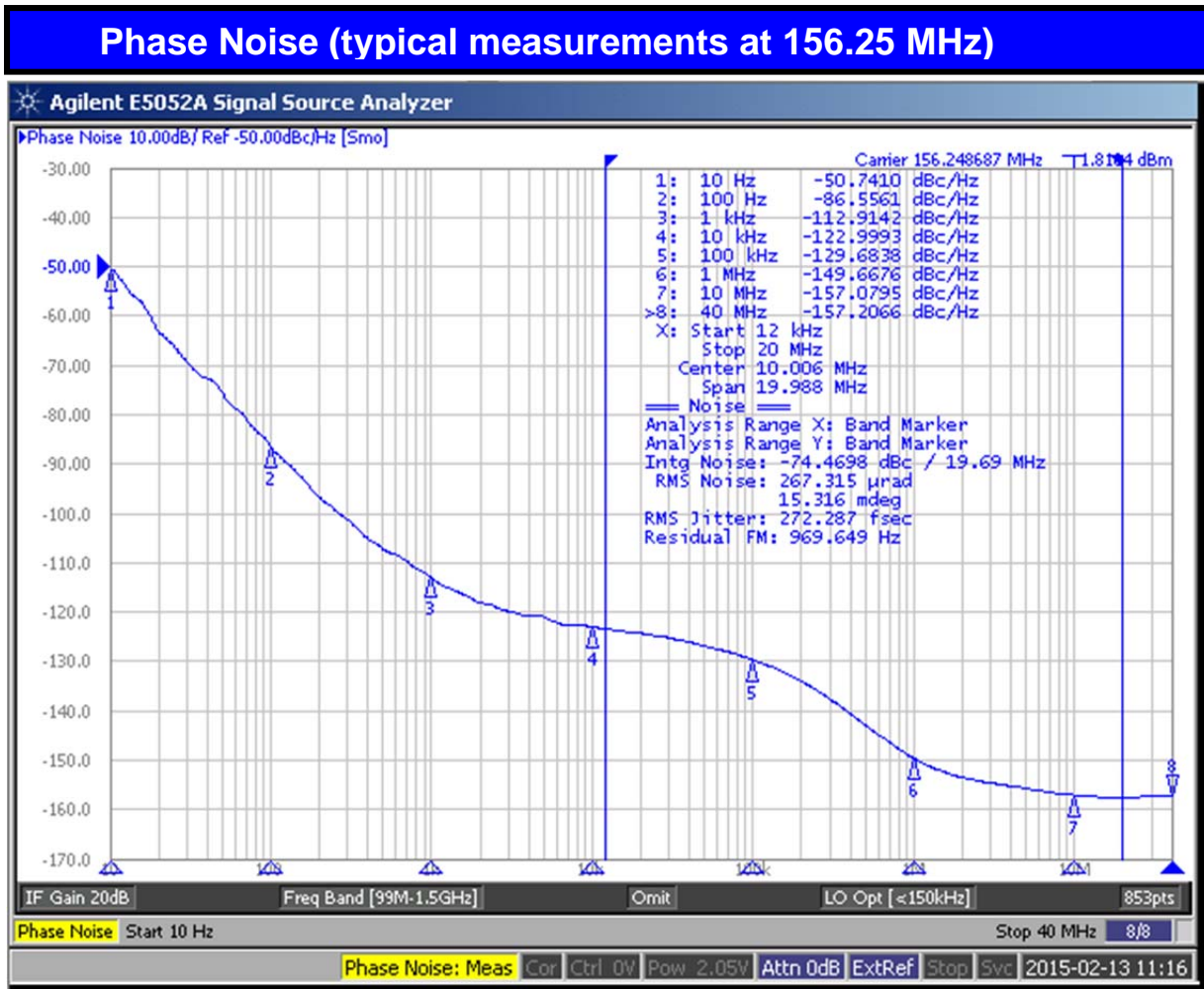
Note1 - An optional PIN # 2 as Enable / Disable is available – see Model Selection Guide (page 2)

#### Rise Time / Fall Time Measurements



#### Oscillator Symmetry





Jitter is frequency dependent. Below are typical values at select frequencies.

### LVPECL Phase Jitter & Time Interval Error (TIE)

Frequency	Phase Jitter (pS) (12kHz to 20MHz)	TIE (pS) (sigma of jitter distribution)
100 MHz	0.330	1.6
125 MHz	0.310	1.4
156.25 MHz	0.272	1.5
212.5 MHz	0.295	1.7

**Phase Jitter** is integrated from Agilent 5052A Signal Noise Analyzer; measured directly into 50 ohm input;  $V_{DD} = 3.3V$ .

**TIE** was measured on LeCroy SDA-6000A Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software;  $V_{DD} = 3.3V$ .  
 Per *MJSQ spec* (Methodologies for Jitter and Signal Quality specifications)

### LVPECL Random & Deterministic Jitter Composition

Frequency	Random (Rj) (pS RMS)	Deterministic (Dj) (pS P-P)	Total Jitter (Tj) (14 x Rj) + Dj
100 MHz	0.8	5.3	16.3
125 MHz	0.7	3.2	12.8
156.25 MHz	0.65	4.25	13.5
212.5 MHz	0.8	4.1	15.5

**Rj and Dj**, measured on LeCroy SDA-6000A Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software.  
 Per *MJSQ spec* (Methodologies for Jitter and Signal Quality specifications)

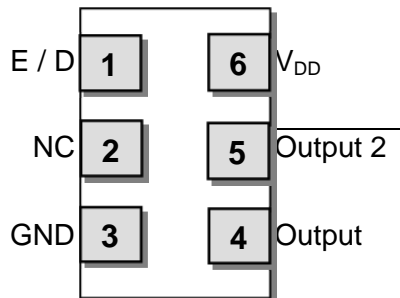


### Pin Description and Recommended Circuit

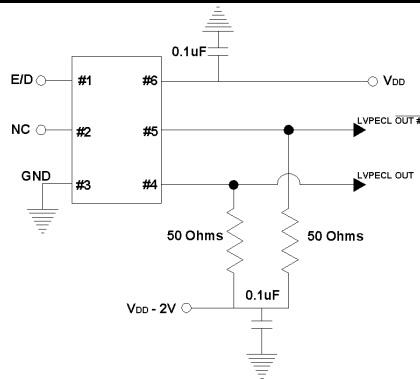
Pin #	Name	Type	Function
1	E / D <sup>1</sup>	Logic	Enable / Disable Control of Output (0 = Disabled)
2	NC <sup>2</sup>		No Connection – Leave OPEN
3	GND	Ground	Electrical Ground for V <sub>DD</sub>
4	Output	Output	LVPECL Oscillator Output
5	Output 2	Output	Differential LVPECL Output
6	V <sub>DD</sub> <sup>3</sup>	Power	Power Supply Source Voltage

#### NOTES:

- <sup>1</sup> Includes pull-up resistor to V<sub>DD</sub> to provide output when the pin (1) is No Connect. (Also see note 2)
- <sup>2</sup> An optional pin # 2 Enable / Disable is available.
- <sup>3</sup> Installation should include a 0.1µF bypass capacitor placed between V<sub>DD</sub> (Pin 6) and GND (Pin 3) to minimize power supply line noise.



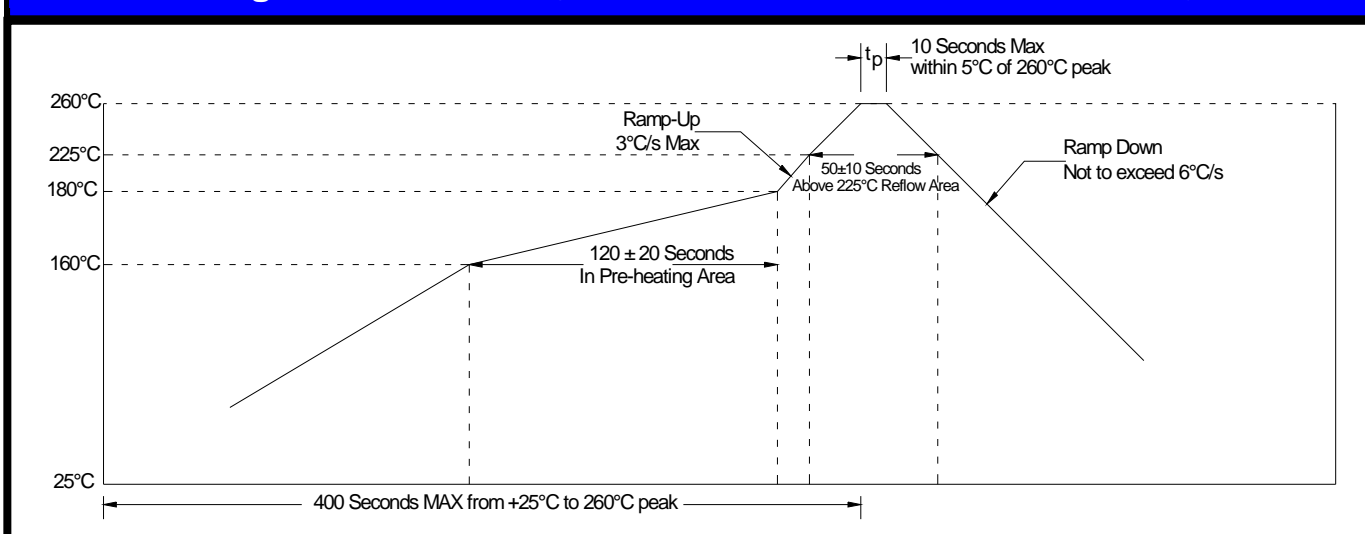
Terminations as viewed from the Top



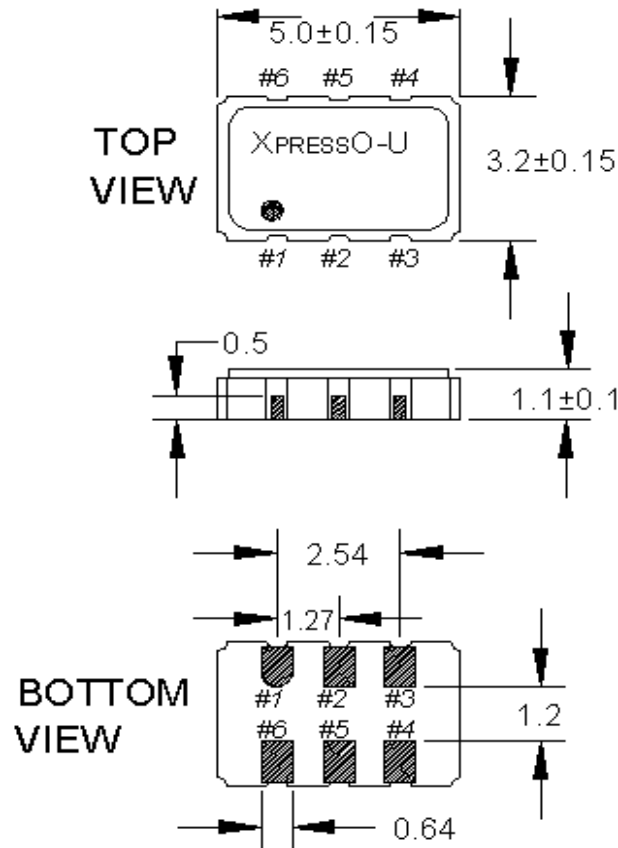
### Enable / Disable Control

Pin # 1 (state)	Output (Pin # 4, Pin # 5)
OPEN (No Connection)	ACTIVE Output
"1" Level $V_{IH} \geq 70\% V_{DD}$	ACTIVE Output
"0" Level $V_{IL} \leq 30\% V_{DD}$	High Impedance

### Soldering Reflow Profile (2 times Maximum at 260°C for 10 seconds MAX)



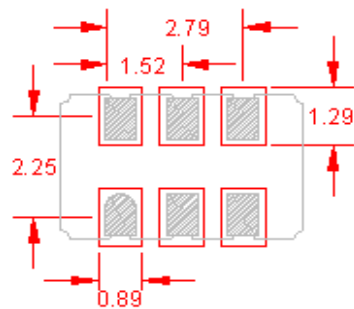
### Mechanical Dimensional Drawing & Pad Layout



**Actual part marking  
is depicted.**

See **Traceability** (pg. 9)  
for more information

### Recommended Solder Pad Layout



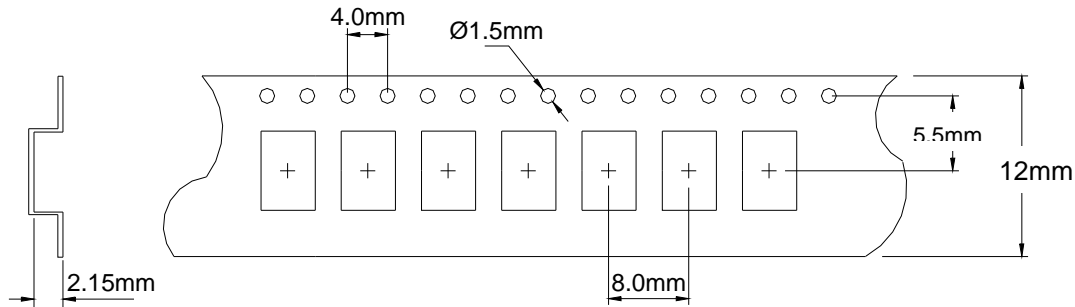
#### Pin Connections

- |          |                     |
|----------|---------------------|
| #1) E/D* | #4) Output          |
| #2) NC*  | #5) Output 2        |
| #3) GND  | #6) V <sub>DD</sub> |

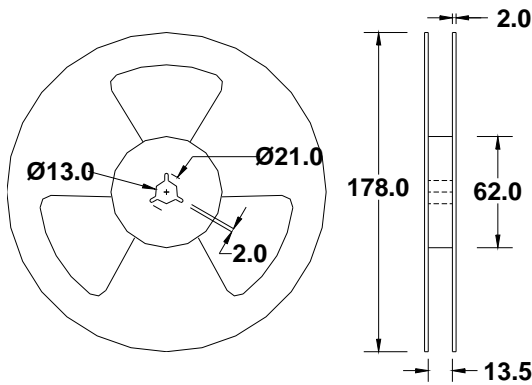
\* An optional PIN # 2 as Enable / Disable is available  
– see Model Selection Guide (page 2)

Drawing is for reference to critical specifications defined by size measurements.  
Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

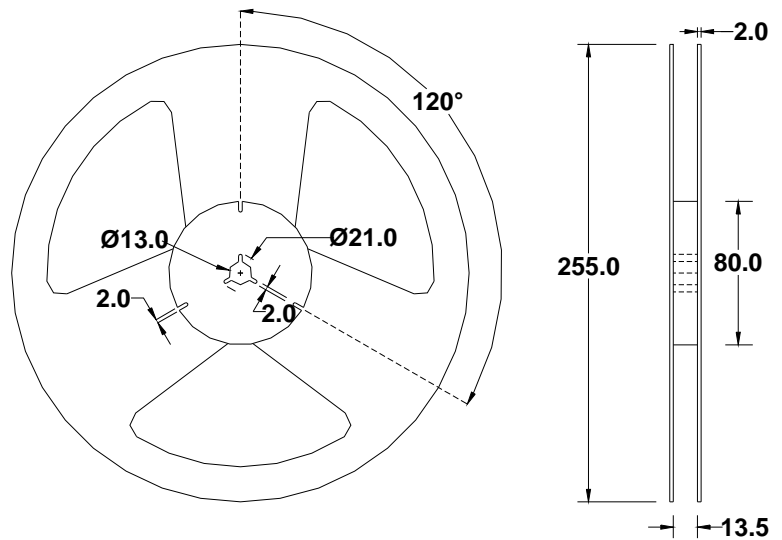
### Tape and Reel Dimensions



### 1k Reel Dimensions in mm



### 2k Reel Dimensions in mm



### Labeling (Reels and smaller packaging are labeled with the below)

- Fox Part Number: 852-156.25-3
- Quantity: 2000 pieces
- Description: FXU-PC535R-156.25
- Date Code: 1335 (YYWW 2013.35<sup>th</sup> wk)
- LOT #: 24435  
*If traceability should become necessary*

**SKU 852-156.25-3**  
 QTY: 2000  
 DESC FXU-PC535R-156.25  
 DATE CODE 1335  
 LOT 24435

**Pb-Free  
RoHS Compliant  
Category (e4)**

**FOX**  
 Xpresso<sup>®</sup>  
 Covered by one or more of listed  
 U.S. Patents: 6,664,860, 5,960,403  
 5,960,405 5,952,890 6,188,290  
 Foreign Patents:  
 China ZL 98802217.6 Mexico 232  
 R.S.A. 98/0896, ROC 120851,  
 Singapore 67081; 67082,  
 EP 0938052 Hong Kong HK1026079  
 Malaysia MY-118540-A  
 Philippines Patent: 1-1998-000246  
 US and Foreign Patents Pending  
 Xpresso<sup>®</sup> is a Registered Trademark of Fox Electronics

An additional identification code is contained internally if tracking should ever be necessary



## Traceability – LOT Number & Serial Identification

### LOT Number

The LOT Number has direct ties to the customer purchase order. The LOT Number is marked on the “Reel” label, and also stored internally on non-volatile memory inside the XPRESSO-ULTRA part. XPRESSO-ULTRA parts that are shipped Tape and Reel, are also placed in an Electro Static Discharge (ESD) bag and will have the LOT Number labeled on the exterior of the ESD bag.

It is recommended that the XPRESSO-ULTRA parts remain in this ESD bag during storage for protection and identification.

If the parts become separated from the label showing the LOT Number, it can be retrieved from inside one of the parts, and the information that can be obtained is listed below:

- Customer Purchase Order Number
- Internal Fox Sales Order Number
- Dates that the XPRESSO-ULTRA part was shipped from the factory
- The assigned customer part number
- The specification that the part was designed for

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### Serial Identification

The Serial ID is the individualized information about the configuration of that particular XPRESSO-ULTRA part. The Serial ID is unique for each and every XPRESSO-ULTRA part, and can be read by special Fox equipment.

With the Serial ID, the below information can be obtained about that individual, XPRESSO-ULTRA part:

- Equipment that the XPRESSO-ULTRA part was configured on
- Raw material used to configure the XPRESSO-ULTRA part
- Traceability of the raw material back to the foundries manufacturing lot
- Date and Time that the part was configured
- Any optimized electrical parameters based on customer specifications
- Electrical testing of the actual completed part
- Human resource that was monitoring the configuration of the part

Fox has equipment placed at key Fox locations World Wide to read the Lot Identification and Serial Number of any XPRESSO-ULTRA part produced and can then obtain the information from above within 24 hours

**Mechanical Testing**

<b>Parameter</b>	<b>Test Method</b>
Mechanical Shock	MIL-STD-202 Method 213 Condition C
Mechanical Vibration	MIL-STD-202 Method 204 5g's for 20 minutes 12 cycles of each 3 orientations: X, Y, Z
High Temperature Operating Life (HTOL)	Under Power @ 125°C for 1000 Hours
Hermetic Seal	He pressure: 4 ±1 kgf / cm <sup>2</sup> 2 Hour soak



[XpressO-ULTRA Home](#)

[XpressO-ULTRA XOs](#)

[XpressO Brochure](#)

Patent Numbers:  
US 6,664,860, US 5,960,403, US 5,952,890; US 5,960,405; US 6,188,290;  
Foreign Patents: R.S.A. 98/0866, R.O.C. 120851; Singapore 67081, 67082; EP 0958652  
China ZL 98802217.6, Malaysia MY-118540-A, Philippines 1-1998-000245, Hong Kong #HK1026079, Mexico #232179  
US and Foreign Patents Pending  
XpressO® Fox Electronics

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The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

