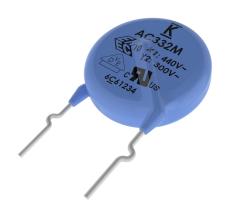


# Safety Standard Recognized, C900, Encapsulated, AC Type, X1 440 VAC/Y2 300 VAC (Industrial Grade)

#### **Overview**

KEMET's 900 encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution when needing to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/ or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application, there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock. With a working voltage of 440 VAC in line-to-line (Class X) and 300 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y2 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 5 KV (Y2) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94V-0.



# **Ordering Information**

C9	7	1	U	472	M	Z	W	D	A	A	7317
Ceramic Series	Body Diameter	Lead Spacing <sup>1,2,4</sup>	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Config. <sup>1,3,4</sup>	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 6 = 13.0 mm 8 = 15.0 mm	7 = 7.5 mm 1 = 10.0 mm	U = Safety	Two significant digits and number of zeroes	J = ±5% K = ±10% M = ±20%	Z = X1 440 VAC /Y2 300 VAC	S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink D = Inside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table" below

<sup>&</sup>lt;sup>1</sup> Due to a high risk of arcing, "Inside Kink" lead configuration cannot be combined with the 5 mm lead spacing option. The "Inside Kink" option is only available on capacitors with lead spacing of 7.5 mm or greater. A potential for arcing may exist when combining the "Inside Kink" lead configuration with a 7.5mm lead spacing option, especially in high humidity environments and/or when exposure to voltages and transients may impact creepage and clearance requirements.

<sup>&</sup>lt;sup>2</sup> Capacitor body diameter will limit available lead spacing and packaging options. See "Dimensions" and "Product Ordering Codes and Ratings" sections of this document to determine availability.

<sup>&</sup>lt;sup>3</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>&</sup>lt;sup>4</sup> Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.



# **Packaging C-Spec Ordering Options Table**

Packaging Type	Lead Length (mm) <sup>2,3</sup>	Packaging Ordering Code (C-Spec)
Ammo Pack	20.0+1.5/-1.0 (straight leads) 18.0+2.0/-0 (preformed leads¹)	7317
	3.0±1.0	WL30
	3.5±1.0	WL35
Pulk Pag	4.0±1.0	WL40
Bulk Bag	4.5±1.0	WL45
	5.0±1.0	WL50
	20.0 minimum⁴	WL20

<sup>&</sup>lt;sup>1</sup> Preformed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

#### **Benefits**

- Safety standard recognized (IEC 60384-14)
- Reliable operation up to 125°C
- Class X1/Y2
- 7.5 mm and 10 mm lead spacing
- · Lead (Pb)-free and RoHS Compliant
- Halogen-free
- · Capacitance offerings ranging from 10 pF up to 10 nF
- Available capacitance tolerances of ±5%, ±10% and ±20%
- · High reliability
- Preformed (crimped) or straight lead configurations
- · Non-polar device, minimizing installation concerns
- Encapsulation meets flammability standard UL 94V-0

# **Applications**

Typical applications include:

- Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- Antenna coupling
- · Primary and secondary coupling (switching power supplies)
- · Line disturbances suppression (motors and motor controls, relays, switching power supplies, and inverters)

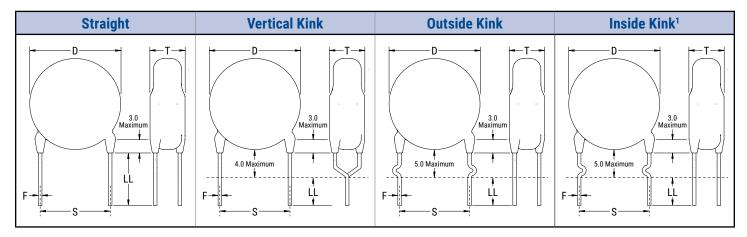
<sup>&</sup>lt;sup>2</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>&</sup>lt;sup>3</sup> For nonstandard lead length inquiries, please contact KEMET.

<sup>&</sup>lt;sup>4</sup> Lead length of 20.0 mm minimum only available for straight leads.



# **Lead Configurations**



<sup>&</sup>lt;sup>1</sup> Due to a high risk of arcing, the "Inside Kink" lead configuration option cannot be combined with 5 mm lead spacing ("S" dimension above). The "Inside Kink" option is only available on devices with lead spacing of 7.5 mm or 10 mm.

### **Dimensions - Millimeters**

Lead	Lead	S	Lead		Т	е	ØF
Configuration	Configuration Ordering Code <sup>1</sup>	Lead Spacing <sup>2</sup>	Spacing Tolerance	Body Diameter <sup>2</sup>	Body Thickness	Lead Meniscus	Lead Diameter
Ctualinht	Δ.	7.5	±1.0				
Straight	A	10.0	±1.0				
Vertical Kink	В	7.5	±1.0				
(Preformed)	В	10.0	±1.0	See Table 1 - "Product Ordering Codes		3.0	0.55.01
Outside Kink	С	7.5	±1.0		atings"	maximum	0.55±0.1
(Preformed)	C	10.0	±1.0				
Inside Kink	0	7.5	±1.0				
(Preformed)	D	10.0	±1.0				

<sup>&</sup>lt;sup>1</sup>Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

<sup>&</sup>lt;sup>2</sup> Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.



# **Approval Standard and Certification No.**

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.	
VDE	IEC 60384-14	X1	440 VAC	40036415	
(ENEC)	150 00304-14	Y2	300 VAC	40030415	
UL	UL 60384-14 and	X1	440 VAC	T256200	
CAN/CSA	E60384-14	Y2	300 VAC	E356389	

These devices are VDE/ENEC and UL recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384–14 and UL 60384–14.

# **Environmental Compliance**

These devices are Halogen free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.



# **General Specifications/Performance Characteristics**

Dielectric/Temperature Characteristic	SL	Y5P	Y5U	Y5V		
Operating Temperature Range:		-40°C to +125°C				
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	-1,000 ~ +350 ppm/ °C	1 +111% 1 +111%/-55%				
Dielectric Withstanding Voltage (7.5 mm and 10 mm Lead Spacing)	2,600 VAC (60±5 seconds at 25°C)					
Quality Factor (Q)	30 pF and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20xC)*	See "Dissipation Factor"				
Dissipation Factor (tanδ) at +25°C1	See "Quality Factor"	2.50%	2.50%	5.0%		
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60±5 seconds at 25°C)					

<sup>\*</sup>C = Nominal capacitance

SL: 1 MHz ± 100 kHz and 1.0 ±0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ± 50 Hz and 1.0 ±0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

<sup>&</sup>lt;sup>1</sup> Capacitance and Dissipation Factor (DF) measured under the following conditions:



## Table 1 - Product Ordering Codes and Ratings

<b></b>				Dir	mensions (mm	)	Lead S	pacing	
Dielectric/ Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging	
	C90(1)U100JZSD(2)A(3)	10 pF							
	C90(1)U120JZSD(2)A(3)	12 pF							
	C90(1)U150JZSD(2)A(3) C90(1)U180JZSD(2)A(3)	15 pF 18 pF							
	C90(1)U200JZSD(2)A(3)	20 pF							
	C90(1)U220JZSD(2)A(3)	22 pF							
	C90(1)U240JZSD(2)A(3)	24 pF							
	C90(1)U270JZSD(2)A(3)	27 pF		7.0					
	C90(1)U300JZSD(2)A(3)	30 pF							
	C90(1)U330JZSD(2)A(3)	33 pF					7.5		
SL	C90(1)U360JZSD(2)A(3)	36 pF	±5%		5.0	0.55 ±0.1		r	
	C90(1)U390JZSD(2)A(3)	39 pF					10 ו	mm	
	C90(1)U470JZSD(2)A(3)	47 pF							
	C90(1)U500JZSD(2)A(3)	50 pF							
	C90(1)U510JZSD(2)A(3)	51 pF			-				
	C91(1)U560JZSD(2)A(3) C91(1)U620JZSD(2)A(3)	56 pF 62 pF							
	C91(1)U680JZSD(2)A(3)	68 pF		8.0					
	C91(1)U750JZSD(2)A(3)	75 pF							
	C92(1)U820JZSD(2)A(3)	82 pF		9.0					
	C93(1)U101JZSD(2)A(3)	100 pF		10.0					
	C90(1)U101KZYD(2)A(3)	100 pF							
	C90(1)U151KZYD(2)A(3)	150 pF		7.0					
	C90(1)U221KZYD(2)A(3)	220 pF		7.0			7.5	i mm or	
Y5P	C90(1)U331KZYD(2)A(3) C90(1)U471KZYD(2)A(3)	330 pF 470 pF	±10%		5.0	0.55 ±0.1			
135	C91(1)U561KZYD(2)A(3)	560 pF	110%		3.0	0.55 10.1	10 :		
	C91(1)U681KZYD(2)A(3)	680 pF		8.0			10 111111		
	C92(1)U821KZYD(2)A(3)	820 pF							
	C92(1)U102KZYD(2)A(3)	1,000 pF		9.0		İ			
	C90(1)U102MZWD(2)A(3)	1,000 pF		7.0					
	C92(1)U152MZWD(2)A(3)	1,500 pF		9.0			7.5	mm	
Y5U	C92(1)U222MZWD(2)A(3) C94(1)U332MZWD(2)A(3)	2,200 pF 3,300 pF	±20%	11.0	5.0	0.55 ±0.1	O	r	
	C96(1)U392MZWD(2)A(3)	3,300 pF 3,900 pF					10 :	mm	
	C96(1)U472MZWD(2)A(3)	4,700 pF		13.0					
	1070(1)0 17 EIIIE 110(E)A(0)	1,7 σσ μι							
	C90(1)U102MZVD(2)A(3)	1,000 pF							
	C90(1)U152MZVD(2)A(3)	1,500 pF		7.0					
	C90(1)U222MZVD(2)A(3)	2,200 pF					7.5	mm	
Y5V	C92(1)U332MZVD(2)A(3)	3,300 pF	±20%	9.0	5.0	0.55 ±0.1		), 	
	C94(1)U392MZVD(2)A(3)	3,900 pF		11.0		0.55 10.1	10 :		
	C94(1)U472MZVD(2)A(3)	4,700 pF 6,800 pF		13.0					
	C96(1)U682MZVD(2)A(3) C98(1)U103MZVD(2)A(3)	6,800 pF		13.0					
	030(1)0103WIZVD(ZJA(3)	ιυ,υυυ με		13.0					
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing	

<sup>(1)</sup> To properly complete ordering code, insert the one-digit numeric code to reflect required lead spacing: (Note that select capacitance values and packaging options may limit lead spacing availability. See table above to verify availability.)
7 = 7.5 mm

 $<sup>1 = 10.0 \, \</sup>text{mm}$ 

<sup>(2)</sup> To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

A = Straight

B = Vertical Kink

C = Outside Kink

D = Inside Kink

<sup>(3)</sup> To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.



# **Table 2 – Performance & Reliability: Test Methods and Conditions**

It	em	Specif	ication	Test Method				
Operating Tem	perature Range			-40°C to +125°C				
	Between lead wires	No fa	ilures	The capacitor shall not be damaged when 2,600 VAC(rms) is applied between the lead wires for 60 seconds.				
Dielectric Strength	Body Insulation	No failures		The terminals (leads) of the capacitor shall be connected together. A metal foil is tightly wrapped around the body of the capacitor at a distance of about 3 to 4 mm from each terminal. The capacitor is then inserted into a container filled with metal balls approximately 1 mm in diameter. 2,600 VAC(rms) is applied for 60 seconds between the capacitor lead wires and metal balls.				
Insulation R	esistance (IR)	10,000 MΩ	) minimum		esistance shall be m ±5 seconds of charg	easured with 500±50 VDC ling.		
Capac	citance	Within specif	fied tolerance					
		Temperature Characteristics	Specification					
		Y5P, Y5U	DF ≤ 2.5%	Y5P, Y5U and Y5V: Capacitance is measured at 1 kHz ±20% and 5 Vrms or less. (20±2°C) SL: Capacitance is measured at 1 MHz ±20% and 1.0±0.2 Vrms				
<u> </u>	. (55)	Y5V	DF ≤ 5.0%					
Dissipation F	actor (DF) or Q	SL	≥ 30 pF: Q ≥ 1000 < 30 pF: Q ≥ 400 +(20 x C) C = Nominal capacitance	(25°C)				
				A capacitance measurement is made at each step specified:				
		Temperature	Capacitance Change	Step	Temperature	e at each step specified.		
		Characteristics		1	+20±2°C			
		Y5P	Within ±10%	2	-25±2°C			
Temperature	Characteristics	Y5U	Within +20%/-55%	3	+20±2°C			
remperature	ond dotenous	Y5V	Within	4	+85±2°C			
			~+30%/-80%	5	+20±2°C			
		SL	-1,000 ~+350 ppm°C (+20°C ~+85°C)	Pre-treatment: Capacitor is stored at 85±2°C for 1 hour and then placed at room condition¹ for 24±2 hours before measurement.				
	Tensile	•	citor body shall not eak.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.  With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.				
Terminal Strength	Bending		citor body shall not eak.					

 $<sup>^1</sup>$  "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



# Table 2 - Performance & Reliability: Test Methods and Conditions cont'd

lte	m	Speci	fication	Test M	lethod		
Soldera	ability	of solder in the ax	ave a uniform coating ial direction and over ircumference.	The lead wire of the capacitor is dipped into molten solder for 5±0.5 seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder Temperature: Lead free solder (Sn-3Ag - 0.5Cu) 245°C ±5°C.			
	Appearance	No visi	ual defect	As shown in the figure below, the lead wires are immersed in molten solder up to 1.5 mm (+5/-0 mm) from the end of the			
	IR	1,0	00 ΜΩ	epoxy meniscus (root of lead wire).			
	Dielectric Strength	Per	item 1	Duration/Solder Temperature: 3.5±0.5 seconds/350°C ±10°C or 10±1 seconds/260°C ±5°C  Thermal Capacitor Screen			
Soldering Effect (Non-Preheat)	Capacitance	SL: Within ±2.5% o	Y5V: Within ±10% r ±0.25 pF, whichever arger.				
	Appearance	No visi	ual defect	Capacitor is stored at 120°C +0/- Then, as shown in the figure belo	-5°C for 60 +0/-5 seconds.		
	IR	1,0	00 ΜΩ	in molten solder up to 1.5 mm (+)	5/-0mm) from the end of the		
	Dielectric Strength	Per	item 1	epoxy meniscus (root of lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C			
Soldering Effect (Preheat)	Capacitance	Y5P, Y5U and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger.		Pre-treatment: Capacitor is store then placed at room condition for measurements.  Post-treatment: Capacitor is store then placed at room condition for measurements.  Post-treatment: Capacitor is store condition.	or 24 ±2 hours before initial		
	Appearance	No visi	ual defect	Steady State Humidity:	Load Humidity:		
Biased Humidity	Capacitance	Temperature Characteristics Y5P Y5U Y5V SL	Capacitance Change  Within ±10%  Within ±20%  Within ±30%  Within ±2.5% or ±0.25 pF, whichever is larger.	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours. Post Treatment:	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.		
	DF		: 5.0% maximum % maximum	Capacitor is stored for 1 to 2 hours at room condition <sup>1</sup> .	Post Treatment: Capacitor is stored for 1 to 2		
	Q	SL: Less Q ≥ 100 More than C C = Nomina	than 30 pF: + 10 × C/3 30 pF: Q ≥ 200 al capacitance		hours at room condition <sup>1</sup> .		
	IR		: 3,000 MΩ minimum MΩ minimum				
	Dielectric Strength		ailures				

 $<sup>^1</sup>$  "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



# Table 2 - Performance & Reliability: Test Methods and Conditions cont'd

Item		Specification	Test Method
	Appearance	No visual defect	Impulse Voltage: Each individual capacitor is subjected to three 5 kv impulses prior to life testing.
	Capacitance Change	Y5P, Y5V and Y5U: Within ±20% SL: Within ±3 or ±0.3 pF, whichever is larger.	Vol
	IR	3,000 M $\Omega$ minimum SL: 1,000 M $\Omega$ minimum	0.9Vp (uS) (uS) (uS) (uS) (uS) (uS) (uS) (uS)
High Temperature Life	Dielectric Strength	No failures	Capacitors are placed in a circulating air oven for a period of 1,000 hours. The air in the oven is maintained at a temperature of 125°C ±2 throughout the test. The capacitors are subjected to AC 510 Vrms. Each hour the voltage is increased to 1,000 Vrms for 0.1 seconds.
		The capacitor flame extinguishes as follows:	The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles.
		Cycle Time	Flame
Flame	e Test	1 ~ 4 30 seconds maximum	38/ 127
		5 60 seconds maximum	Gas Burner (Unit:mm)
			The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge.  Signature:  Oscilloscope
			C <sub>1,2</sub> 1 μF ±10% C <sub>3</sub> 0.033 μF ±5% 10 kV
Active Fla	mmability	The cheesecloth should not ignite.	L <sub>1.4</sub> 1.5 Mh ±20% 16A Rod core choke Cx Test capacitor
			R 100±2% V <sub>AC</sub> VR±5%
			Ct 3 µF ±5% 10 kV V <sub>R</sub> Rated Voltage
			F Fuse, Rated 10A Vt Voltage applied to Ct

<sup>&</sup>quot;Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



# Table 2 - Performance & Reliability: Test Methods and Conditions cont'd

Item		Specif	cation	Test Method				
Passive Flammability		The burning time should not exceed 30 seconds. The tissue paper should not ignite.		The capacitor under test is held into a flame and in a position which best promotes burning. Each specimen is exposed to the flame one time.  Test Specimen  About 10mm Thick Board  Time of exposure to flame:  Length of flame:  12±1 mm  Gas burner length:  Inside diameter:  Outside diameter:  0.9 mm maximum  Gas butane gas purity:  95% minimum			xposed to the	
	Appearance	No visua	I defect					
		Temperature Characteristics	Capacitance Change Within ±5%	The capacitor is subjected to 5 temperature cycles.  Temperature Cycle				
	Capacitance	SL Y5P Y5U, Y5V	Within ±10% Within ±20%	Step	Temperature (°C)	Dwell Time (minutes)	Transition Time (minutes)	
Temperature		SL	≥ 30 pF: Q ≥ 350	1	-40+0/-3	30		
Cycle			< 30 pF: Q ≥ 275	2	Room temperature	3		
			+5/2C C = Nominal	3	125+3/-0	30	3	
	DF/Q		capacitance	4	Room temperature	3		
		Y5P Y5U, Y5V	DF ≤ 5% DF ≤ 7.5%	Pre-treatment: Capacitor shall be stored at 85±2 for 1 hour the placed at room condition for 24±2 hours.				
	IR	3,000 MΩ	minimum	condition <sup>1</sup> .	ment: Capacitor is stored	a for 1 to 2 hou	rs at room	
	Dielectric Strength	3,000 MΩ minimum  No failures						

 $<sup>^{1}</sup>$  "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



# **Soldering and Mounting Information**

#### **Soldering:**

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

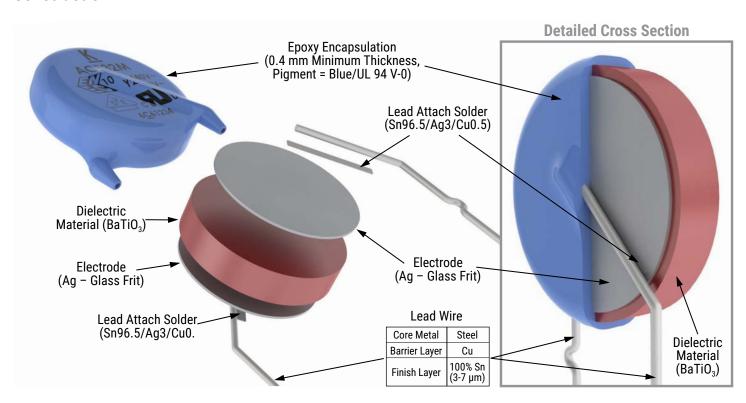
- Temperature of iron-tip: 400°C maximum
- · Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

#### Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: Output of 20 watts per liter or less
- Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

#### Construction

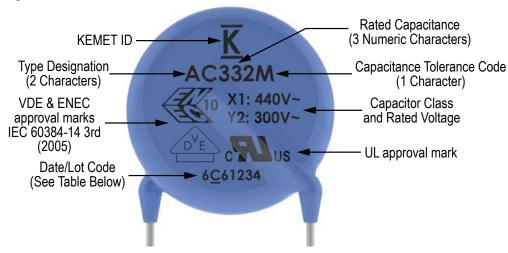




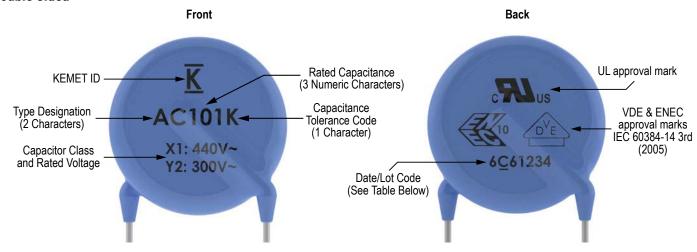
## **Marking**

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two sided marking is limited to capacitors with body diameters ≤ 8.0 mm.)

#### **Single Sided**



#### **Double Sided**



Date/Lot Code Explanation

6	<u>C</u>	6	1234
Last digit of year, e.g., 6 = 2016	Manufacturing Location Code	Manufacturing Month:  1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot no.



# **Packaging Quantities**

Capacitor			Tampangan nitan an carria		ape)	
Body Diameter	Body Diameter Code <sup>1</sup>	Bulk Bag (Loose)			er tape <sup>2</sup>	
(mm)	Oouc	(LUUSC)			25.4 mm	
7.0	0					
8.0	1					
9.0	2				1 000 pieces /hey	
10.0	3				1,000 pieces/box	
11.0	4	500 pieces/bag	1,000 pie	eces/box		
12.0	5					
13.0	6					
14.0	7				500 pieces/box	
15.0	8					

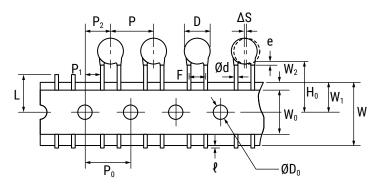
<sup>&</sup>lt;sup>1</sup> The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

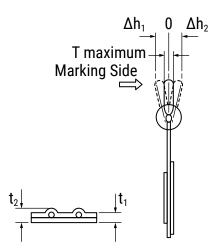
<sup>&</sup>lt;sup>2</sup> For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.



# **Figure 1 - Ammo Pack Taping Format**

#### 5 mm and 7.5 mm Lead Spacing:





#### 10 mm Lead Spacing:

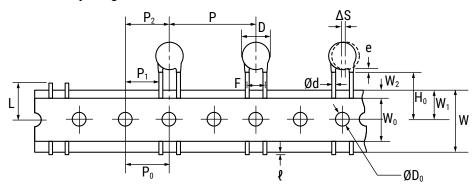


Table 3 - Ammo Pack Taping Specifications

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style	Lead Style Straight Preformed <sup>1</sup> Straight Preformed <sup>1</sup> Stra				Straight	Preformed <sup>1</sup>	
Item	Symbol	Dimensions (mm)					
Lead Spacing	F	5.0+0.8/-0.2		7.5±1.0		10.0±1.0	
Component Pitch	Р	12.7		15.0		25.4±2	
Sprocket Hole Pitch	P <sub>0</sub>	12.7±0.3		15.0±0.3		12.7±0.3	
Sprocket Hole Center to Component Center	P <sub>2</sub>	6.35±1.5		7.5±1.5		12.7±1.5	
Sprocket Hole Center to Lead Center	P <sub>1</sub>	3.75±1.0		3.75±1.0		7.7±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.					
Component Alignment (side/side)	ΔS	0±2.0					
Carrier Tape Width	W	18.0+1.0/-0.5					

<sup>&</sup>lt;sup>1</sup> Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>&</sup>lt;sup>2</sup> Also referred to as "lead length" in this document.



# Table 3 - Ammo Pack Taping Specifications cont'd

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Preformed <sup>1</sup>	Straight	Preformed <sup>1</sup>	Straight	Preformed <sup>1</sup>
Item	Symbol	Dimensions (mm)					
Sprocket Hole Position	$W_{1}$	9.0±0.5					
Height to Seating Plane <sup>2</sup> (preformed leads <sup>1</sup> )	$H_0$	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0
Height to Seating Plane <sup>2</sup> (straight leads)	Н	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A
Lead Protrusion	ę	2.0 maximum					
Diameter of Sprocket Hole	$D_{0}$	4.0±0.2					
Lead Diameter	φd	0.55±0.1					
Carrier Tape Thickness	t <sub>1</sub>	0.6±0.3					
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t <sub>2</sub>	1.5 maximum					
Component Alignment (front/	$\Delta h_{_1}$	2.0 maximum					
back)	$\Delta h_2$			2.0 maximum			
Cut Out Length	L	11.0 maximum					
Hold-Down Tape Width	$W_{0}$	11.0 minimum		11.5 minimum			
Hold-Down Tape Position	$W_{2}$	3.0 maximum 1.5±1.5			±1.5		

<sup>&</sup>lt;sup>1</sup> Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

# **Application Notes:**

#### **Storage and Operating Conditions:**

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

#### **Working Voltage:**

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.

<sup>&</sup>lt;sup>2</sup> Also referred to as "lead length" in this document.



Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)	
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p	

#### **Operating Temperature and Self-Generating Heat:**

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

#### **Handling - Vibration and Impact:**

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



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Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicated or that other measures may not be required.