# Low-power 2-input NOR gate Rev. 7 — 21 January 2015

**Product data sheet** 

#### **General description** 1.

The 74AUP1G02 provides the single 2-input NOR function.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V<sub>CC</sub> range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V<sub>CC</sub> range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I<sub>OFF</sub>.

The I<sub>OFF</sub> circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

#### 2. **Features and benefits**

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - ◆ JESD8-12 (0.8 V to 1.3 V)
  - ◆ JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - ◆ JESD8-5 (1.8 V to 2.7 V)
  - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



# 3. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range Name		Description	Version			
74AUP1G02GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
74AUP1G02GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886			
74AUP1G02GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1 $\times$ 0.5 mm	SOT891			
74AUP1G02GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115			
74AUP1G02GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202			
74AUP1G02GX	−40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226			

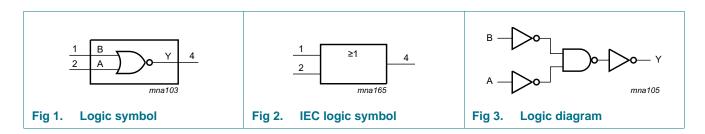
# 4. Marking

Table 2. Marking

Type number	Marking code <sup>[1]</sup>
74AUP1G02GW	рВ
74AUP1G02GM	рВ
74AUP1G02GF	рВ
74AUP1G02GN	рВ
74AUP1G02GS	рВ
74AUP1G02GX	рВ

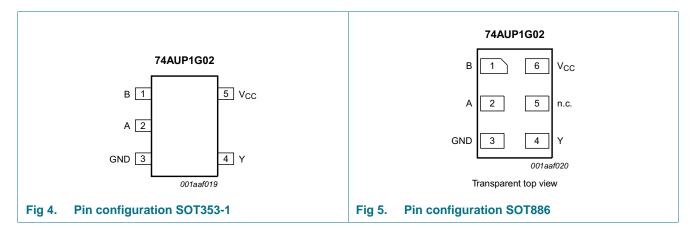
<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

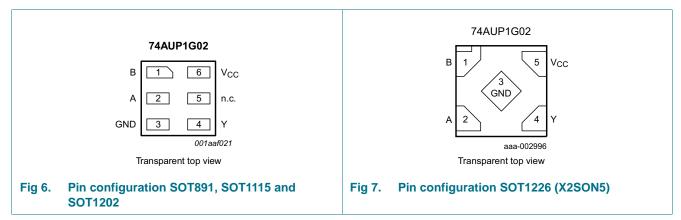
# 5. Functional diagram



# 6. Pinning information

### 6.1 Pinning





# 6.2 Pin description

Table 3. Pin description

Symbol	Pin		Description		
	TSSOP5 and X2SON5	XSON6			
В	1	1	data input		
Α	2	2	data input		
GND	3	3	ground (0 V)		
Υ	4	4	data output		
n.c.	-	5	not connected		
V <sub>CC</sub>	5	6	supply voltage		

# 7. Functional description

Table 4. Function table[1]

Input		Output
A	В	Υ
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

<sup>[1]</sup> H = HIGH voltage level;

# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> > V <sub>CC</sub> or V <sub>O</sub> < 0 V	-	±50	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	$V_O = 0 \text{ V to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	+50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$	-	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V

L = LOW voltage level.

<sup>[2]</sup> For TSSOP5 packages: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.
For XSON6 and X2SON5 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K.

# 10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	5 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.30 \times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		$I_O = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_O = -1.7 \text{ mA}$ ; $V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_O = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_O = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_O = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_O = 20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
I <sub>I</sub>	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.5	μΑ
Δl <sub>CC</sub>	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ [1] $V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
C <sub>I</sub>	input capacitance	$V_{CC} = 0 \text{ V to } 3.6 \text{ V; } V_{I} = \text{GND or } V_{CC}$	-	8.0	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF

 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.30 \times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	$0.7 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.30	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_O = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0 = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.5	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	V <sub>1</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	μА
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μΑ
$\Delta I_{CC}$	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ

 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.75 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.70 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.25 \times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.30 \times V_{CC}$	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O} = -20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.11	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.41	V
		$I_{O} = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.39	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
l <sub>l</sub>	input leakage current	$V_1 = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μΑ
l <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0 = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.75	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μΑ
Δl <sub>CC</sub>	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ [1] $V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

<sup>[1]</sup> One input at  $V_{CC}$  – 0.6 V, other input at  $V_{CC}$  or GND.

# 11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	Min	Typ [1]	Max	Unit
T <sub>amb</sub> = 25	°C; C <sub>L</sub> = 5 pF					
pd	propagation delay	A, B to Y; see Figure 8				
		V <sub>CC</sub> = 0.8 V	-	17.0	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.5	5.1	10.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.6	3.7	6.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.3	3.0	5.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	2.4	3.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	2.2	3.4	ns
Γ <sub>amb</sub> = 25	°C; C <sub>L</sub> = 10 pF					
pd	propagation delay	A, B to Y; see Figure 8				
		V <sub>CC</sub> = 0.8 V	-	20.4	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.4	6.0	12.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.9	4.3	7.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.6	3.6	6.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	3.0	4.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	2.7	4.2	ns
T <sub>amb</sub> = 25	°C; C <sub>L</sub> = 15 pF					
pd	propagation delay	A, B to Y; see Figure 8				
		V <sub>CC</sub> = 0.8 V	-	23.9	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.4	6.8	14.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.3	4.8	8.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	4.0	7.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	3.4	5.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.6	3.2	4.8	ns
Γ <sub>amb</sub> = 25	°C; C <sub>L</sub> = 30 pF					
pd	propagation delay	A, B to Y; see Figure 8				
		V <sub>CC</sub> = 0.8 V	-	34.2	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.6	9.0	19.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.4	6.4	11.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	5.3	9.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.4	4.5	7.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.3	4.2	6.4	ns

 Table 8.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	Min	Typ [1]	Max	Unit
T <sub>amb</sub> = 25	°C					
C <sub>PD</sub>	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$				
		V <sub>CC</sub> = 0.8 V	-	2.6	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.7	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.9	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.1	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.5	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.1	-	pF

- [1] All typical values are measured at nominal  $V_{CC}$ .
- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

 $P_D = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}{}^2 \times f_o) \text{ where:}$ 

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	-40 °C to +85 °C		-40 °C to	+125 °C	Unit
			Min	Max	Min	Max	
C <sub>L</sub> = 5 pF	'						
t <sub>pd</sub>	propagation delay	A, B to Y; see Figure 8					
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.1	12.1	2.1	13.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.4	7.8	1.4	8.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.1	6.2	1.1	6.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	0.9	4.6	0.9	5.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.8	4.0	0.8	4.4	ns
C <sub>L</sub> = 10 pF	:				-		
t <sub>pd</sub>	propagation delay	A, B to Y; see Figure 8					
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.2	14.3	2.2	15.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.7	9.2	1.7	10.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.5	7.3	1.5	8.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.2	5.6	1.2	6.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.2	5.0	1.2	5.5	ns

 Table 9.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	-40 °C t	to +85 °C	-40 °C to	+125 °C	Unit
			Min	Max	Min	Max	
C <sub>L</sub> = 15 pl	=						
t <sub>pd</sub>	propagation delay	A, B to Y; see Figure 8					
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.1	16.4	3.1	18.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.0	10.4	2.0	11.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	8.3	1.7	9.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.5	6.3	1.5	7.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	5.7	1.4	6.3	ns
C <sub>L</sub> = 30 pl	=						
t <sub>pd</sub> propagation delay		A, B to Y; see Figure 8					
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.1	22.4	4.1	24.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.9	13.9	2.9	15.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	11.1	2.3	12.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.1	8.5	2.1	9.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	7.7	2.1	8.5	ns

<sup>[1]</sup>  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

#### 12. Waveforms

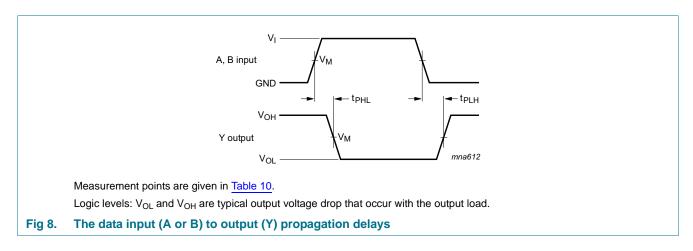
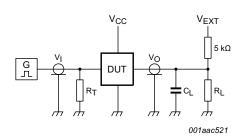


Table 10. Measurement points

Supply voltage	Output	Input		
V <sub>CC</sub>	$V_{M}$	V <sub>M</sub>	V <sub>I</sub>	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V <sub>CC</sub>	≤ 3.0 ns



Test data is given in Table 11.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

 $V_{\mathsf{EXT}}$  = External voltage for measuring switching times.

Fig 9. Test circuit for measuring switching times

#### Table 11. Test data

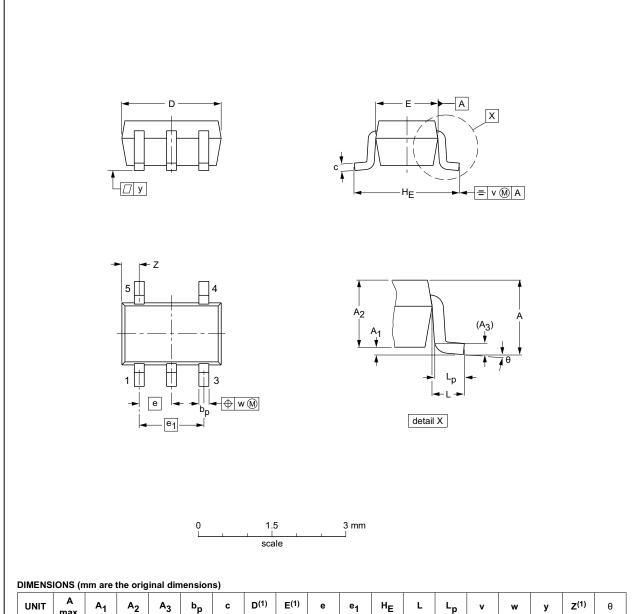
Supply voltage	Load		V <sub>EXT</sub>					
V <sub>CC</sub>	C <sub>L</sub>	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>			
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	$5$ k $\Omega$ or $1$ M $\Omega$	open	GND	$2 \times V_{CC}$			

[1] For measuring enable and disable times  $R_L$  = 5  $k\Omega$ , for measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1  $M\Omega$ .

# 13. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	bp	U	D <sup>(1)</sup>	E(1)	е	e <sub>1</sub>	HE	L	Lp	>	w	у	Z <sup>(1)</sup>	θ
mm	1.1	0.1 0	1.0 0.8	0.15	0.30 0.15	0.25 0.08	2.25 1.85	1.35 1.15	0.65	1.3	2.25 2.0	0.425	0.46 0.21	0.3	0.1	0.1	0.60 0.15	7° 0°

#### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	EC JEDEC JEITA		PROJECTION	ISSUE DATE
SOT353-1		MO-203	SC-88A		<del>-00-09-01</del> 03-02-19

Fig 10. Package outline SOT353-1 (TSSOP5)

74AUP1G0

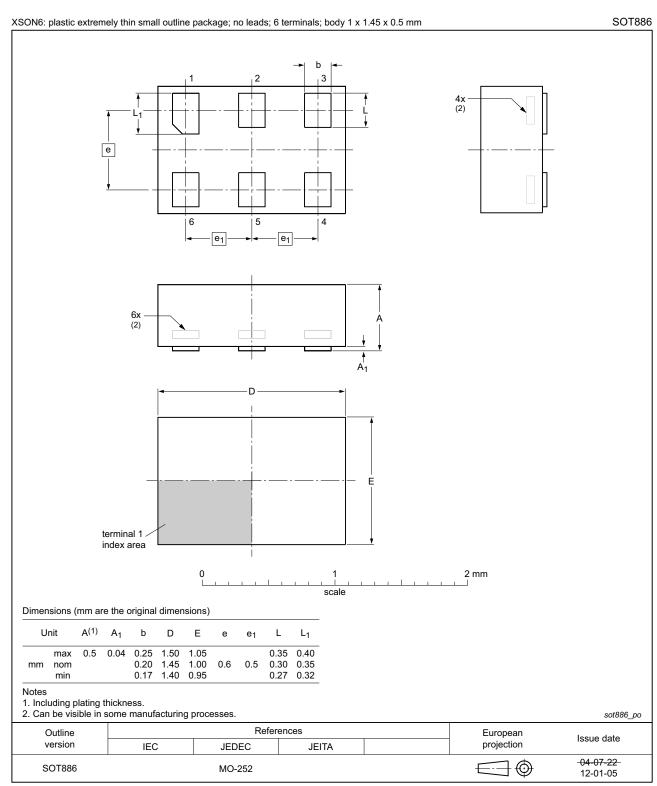


Fig 11. Package outline SOT886 (XSON6)

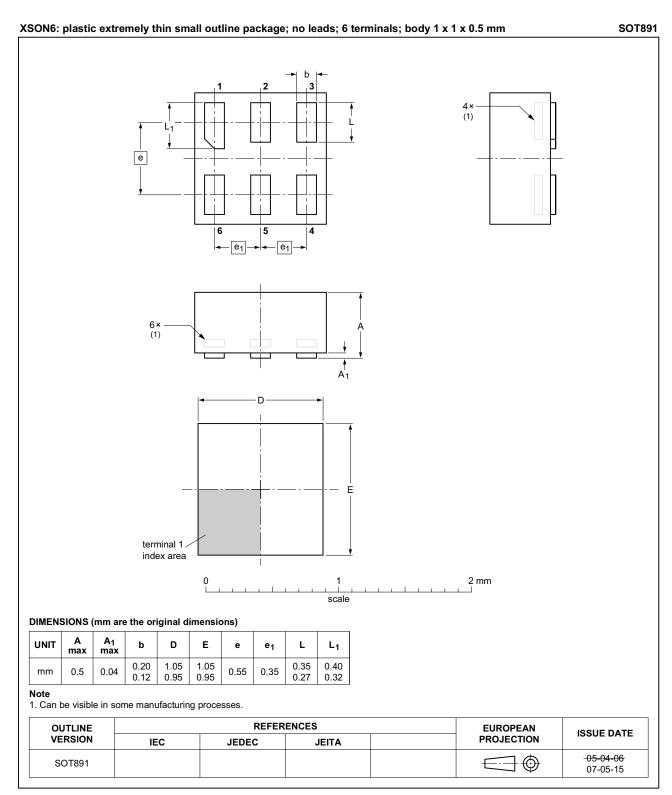


Fig 12. Package outline SOT891 (XSON6)

**Product data sheet** 

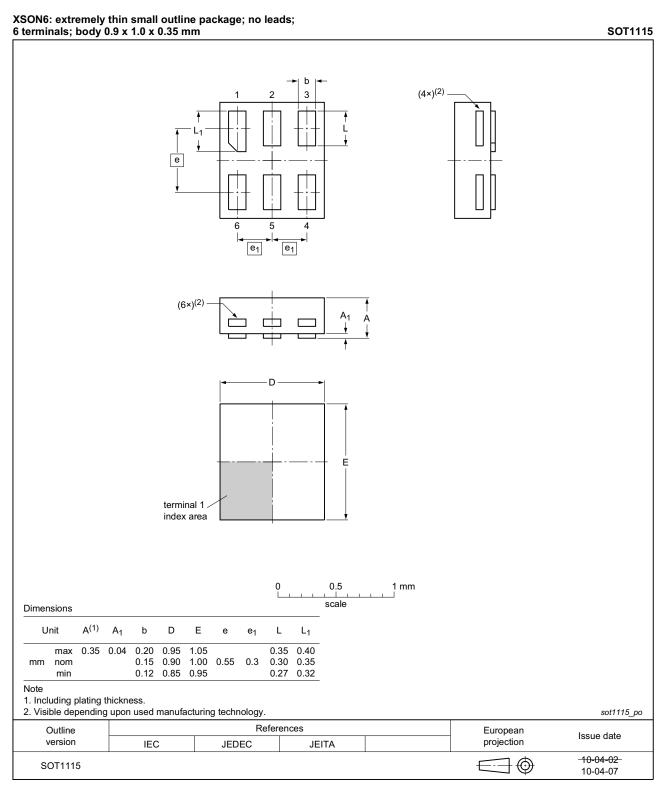


Fig 13. Package outline SOT1115 (XSON6)

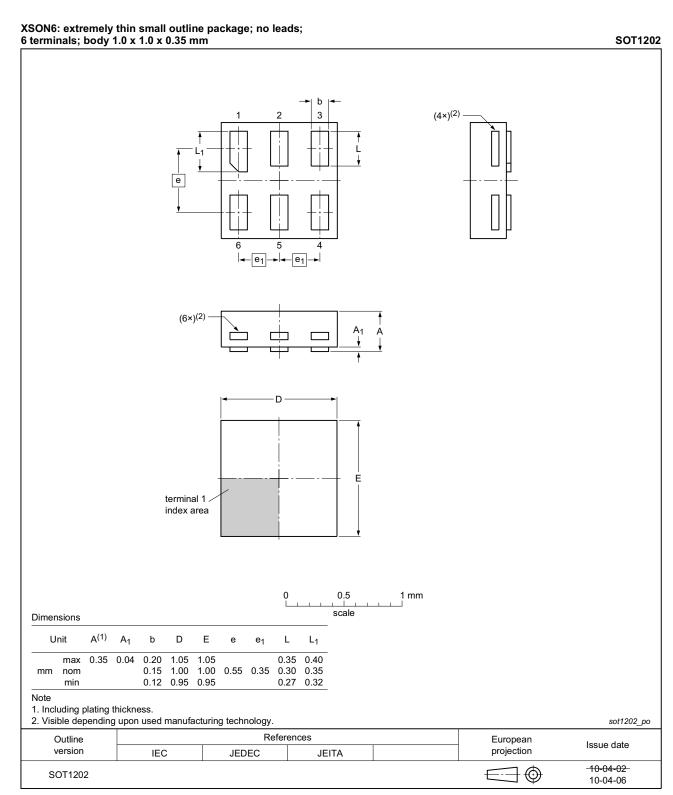


Fig 14. Package outline SOT1202 (XSON6)

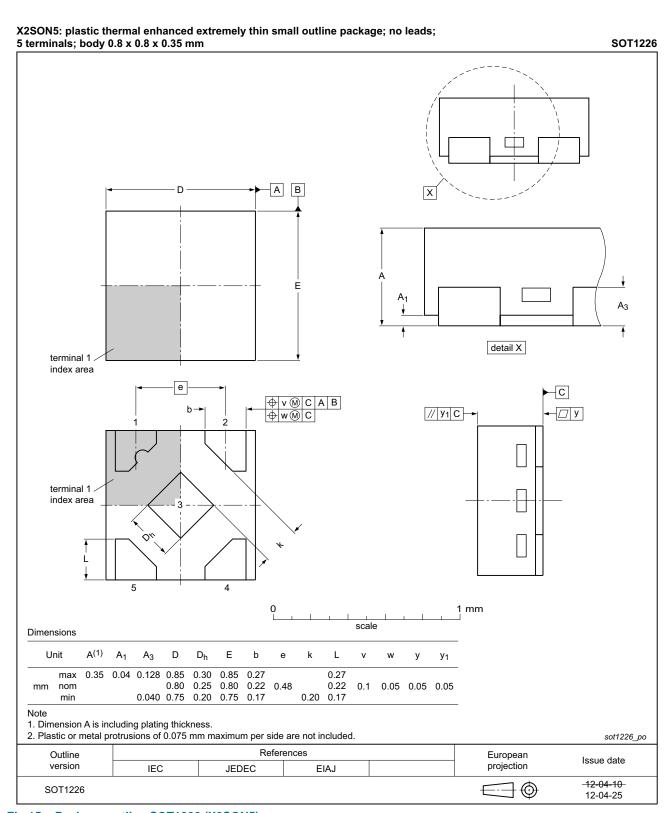


Fig 15. Package outline SOT1226 (X2SON5)

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Low-power 2-input NOR gate

# 14. Abbreviations

#### Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

# 15. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G02 v.7	20150121	Product data sheet	-	74AUP1G02 v.6
Modifications:	X2SON5 add	ded to pin description table (T	able 3).	
74AUP1G02 v.6	20120627	Product data sheet	-	74AUP1G02 v.5
Modifications:	<ul> <li>Added type r</li> </ul>	number 74AUP1G02GX (SO	Γ1226).	
74AUP1G02 v.5	20120216	Product data sheet	-	74AUP1G02 v.4
Modifications:	Logic diagrar	m (Figure 3) modified.		
	Package out	line drawing of SOT886 (Figu	re 11) modified.	
74AUP1G02 v.4	20111115	Product data sheet	-	74AUP1G02 v.3
Modifications:	Legal pages	updated.		
74AUP1G02 v.3	20101012	Product data sheet	-	74AUP1G02 v.2
74AUP1G02 v.2	20060628	Product data sheet	-	74AUP1G02 v.1
74AUP1G02 v.1	20050718	Product data sheet	-	-

# 16. Legal information

#### 16.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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#### Low-power 2-input NOR gate

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#### Low-power 2-input NOR gate

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