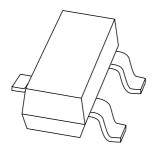
DISCRETE SEMICONDUCTORS

DATA SHEET



PMBTA13; PMBTA14 NPN Darlington transistors

Product data sheet Supersedes data of 1999 Apr 29 2004 Jan 22



NPN Darlington transistors

PMBTA13; PMBTA14

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 30 V)
- High DC current gain (min. 10000).

APPLICATIONS

• High input impedance preamplifiers.

DESCRIPTION

NPN Darlington transistor in a SOT23 plastic package. PNP complement: PMBTA64.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
PMBTA13	*1M
PMBTA14	*1N

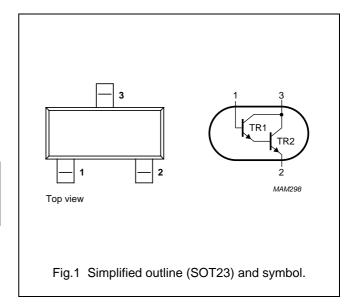
Note

* = p : Made in Hong Kong.
 * = t : Made in Malaysia.

* = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE		PACKAGE					
NUMBER	NAME	DESCRIPTION	VERSION				
PMBTA13	_	plastic surface mounted package; 3 leads	SOT23				
PMBTA14							

NPN Darlington transistors

PMBTA13; PMBTA14

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	_	30	V
V _{CES}	collector-emitter voltage	V _{BE} = 0	_	30	V
V _{EBO}	emitter-base voltage	open collector	_	10	V
Ic	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	800	mA
I _B	base current (DC)		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

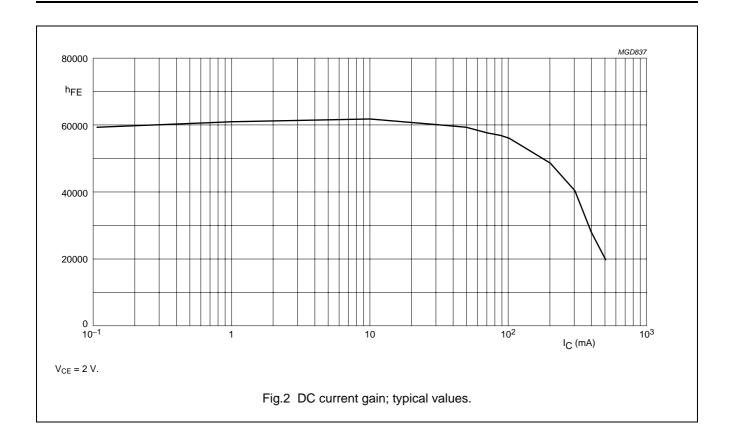
CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	100	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 10 V	_	100	nA
h _{FE}	DC current gain	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; \text{ (see Fig.2)}$			
	PMBTA13		5000	_	
	PMBTA14		10000	_	
	DC current gain	I _C = 100 mA; V _{CE} = 5 V; (see Fig.2)			
	PMBTA13		10000	_	
	PMBTA14		20000	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 0.1 mA	_	1.5	V
V_{BEon}	base-emitter on-state voltage	$I_C = 100 \text{ mA}; V_{CE} = 5 \text{ V}$	_	1.4	V
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	125	_	MHz

NPN Darlington transistors

PMBTA13; PMBTA14

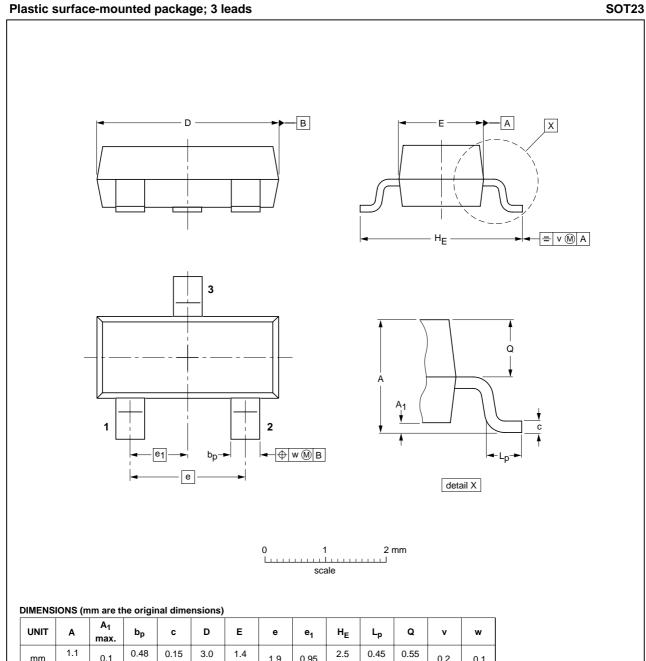


NPN Darlington transistors

PMBTA13; PMBTA14

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads



mm 1.1 0.9 0.1 0.48 0.15 3.0 1.4 1.9 0.95 2.5 0.45 0.55 0.2 0.1	UNIT	А	max.	Dp	· ·	ט	_	e	e ₁	"E	Ľp	Q	V	w
	mm		0.1				12	1.9	0.95	21		l	0.2	0.1

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

2004 Jan 22 5

NPN Darlington transistors

PMBTA13; PMBTA14

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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