Single Schmitt trigger inverter Rev. 3 — 8 December 2016

Product data sheet

General description 1.

The 74LVC1G14-Q100 provides the inverting buffer function with Schmitt trigger input. It is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

The input can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment. Schmitt trigger action at the input makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

Features and benefits 2.

- Automotive product qualification in accordance with AEC-Q100 (Grade 1) Specified from –40 °C to +85 °C and from –40 °C to +125 °C
- Wide supply voltage range from 1.65 V to 5.5 V
- High noise immunity
- Complies with JEDEC standard:
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8-B/JESD36 (2.7 V to 3.6 V).
- ± 24 mA output drive (V_{CC} = 3.0 V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Unlimited rise and fall times
- Input accepts voltages up to 5 V
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pf, R = 0 Ω)



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3. Applications

- Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator

4. Ordering information

Table 1.Ordering information

Type number	Package	Package							
	Temperature range Name Description Vertice								
74LVC1G14GW-Q100	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1					
74LVC1G14GV-Q100	–40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753					

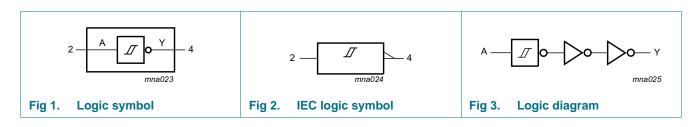
5. Marking

Table 2. Marking

Type number	Marking code ^[1]
74LVC1G14GW-Q100	VF
74LVC1G14GV-Q100	V14

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

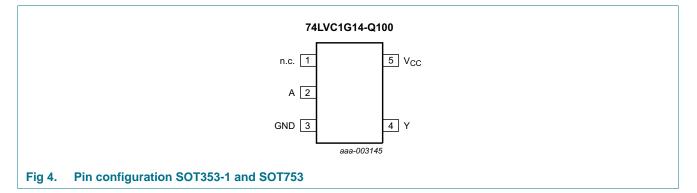
6. Functional diagram



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7. Pinning information

7.1 Pinning



7.2 Pin description

Fable 3. Pin description							
Symbol	Pin	Description					
n.c.	1	not connected					
A	2	data input					
GND	3	ground (0 V)					
Y	4	data output					
V _{CC}	5	supply voltage					

8. Functional description

Table 4. Function table^[1]

Input	Output
Α	Y
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level

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9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+6.5	V
VI	input voltage		<u>[1]</u>	-0.5	+6.5	V
Vo	output voltage	Active mode	<u>[1][2]</u>	-0.5	V _{CC} + 0.5	V
		Power-down mode	<u>[1][2]</u>	-0.5	+6.5	V
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
I _{ОК}	output clamping current	$V_{\rm O}$ > $V_{\rm CC}$ or $V_{\rm O}$ < 0 V		-	±50	mA
I _O	output current	$V_{O} = 0 V$ to V_{CC}		-	±50	mA
I _{CC}	supply current			-	+100	mA
I _{GND}	ground current			-100	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \text{ to } +125 \ ^{\circ}C$	<u>[3]</u>	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When $V_{CC} = 0 V$ (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP5 and SC-74A packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V _{CC}	V
		Power-down mode; $V_{CC} = 0 V$	0	-	5.5	V
T _{amb}	ambient temperature		-40	-	+125	°C

11. Static characteristics

Table 7.Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	–40 ℃ to +85 ℃		°C	–40 °C to	+125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
V _{OH}	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}$						
	output voltage	$I_{O} = -100 \ \mu\text{A};$ $V_{CC} = 1.65 \ \text{V} \text{ to } 5.5 \ \text{V}$	V _{CC} - 0.1	-	-	V _{CC} – 0.1	-	V
		$I_{O} = -4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.2	1.54	-	0.95	-	V
		$I_{O} = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	2.15	-	1.7	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	2.2	2.50	-	1.9	-	V
		$I_{O} = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.3	2.62	-	2.0	-	V
		$I_{O} = -32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.8	4.11	-	3.4	-	V

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Symbol	Parameter	Conditions	-40	°C to +85	°C	–40 °C to	o +125 ℃	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
V _{OL}	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}$						
	output voltage	$I_{O} = 100 \ \mu A;$ $V_{CC} = 1.65 \ V \ to \ 5.5 \ V$	-	-	0.10	-	0.10	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	0.07	0.45	-	0.70	V
		$I_0 = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	0.12	0.30	-	0.45	V
		$I_0 = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	-	0.17	0.40	-	0.60	V
		$I_{O} = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	0.33	0.55	-	0.80	V
		$I_{O} = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	0.39	0.55	-	0.80	V
lı	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	±0.1	±1	-	±1	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 5.5 V; V_{CC} = 0 V	-	±0.1	±2	-	±2	μA
I _{CC}	supply current	$V_{I} = 5.5 V \text{ or GND}; I_{O} = 0 A;$ $V_{CC} = 1.65 V \text{ to } 5.5 V$	-	0.1	4	-	4	μA
Δl _{CC}	additional supply current		-	5	500	-	500	μA
CI	input capacitance	V_{CC} = 3.3 V; V_{I} = GND to V_{CC}	-	5.0	-	-	-	pF

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

[1] All typical values are measured at maximum V_{CC} and T_{amb} = 25 °C.

Table 8. Transfer characteristics

Voltages are referenced to GND (ground = 0 V); for load circuit see <u>Figure 6</u>.

Symbol	Parameter	Conditions	-40	°C to +85	5 °C	–40 °C to	o +125 ℃	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
V _{T+}	positive-going	see Figure 7 and Figure 8						
	threshold voltage	V _{CC} = 1.8 V	0.82	1.0	1.14	0.79	1.14	V
		V _{CC} = 2.3 V	1.03	1.2	1.40	1.00	1.40	V
		V _{CC} = 3.0 V	1.29	1.5	1.71	1.26	1.71	V
		V _{CC} = 4.5 V	1.84	2.1	2.36	1.81	2.36	V
		V _{CC} = 5.5 V	2.19	2.5	2.79	2.16	2.79	V
V _{T-}	negative-going	see Figure 7 and Figure 8						
	threshold voltage	V _{CC} = 1.8 V	0.46	0.6	0.75	0.46	0.78	V
		V _{CC} = 2.3 V	0.65	0.8	0.96	0.65	0.99	V
		V _{CC} = 3.0 V	0.88	1.0	1.24	0.88	1.27	V
		V _{CC} = 4.5 V	1.32	1.5	1.84	1.32	1.87	V
		V _{CC} = 5.5 V	1.58	1.8	2.24	1.58	2.27	V

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Symbol	Parameter	Conditions	-40	°C to +85	5 °C	–40 °C to	o +125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
V _H	hysteresis voltage	$(V_{T+} - V_{T-})$; see Figure 7, Figure 8 and Figure 9						
		V _{CC} = 1.8 V	0.26	0.4	0.51	0.19	0.51	V
		$V_{CC} = 2.3 V$	0.28	0.4	0.57	0.22	0.57	V
		V _{CC} = 3.0 V	0.31	0.5	0.64	0.25	0.64	V
		$V_{CC} = 4.5 V$	0.40	0.6	0.77	0.34	0.77	V
1		V _{CC} = 5.5 V	0.47	0.6	0.88	0.41	0.88	V

Table 8. Transfer characteristics ... continued

[1] All typical values are measured at $T_{amb} = 25 \ ^{\circ}C$

12. Dynamic characteristics

Table 9. **Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for load circuit see Figure 6.

Symbol	Parameter	Conditions	-40	°C to +8	5 °C	–40 °C to	• +125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
t _{pd}	propagation delay	A to Y; see Figure 5						
		V _{CC} = 1.65 V to 1.95 V	1.0	4.1	11.0	1.0	14.0	ns
		V_{CC} = 2.3 V to 2.7 V	0.7	2.8	6.5	0.7	8.5	ns
		V _{CC} = 2.7 V	0.7	3.2	6.5	0.7	8.5	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	0.7	3.0	5.5	0.7	7.0	ns
		V_{CC} = 4.5 V to 5.5 V	0.7	2.2	5.0	0.7	6.5	ns
C _{PD}	power dissipation capacitance	$V_{CC} = 3.3 \text{ V}; \text{ V}_{I} = \text{GND to V}_{CC}$ [3]	-	15.4	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} + (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V.

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13. Waveforms

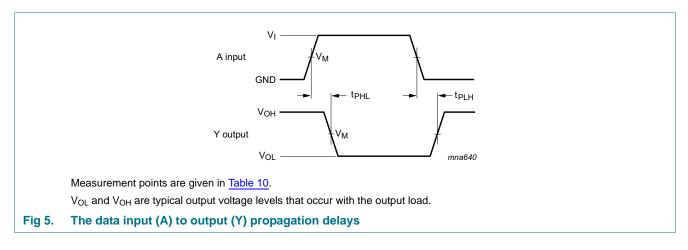
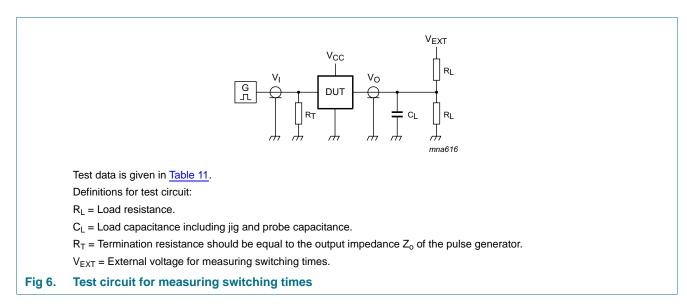


Table 10.Measurement points

Supply voltage	Input	Output
V _{cc}	V _M	V _M
1.65 V to 1.95 V	0.5V _{CC}	0.5V _{CC}
2.3 V to 2.7 V	0.5V _{CC}	0.5V _{CC}
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	0.5V _{CC}	0.5V _{CC}



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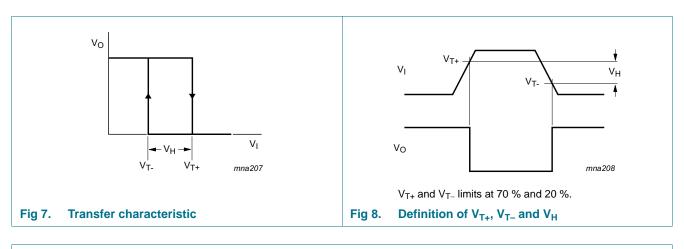
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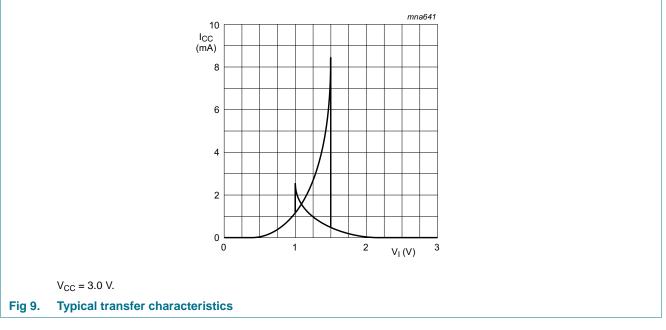
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Tab	le 1	1.	Test	data

Supply voltage	Input	Input		Load	
V _{cc}	VI	$t_r = t_f$	CL	RL	t _{PLH} , t _{PHL}
1.65 V to 1.95 V	V _{CC}	≤ 2.0 ns	30 pF	1 kΩ	open
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	30 pF	500 Ω	open
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	50 pF	500 Ω	open

14. Waveforms transfer characteristics





15. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$ where:

 P_{add} = additional power dissipation (μ W);

 $f_i = input frequency (MHz);$

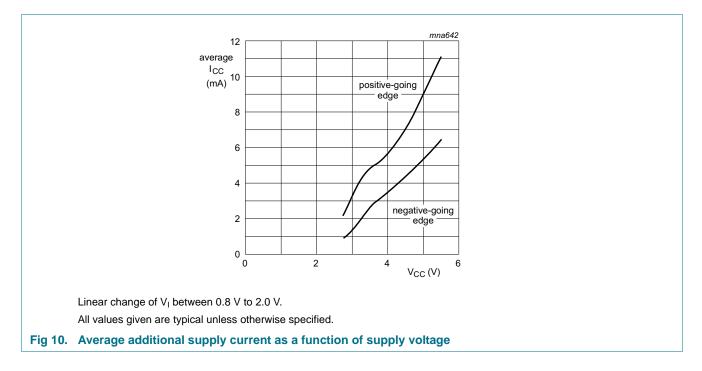
 t_r = input rise time (ns); 10 % to 90 %;

 t_f = input fall time (ns); 90 % to 10 %;

 $\Delta I_{CC(AV)}$ = average additional supply current (µA).

Average $\Delta I_{CC(AV)}$ differs with positive or negative input transitions, as shown in Figure 10.

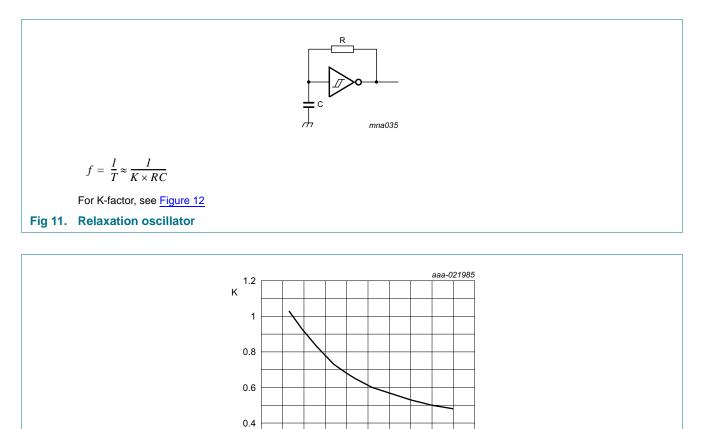
An example of a relaxation circuit using the 74LVC1G14-Q100 is shown in Figure 11.

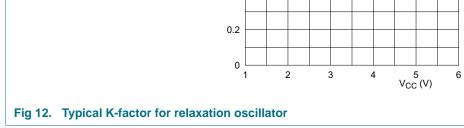


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16. Package outline

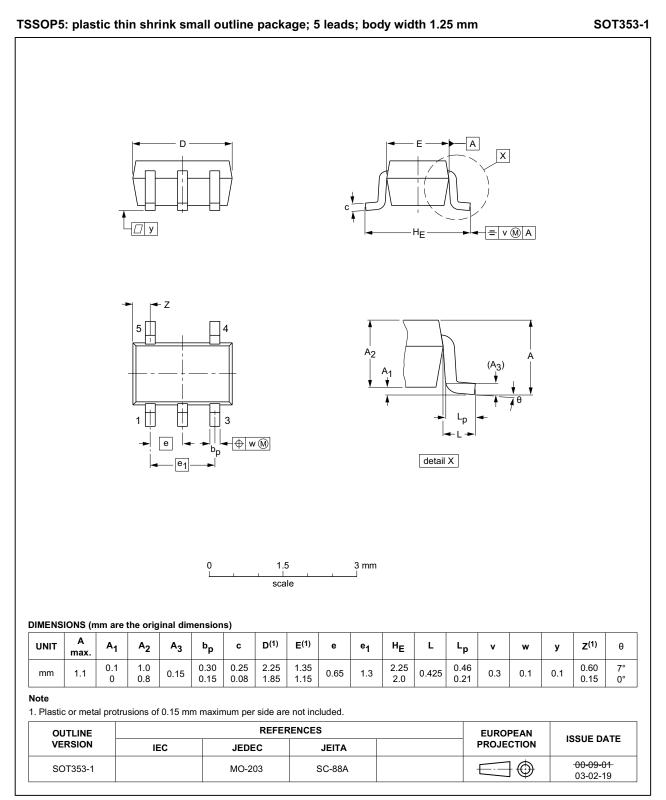


Fig 13. Package outline SOT353-1 (TSSOP5)

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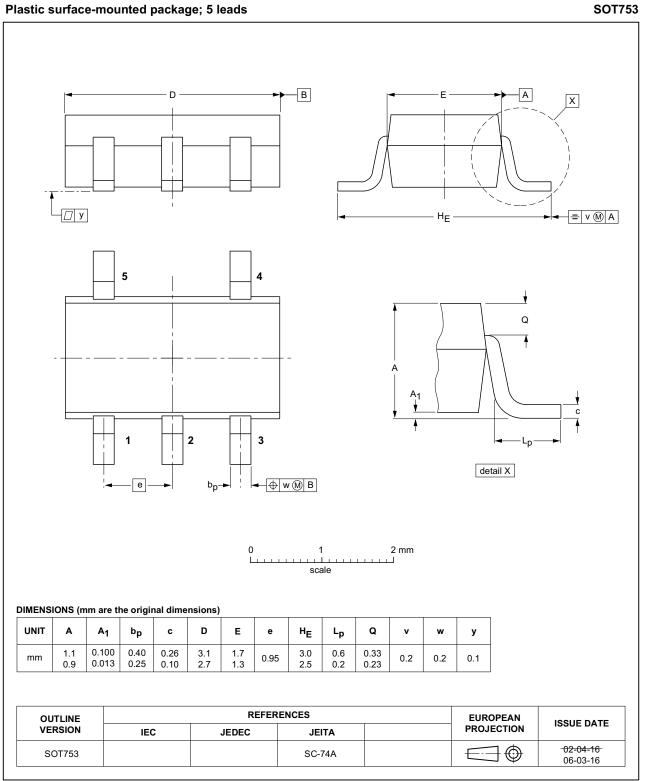


Fig 14. Package outline SOT753 (SC-74A)

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17. Abbreviations

Table 12. Abbreviations		
Acronym	Description	
CMOS	Complementary Metal Oxide Semiconductor	
TTL	Transistor-Transistor Logic	
НВМ	Human Body Model	
ESD	ElectroStatic Discharge	
MM	Machine Model	
DUT	Device Under Test	
MIL	Military	

18. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LVC1G14_Q100 v.3	20161208	Product data sheet	-	74LVC1G14_Q100 v.2
Modifications:	• <u>Table 7</u> : The r	maximum limits for leakage cui	rrent and supply cur	rent have changed.
74LVC1G14_Q100 v.2	20160315	Product data sheet	-	74LVC1G14_Q100 v.1
Modifications:	• Figure 12 add	led (typical K-factor for relaxati	on oscillator).	
74LVC1G14_Q100 v.1	20120709	Product data sheet	-	-

19. Legal information

19.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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