

Transistors with Built-in Resistor DRC2114E0L

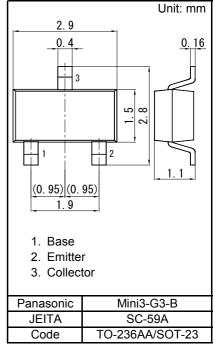
DRC2114E0L Silicon NPN epitaxial planar type

For digital circuits Complementary to DRA2114E

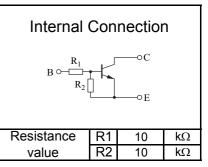
Features

- Low collector-emitter saturation voltage Vce(sat)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: NB
- Packaging

Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)



Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	100	mA
Total power dissipation	PT	200	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	С°



Electrical Characteristics	Ta = 25 °C ± 3 °C
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■ Absolute Maximum Ratings Ta = 25 °C

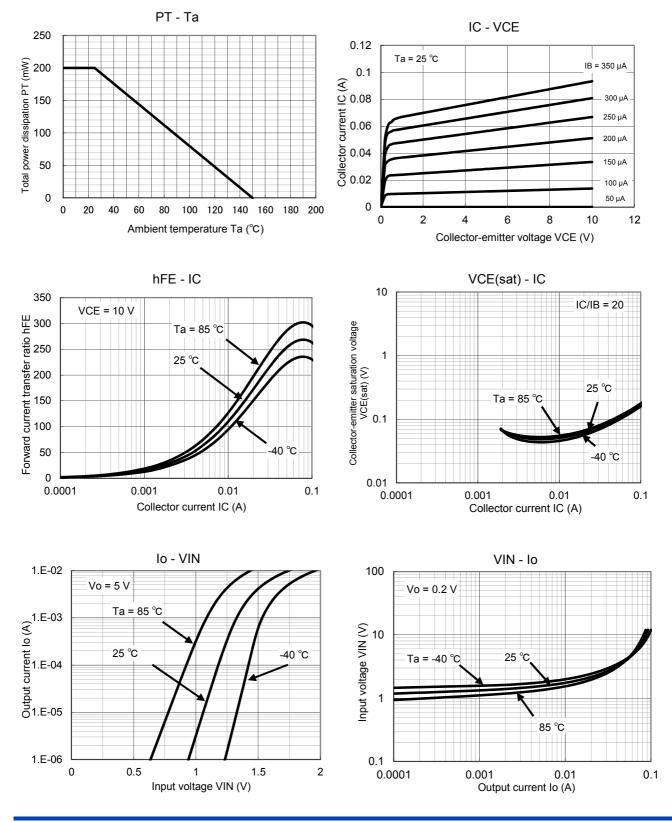
Conditions Min , IE = 0 50 IB = 0 50 /, IE = 0 ////////////////////////////////////	Тур	Max 0.1	Unit V V
IB = 0 50 /, IE = 0		0.1	V V
/, IE = 0		0.1	V
2		0.1	۸
/. IB = 0			μA
, -		0.5	μA
IC = 0		0.5	mΑ
/, IC = 5 mA 35			-
, IB = 0.5 mA		0.25	V
V, IC = 5 mA 2.1			V
, IC = 100 μA		0.8	V
200/	10	+30%	kΩ
-30%	10	1.2	-
	V, IC = 5 mA 2.1 γ, IC = 100 μA -30%	V, IC = 5 mA 2.1 , IC = 100 μA 2.1	V, IC = 5 mA 2.1 γ, IC = 100 μA 0.8 -30% 10 +30%

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

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Technical Data (reference)



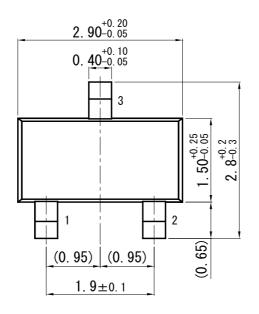
Established : 2009-10-21 Revised : 2014-03-13

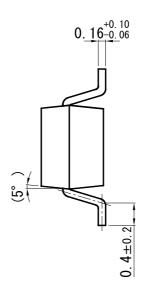


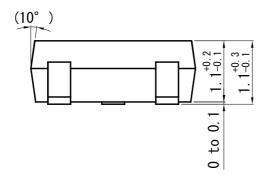
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Mini3-G3-B

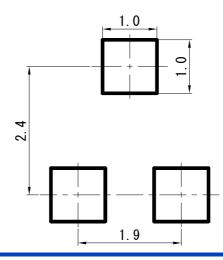
Unit: mm







Land Pattern (Reference) (Unit: mm)



Established : 2009-10-21 Revised : 2014-03-13

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