

Evaluation board for SPEAr310 embedded MPU

Data brief

Features

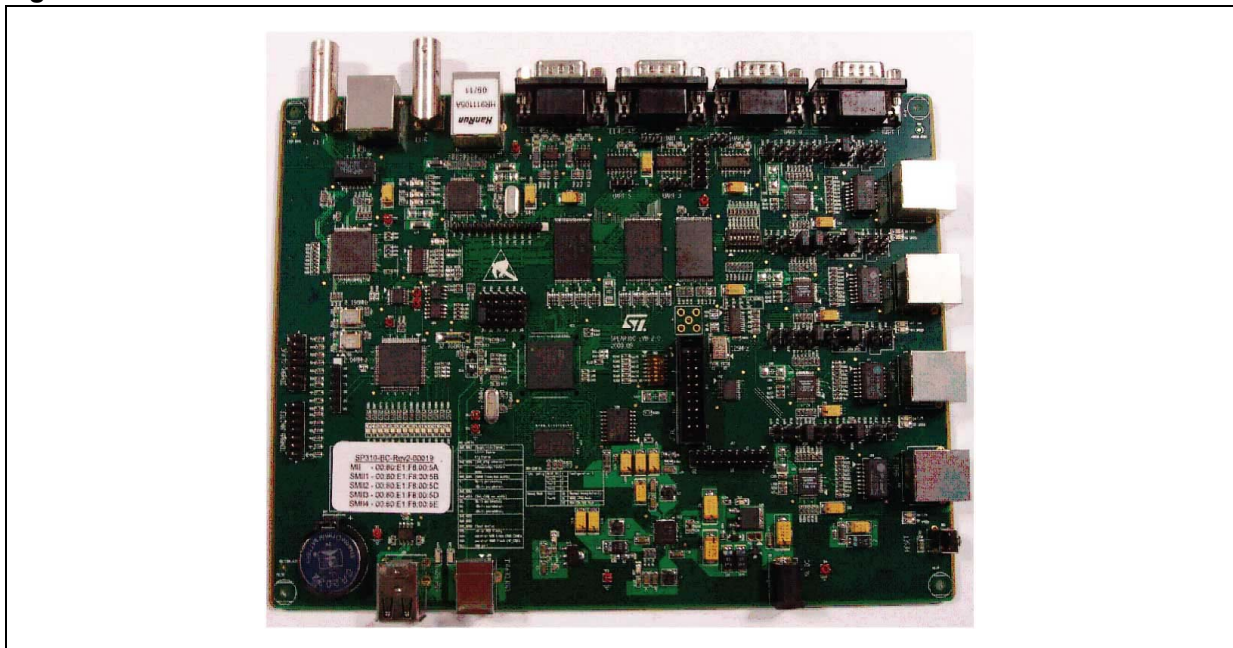
- SPEAr310 embedded MPU
- Up to 2 Gbits DDR 2333 MHz (std 128 Mbytes)
- Up to 1.5 Gbytes parallel NOR Flash (std 16 Mbytes)
- Up to 16 Mbytes serial NOR Flash memory (std 64 Mbytes)
- Up to 2 Gbits NAND Flash memory (std 64 Mbytes)
- 4 Kb Serial I²C EEPROM
- 4 Mb SPI Flash memory
- Two USB 2.0 full Host port channels
- One USB 2.0 high speed Device
- One 10/100 Ethernet port based on MII PHY
- Four 10/100 Ethernet port based on SMII PHY
- One E1, 2x BNC/RJ48(2.048 Mbits)
- Two HDLC RS485 DB9 ports (up to 4 MHz)

- One HDLC-TDM port (8 Mb, 128 TS)
- Six serial ports (up to 115 kbaud)
- 8 ADC channels (10-bit, 1 Msamples)
- 6 GPIOs
- JTAG debug port

Description

The EVALSPEAr310 evaluation board allows you to quickly evaluate and debug software for the SPEAr310. It acts as a learning tool to rapidly get familiar with the SPEAr310 features and provides a starting point for the development of the final application board. It is equipped with most of the interfaces offered by the SPEAr310. The board is equipped with one MII Ethernet port, four SMII Ethernet ports, six RS232 ports, two HDLC based RS485 ports and one E1/TDM port. Memory devices included are NAND Flash, parallel NOR Flash, serial NOR Flash, and I²C EEPROM.

Figure 1. EVALSPEAr310 evaluation board



Revision history

Table 1. Document revision history

Date	Revision	Changes
20-Sep-2010	1	Initial release.

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