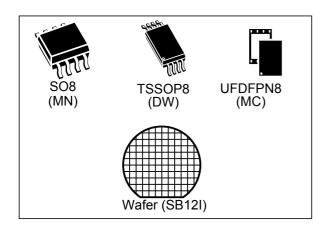


# Dynamic NFC/RFID tag IC with 2-Kbit EEPROM, NFC Forum Type 4 Tag and I<sup>2</sup>C interface

Data brief



### **Features**

## I<sup>2</sup>C interface

- Two-wire I<sup>2</sup>C serial interface supports 1 MHz protocol
- Single supply voltage: 2.7 V to 5.5 V

#### **Contactless interface**

- NFC Forum Type 4 Tag
- ISO/IEC 14443 Type A
- 106 Kbps data rate
- Internal tuning capacitance: 25 pF

### **Memory**

- 256-byte (2-kbit) EEPROM
- · Support of NDEF data structure
- Data retention: 200 years
- Endurance: 1 million erase-write cycles
- Read up to 246 bytes in a single command
- Write up to 246 bytes in a single command
- 7 bytes unique identifier (UID)
- 128 bits passwords protection

### **Package**

- 8-lead small-outline package (SO8) ECOPACK<sup>®</sup>2
- TSSOP8 ECOPACK<sup>®</sup>2
- UFDFPN8 ECOPACK<sup>®</sup>2

### Digital pad

- GPO: configurable General Purpose Output
- RF disable: activation/deactivation of RF commands

# **Description**

The M24SR02-Y device is a dynamic NFC/RFID tag IC with a dual interface. It embeds an EEPROM memory. It can be operated from an I<sup>2</sup>C interface or by a 13.56 MHz RFID reader or an NFC phone.

The I<sup>2</sup>C interface uses a two-wire serial interface, consisting of a bidirectional data line and a clock line. It behaves as a slave in the I<sup>2</sup>C protocol.

The RF protocol is compatible with ISO/IEC 14443 Type A and NFC Forum Type 4 Tag.

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# 1 Functional description

The M24SR02-Y device is a dynamic NFC/RFID tag that can be accessed either from the I<sup>2</sup>C or the RF interface. The RF and I<sup>2</sup>C host can read or write to the same memory, that is why only one host can communicate at a time with the M24SR02-Y. The management of the interface selection is controlled by the M24SR02-Y device itself.

The RF interface is based on the ISO/IEC 14443 Type A standard. The M24SR02-Y is compatible with the NFC Forum Type 4 Tag specifications and supports all corresponding commands.

The I<sup>2</sup>C interface uses a two-wire serial interface consisting of a bidirectional data line and a clock line. The devices carry a built-in 4-bit device type identifier code in accordance with the I<sup>2</sup>C bus definition.

The device behaves as a slave in the I<sup>2</sup>C protocol.

Figure 1 displays the block diagram of the M24SR02-Y device.

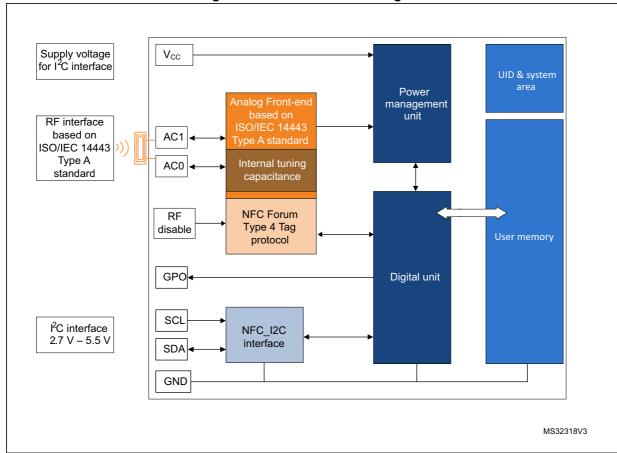


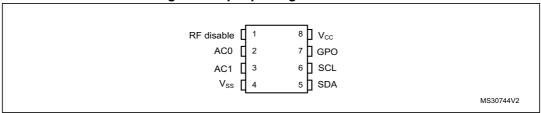
Figure 1. M24SR02-Y block diagram

Signal name	Function	Direction
SDA	Serial data	I/O
SCL	Serial clock	Input
AC0, AC1	Antenna coils	-
V <sub>CC</sub>	Supply voltage	-
VSS	Ground	-
GPO	Interrupt output (1)	Open drain output
RF disable	Disable the RF communication (2)	Input

Table 1. Signal names

- 1. An external pull-up > 4.7 k $\Omega$  is required.
- 2. An external pull-down is required when the voltage on  $V_{\text{cc}}$  is above its POR level.

Figure 2. 8-pin package connections



1. See Package mechanical data section for package dimensions, and how to identify pin 1.

## 1.1 Functional modes

The M24SR02-Y has two functional modes available. The difference between the modes lies in the power supply source (see *Table 2*).

Table 2. Functional modes

Modes	Supply source	Comments
I <sup>2</sup> C mode	V <sub>cc</sub>	The I <sup>2</sup> C interface is available
Tag mode	RF field only	The I <sup>2</sup> C interface is disconnected
Dual interface mode	RF field or V <sub>cc</sub>	Both I <sup>2</sup> C and RF interfaces are available

## 1.1.1 I<sup>2</sup>C mode

M24SR02-Y is powered by  $V_{CC}$ . The I<sup>2</sup>C interface is connected to the M24SR02-Y. The I<sup>2</sup>C host can communicate with the M24SR02-Y device.

## 1.1.2 Tag mode

The M24SR02-Y is supplied by the RF field and can communicate with an RF host (RFID reader or an NFC phone). The User memory can only be accessed by the RF commands.

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## 1.1.3 Dual interface mode

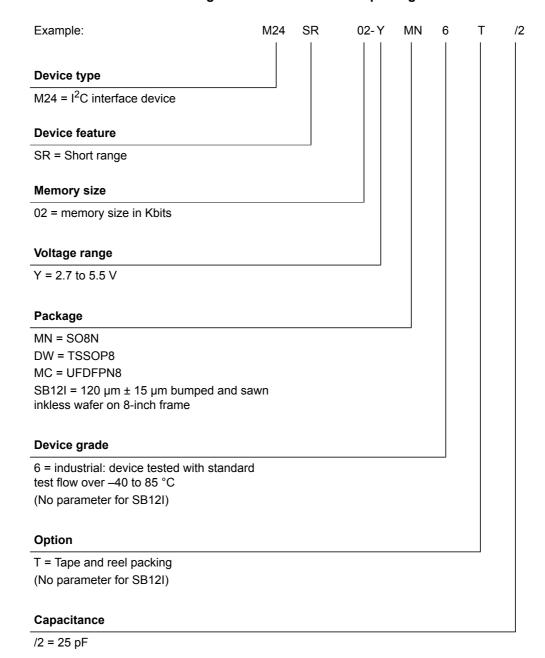
Both interfaces, RF and  $I^2C$ , are connected to the M24SR02-Y and both RF or  $I^2C$  host can communicate with the M24SR02-Y device. The power supply and the access management are carried out by the M24SR02-Y itself. For further details, please refer to the token mechanism chapter.

Part numbering M24SR02-Y

# 2 Part numbering

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Table 3. Ordering information scheme for packaged devices



M24SR02-Y Revision history

# 3 Revision history

Table 4. Document revision history

Date	Revision	Changes
24-Jan-2014	1	Initial release.

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