SiT9102

LVPECL / HCSL / LVDS / CML 1 to 220 MHz High Performance Oscillator

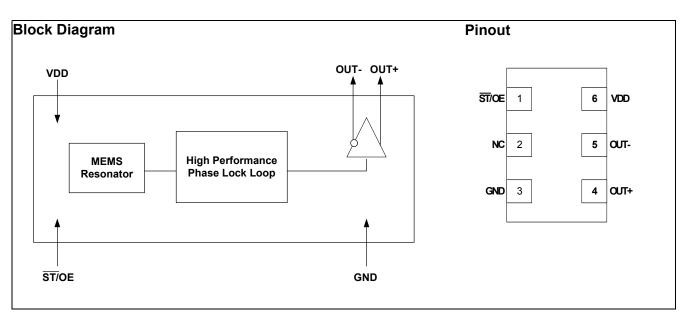
Features

- · Extremely low RMS phase jitter (random)
 - <1 ps (typical)</p>
- Wide frequency range
 - 1 MHz to 220 MHz
 - 220 MHz to 800 MHz refer to SiT9107
- · High frequency stability
 - ±10 PPM, ±15 PPM, ±20 PPM
 - ±25 PPM, ±50 PPM
- · Operating voltage
 - 1.8, 2.5 or 3.3 V
 - Other voltages up to 3.63 V (contact SiTime)
- Operating temperature range
 - Industrial, -40 to 85 °C
 - Extended Commercial, -20 to 70 °C
 - Commercial, 0 to 70 °C
- · Small footprint
 - 7.0 x 5.0 x 0.90 mm
- · Pb-free and RoHS compliant
- · For Spread Spectrum see SiT9002
- · Ultra-reliable start up and greater immunity from inter ference

- **Benefits**
- · Ultra fast lead time: 2 to 3 weeks
- · No crystal or capacitors required
- · Eliminates crystal qualification time
- 50% + board saving space
- · More cost effective than quartz oscillators, quartz crystals and clock ICs.
- · Completely quartz-free

Applications

- · Server
- Router
- RAID controller
- · Gigabit Ethernet
- 10 Gigabit Ethernet
- · Fiber Channel
- · SATA / SAS
- · PCI-Express
- Fully Buffered DIMM
- · System clock
- · Networking and computing



• 5.0 x 3.2 x 0.75 mm



Pin Description

Pin No.	Name		Pin Description
1	ST/OE	Input	Standby or Output Enable pin for OUT+ and OUT OE: When High or Open : OUT+ and OUT- = active When Low : OUT+ and OUT- = High Impedance state ST: When High or Open : OUT+ and OUT- = active When Low : OUT+ and OUT- = Output is low (weak pull down), oscillation stops
2	NC	NA	Do Not connect pin, leave it floating.
3	GND	Power	VDD power supply ground. Connect to Ground
4	OUT+	Output	1 to 220 MHz programmable clock output .
5	OUT-	Output	
6	VDD	Power	Power supply

Absolute Maximum Ratings

Attempted operation outside the absolute maximum ratings of the part may cause permanent damage to the part. Actual performance of the IC is only guaranteed within the operational specifications, not at absolute maximum ratings.

Absolute Maximum Table

Parameter	Min.	Max.	Unit
Storage Temperature	-65	150	°C
VDD	-0.5	4	V
Vin	GND - 0.5	VDD + 0.5	V
Theta JA (with copper plane on VDD and GND) 5.0 x 3.2 package	-	68	°C/W
7.0 x 5.0 package when center pad is soldered down 7.0 x 5.0 package when center pad is not soldered down	-	38	°C/W
7.0 x 0.0 package when center pauls not soldered down	_	90	°C/W
Theta JC (with PCB traces of 0.010 inch to all pins) 5.0 x 3.2 package	-	45	°C/W
7.0 x 5.0 package when center pad is soldered down 7.0 x 5.0 package when center pad is not soldered down	-	35	°C/W
7.0 x 0.0 package when center pauls not soldered down	_	48	°C/W
Soldering Temperature (follow standard Pb free soldering guidelines)	-	260	°C
Number of Program Writes	-	1	NA
Program Retention over -40 to 125C, Process, VDD (0 to 3.6V)	_	1,000+	years
Human Body Model (JESD22-A114)	2000	-	_
Charged Device Model (JESD22-C101)	750	-	_
Machine Model (JESD22-A115)	200	_	-

Environmental Compliance

Parameter	Condition/Test Method
Mechanical Shock	MIL-STD-883F, Method 2002
Mechanical Vibration	MIL-STD-883F, Method 2007
Temperature Cycle	MIL-STD-883F, Method 1010-65-150°C (1000 cycle)
Solderability	MIL-STD-883F, Method 2003
Moisture Sensitivity Level	MSL1 @ 260°C



DC Electrical Specifications

LVCMOS input, OE or ST pin, 3.3V ±10% or 2.5V ±10% or 1.8V ±5%, -40 to 85°C

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
V _{IH}	Input High Voltage		70	-	-	%Vdd
V _{IL}	Input Low Voltage		-	-	30	%Vdd
I _{IH}	Input High Current	OE or ST pin	-	-	10	μA
IIL	Input Low Current	OE or ST pin	-10	-	-	μA
Т _{ри}	Power Up Time	Time from minimum power supply voltage to the first cycle (Guaranteed no runt pulses)	_	-	10	ms

LVPECL, 3.3V ±10% or 2.5V ±10%, -40 to 85°C

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
V _{DD}	Supply Voltage		2.97	3.3	3.63	V
			2.25	2.5	2.75	V
I _{DD}	Supply Current	V _{DD} = 3.3, Excluding Load Termination Current	-	68	74	mA
		V _{DD} = 2.5, Excluding Load Termination Current	-	65	71	mA
V _{OH}	Output High Voltage	50 Ohm termination to V_{DD} - 2.0V	V _{DD} -1.1	_	V _{DD} -0.7	V
V _{OL}	Output Low Voltage	See Figure 2, 3.	V _{DD} -2.0	_	V _{DD} -1.4	V
V _{swing}	Pk-Pk Output Voltage Swing		600	800	1000	mV

HCSL, 3.3V ±10% or 2.5V ±10%, -40 to 85°C

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
V _{DD}	Supply Voltage		2.97	3.3	3.63	V
			2.25	2.5	2.75	V
I _{DD}	Supply Current	V _{DD} = 3.3, Excluding Load Termination Current	-	65	70	mA
		V _{DD} = 2.5, Excluding Load Termination Current	_	62	67	mA
V _{OH}	Output High Voltage	50 Ohm termination to GND	600	-	950	mV
V _{OL}	Output Low Voltage	See Figure 4.	0.0	-	50	mV
V _{swing}	Pk-Pk Output Voltage Swing		600	-	950	mV

LVDS, $3.3V \pm 10\%$ or $2.5V \pm 10\%$, -40 to $85^{\circ}C$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
V _{DD}	Supply Voltage		2.97	3.3	3.63	V
			2.25	2.5	2.75	V
I _{DD}	Supply Current	V _{DD} = 3.3, Excluding Load Termination Current	_	73	79	mA
		V_{DD} = 3.3, Excluding Load Termination Current V_{DD} = 2.5, Excluding Load Termination Current Swing Mode = Normal Single load termination. See Figure 5. Swing Mode = High Single load termination. See Figure 5. Swing Mode = High Double load termination.	-	70	76	mA
V _{OD1}	Differential Output Voltage	$V_{DD} = 3.3, Excluding Load Termination Current$ $V_{DD} = 2.5, Excluding Load Termination Current$ Swing Mode = Normal Single load termination. See Figure 5. Swing Mode = High Single load termination. See Figure 5. Swing Mode = High	250	350	450	mV
ΔV_{OD1}	V _{OD} Magnitude Change		-	-	50	mV
V _{OS1}	Offset Voltage		-	1.2	_	V
ΔV_{OS1}	V _{OS} Magnitude Change		-	-	50	mV
V _{OD2}	Differential Output Voltage		500	700	900	mV
ΔV_{OD2}	V _{OD} Magnitude Change		-	-	50	mV
V _{OS2}	Offset Voltage		-	1.2	-	V
ΔV_{OS2}	V _{OS} Magnitude Change		_	-	50	mV
V _{OD3}	Differential Output Voltage		250	350	450	mV
ΔV_{OD3}	V _{OD} Magnitude Change		-	-	50	mV
V _{OS3}	Offset Voltage		_	1.2	-	V
ΔV_{OS3}	V _{OS} Magnitude Change		-	-	50	mV



Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
V _{DD}	Supply Voltage			2.97	3.3	3.63	V
				2.25	2.5	2.75	V
				1.71	1.8	1.89	V
I _{DD}	Supply Current	V _{DD} = 3.3V	Excluding Load	-	48	51	mA
		V _{DD} = 2.5V	Termination Current	-	47	50	mA
		V _{DD} = 1.8V		-	38	41	mA
V _{OH1}	Output High Voltage	Swing Mode = Normal		V _{DD} -0.1	-	V _{DD}	V
V _{OL1}	Output Low Voltage	wing Mode = Normal ingle Load Termination ee Figure 7.		V _{DD} -0.55	V _{DD} -0.425	V _{DD} -0.3	V
V _{swing1}	Pk-Pk Output Voltage Swing			300	425	550	mV
V _{OH2}	Output High Voltage	Swing Mode = High		V _{DD} -0.1	-	V _{DD}	V
V _{OL2}	Output Low Voltage	Single Load Termination See Figure 7.		V _{DD} -1.1	V _{DD} -0.85	V _{DD} -0.6	V
V _{swing2}	Pk-Pk Output Voltage Swing			600	850	1100	mV
V _{OH3}	Output High Voltage	Swing Mode = High		V _{DD} -0.1	-	V _{DD}	V
V _{OL3}	Output Low Voltage	Double Load Termination See Figure 8.		V _{DD} -0.55	V _{DD} -0.425	V _{DD} -0.3	V
V _{swing3}	Pk-Pk Output Voltage Swing			300	425	550	mV

CML, 3.3V ±10% or 2.5V ±10% or 1.8V ±5%, -40 to 85°C

AC Electrical Specifications

LVPECL, 3.3V ±10%, -40 to 85°C

Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage change, load	-20 to 70°C	-15	-	+15	PPM
		change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C	•	-	_	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		100	150	300	ps
PH_{J}	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 637	kHz to10 MHz	-	1.6	-	ps
		F _{out} = 156.25 MHz @ BW: 1.875 to 20 MHz		-	0.5	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20	MHz	-	0.7	-	ps
PJ	RMS Period Jitter	F _{out} = 106.25 MHz		-	1.8	2.3	ps
	F _{out} = 156.25 MHz		-	1.3	1.8	ps	
		F _{out} = 200 MHz		_	1.3	1.8	ps



LVPECL, 2.5V ±10%, -40 to 85°C

Symbol	Parameter	Condition	I	Min.	Тур.	Max.	Unit
F _{out}	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
			-50		+50	PPM	
F _{age}	Aging	First year @ 25°C		_	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		100	150	300	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 6	37 kHz to10 MHz	-	1.6	_	ps
		F _{out} = 156.25 MHz @ BW: 1	.875 to 20 MHz	-	0.5	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		-	0.7	-	ps
PJ	RMS Period Jitter	F _{out} = 106.25 MHz		-	1.8	2.3	ps
		F _{out} = 156.25 MHz		-	1.3	1.8	ps
		F _{out} = 200 MHz		-	1.3	1.8	ps

HCSL, 3.3V ±10%, -40 to 85°C

Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	_	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage change, load	-2010700	-15	-	+15	PPM
		change -40 to 85	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		_	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		200	280	375	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 100 MHz @ BW: 1.5 MH	Hz to 22 MHz	-	0.8	_	ps
		F _{out} = 200 MHz @ BW: 1.5 MHz to 22 MHz		_	0.4	_	ps
PJ	RMS Period Jitter	F _{out} = 100 MHz		-	1.6	2.2	ps
	F _{out} = 200 MHz		_	1.5	1.9	ps	



HCSL, 2.5V ±10%, -40 to 85°C

Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
			-50		+50	PPM	
F _{age}	Aging	First year @ 25°C		-	_	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		200	300	400	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 100 MHz @ BW: 1.5 M	/Hz to 22 MHz	-	0.8	-	ps
		F _{out} = 200 MHz @ BW: 1.5 MHz to 22 MHz		-	0.4	-	ps
PJ	RMS Period Jitter	F _{out} = 100 MHz		-	1.6	2.2	ps
		F _{out} = 200 MHz		-	1.5	2.1	ps

LVDS, 3.3V ±10%, -40 to 85°C

Symbol	Parameter	Condition	n	Min.	Тур.	Max.	Unit
Fout	Output Frequency			10	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		-	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		100	200	325	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 6	637 kHz to10 MHz	_	1.7	-	ps
		F _{out} = 156.25 MHz @ BW: 1	1.875 to 20 MHz	-	0.7	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		-	0.7	-	ps
PJ	RMS Period Jitter	F _{out} = 106.25 MHz		_	2.0	2.7	ps
		F _{out} = 156.25 MHz		-	1.8	2.5	ps
		F _{out} = 200 MHz		_	1.8	2.5	ps



LVDS, 2.5V ±10%, -40 to 85°C

Symbol	Parameter	Condition	n	Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	equency Stability Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		-	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		100	260	325	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 637 kHz to10 MHz		-	1.7	-	ps
		F _{out} = 156.25 MHz @ BW: 1	1.875 to 20 MHz	-	0.7	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		_	0.7	-	ps
PJ	P _J RMS Period Jitter F _{out} = 106.25 MHz			-	2.5	3.3	ps
		F _{out} = 156.25 MHz		_	2.4	3.5	ps
		F _{out} = 200 MHz		_	2.4	3.5	ps

CML, 3.3V $\pm 10\%$, -40 to 85°C

Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	_	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		_	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		150	220	300	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 63	37 kHz to10 MHz	-	1.6	-	ps
		F _{out} = 156.25 MHz @ BW: 1.875 to 20 MHz		-	0.6	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		-	0.8	-	ps
PJ	RMS Period Jitter	F _{out} = 106.25 MHz		-	2	2.5	ps
		F _{out} = 156.25 MHz		_	1.9	2.5	ps
		F _{out} = 200 MHz		_	1.9	2.4	ps



CML, 2.5V ± 10%, -40 to 85°C

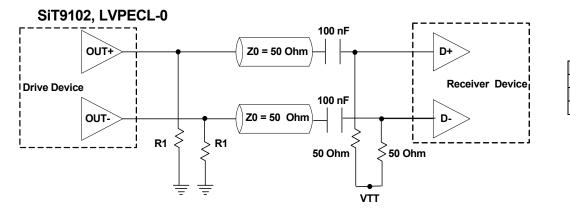
Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-10	-	+10	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-15	-	+15	PPM
		change, load change	-40 to 85°C	-20	-	+20	PPM
				-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		-	-	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		150	230	300	ps
PH_{J}	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 6	37 kHz to10 MHz	-	1.6	-	ps
		F _{out} = 156.25 MHz @ BW: 1	.875 to 20 MHz	-	0.6	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		-	0.8	-	ps
PJ	RMS Period Jitter	F _{out} = 106.25 MHz		-	2.1	2.5	ps
		F _{out} = 156.25 MHz		_	1.9	2.5	ps
		F _{out} = 200 MHz		-	1.9	2.5	ps

CML, 1.8V \pm 5%, -40 to 85°C

Symbol	Parameter	Condition		Min.	Тур.	Max.	Unit
Fout	Output Frequency			1.0	-	220	MHz
F _{stab}	Frequency Stability	Inclusive of initial stability,	0 to 70°C	-15	-	+15	PPM
		operating temp., rated power supply voltage	-20 to 70°C	-20	_	+20	PPM
		change, load change	-40 to 85°C	-25		+25	PPM
				-50		+50	PPM
F _{age}	Aging	First year @ 25°C		_	_	1	PPM
DC	Duty Cycle			45	-	55	%
t _R /t _F	Output Rise/Fall Time	20% to 80%		150	240	325	ps
PH_J	RMS Phase Jitter (random)	F _{out} = 106.25 MHz @ BW: 6	37 kHz to10 MHz	-	1.7	-	ps
		F _{out} = 156.25 MHz @ BW: 1.87 to 20 MHz		-	0.6	-	ps
		F _{out} = 200 MHz @ BW: 1 to 20 MHz		-	0.8	-	ps
PJ	RMS Period Jitter F _{out} = 106.25 MHz			-	2.3	2.9	ps
		F _{out} = 156.25 MHz		_	2.1	2.7	ps
		F _{out} = 200 MHz		_	2.1	2.7	ps



Termination Diagrams



VDD = 3.3V
R1 = 150 Ohm
VDD = 2.5V
R1 = 120 Ohm

Figure 1. LVPECL AC Coupled Typical Termination

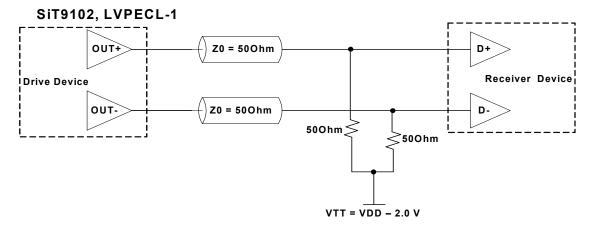
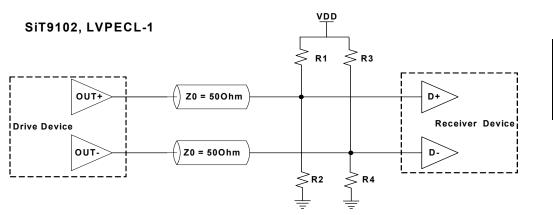


Figure 2. LVPECL DC Coupled Typical Termination with Termination Voltage



VDD = 3.3V
R1 = R3 = 133 Ohm
R2 = R4 = 82 Ohm
VDD = 2.5V
R1 = R3 = 250 Ohm
R2 = R4 = 62.5 Ohm

Figure 3. LVPECL DC Coupled Typical Termination without Termination Voltage

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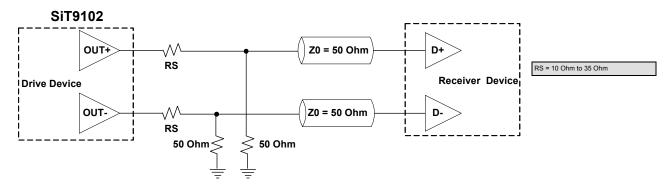


Figure 4. HCSL Typical Termination

Note:

1. All the tests are done with RS = 20 Ohm (recommended).

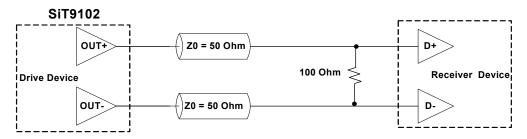
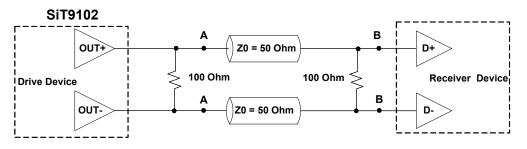


Figure 5. LVDS Single Termination (Load Terminated)



Note: For AC coupled operation, include/insert decoupling caps at points A or B

Figure 6. LVDS Double Termination (Source + Load Terminated)



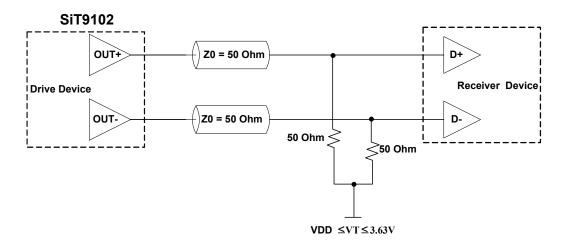
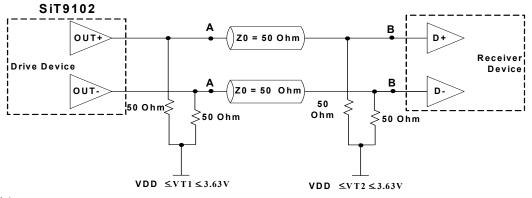


Figure 7. CML Single Load Termination



Notes:

 For DC-coupled operation, VT1 = VT2
 For AC coupled operation, include/insert decoupling caps at points A or B
 For AC-coupled operation with capacitors placed at point A, VT2 sets the input common mode of Receiver Device and need not to be related to VT1

Figure 8. CML Double Load Termination

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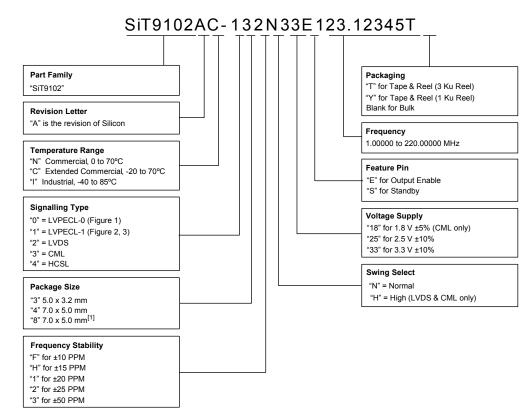
SiT9102

LVPECL / HCSL / LVDS / CML 1 to 220 MHz High Performance Oscillator



Ordering Information

The Part No. Guide is for reference only. For real-time customization and exact part number, use the SiTime Part Number Generator.



Frequency Stability vs. Temperature Range Options

Frequency	Temperature	Su	upply Volta	ge
Stability (PPM)	Range	1.8 V	2.5 V	3.3 V
	N (0 to +70°C)	-	\checkmark	\checkmark
±10	C (-20 to +70°C)	-	-	-
	I (-40 to +85°C)	-	-	-
	N (0 to +70°C)	\checkmark	\checkmark	\checkmark
±15	C (-20 to +70°C)	-	\checkmark	\checkmark
	l (-40 to +85°C)	-	\checkmark	\checkmark
	N (0 to +70°C)	\checkmark	\checkmark	\checkmark
±20	C (-20 to +70°C)	\checkmark	\checkmark	\checkmark
	l (-40 to +85°C)	\checkmark	\checkmark	\checkmark
	N (0 to +70°C)	\checkmark	\checkmark	\checkmark
±25	C (-20 to +70°C)	\checkmark	\checkmark	\checkmark
	l (-40 to +85°C)	\checkmark	\checkmark	\checkmark
	N (0 to +70°C)	\checkmark	\checkmark	\checkmark
±50	C (-20 to +70°C)	\checkmark	\checkmark	\checkmark
	I (-40 to +85°C)	\checkmark	\checkmark	\checkmark

Signaling Type vs. Swing Select Options

Signaling		Supply Voltage				
Туре	Swing Select	1.8 V	1.8 V 2.5 V			
LVPECL-0	Normal	_	\checkmark	\checkmark		
LVPECL-0	High	-	-	-		
	Normal	-	\checkmark	\checkmark		
LVPECL-1	High	-	-	-		
11/120	Normal	-	\checkmark	\checkmark		
LVDS	High	-	\checkmark	\checkmark		
014	Normal	\checkmark	\checkmark	\checkmark		
CML	High	\checkmark	\checkmark	\checkmark		
11001	Normal	-	\checkmark	\checkmark		
HCSL	High	-	-	_		

Note:

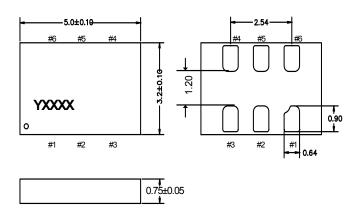
1. Without Center Pad.

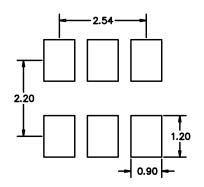


Package Information^[2]

Dimension (mm)

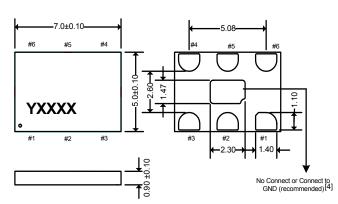
5.0 x 3.2 x 0.75mm

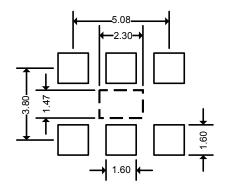




Land Pattern^[3] (recommended) (mm)

7.0 x 5.0 x 0.90mm





Notes:

- "Y" denotes manufacturing origin and "XXXX" denotes manufacturing lot number. The value of "Y" depend on the assembly location of the device.
 A capacitor of value 0.1μF between VDD and GND is recommended.
- 4. The 7050 package with part number designation "-8" has NO center pad.

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